

# GHC(STG,Cmm,asm) illustrated

for hardware persons

*exploring some mental models and implementations*

Takenobu T.

"Any sufficiently advanced technology is  
indistinguishable from magic."

Arthur C. Clarke

## NOTE

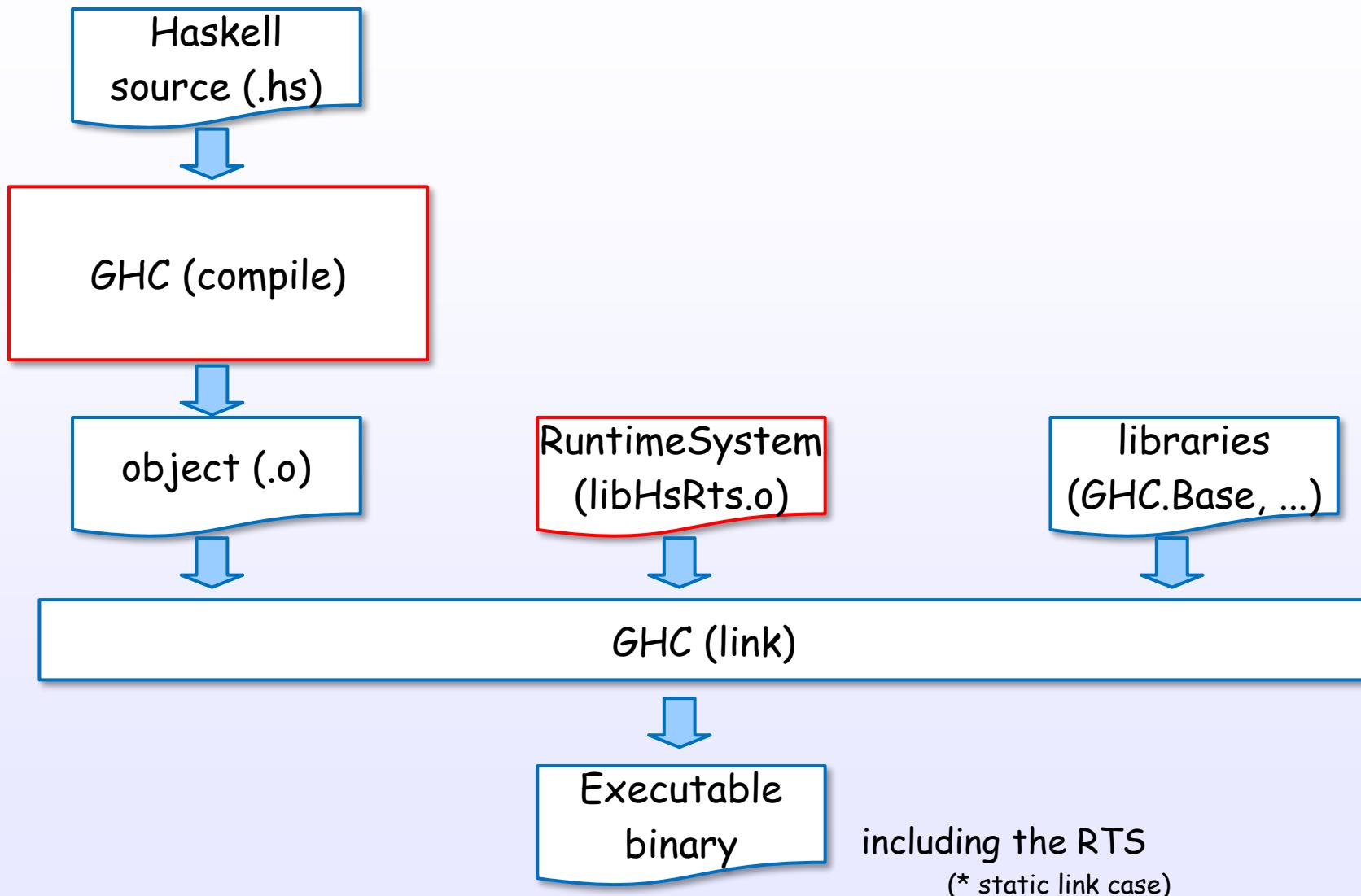
- This is not an official document by the ghc development team.
- Please don't forget "semantics". It's very important.
- This is written for ghc 9.0.

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# Executable binary

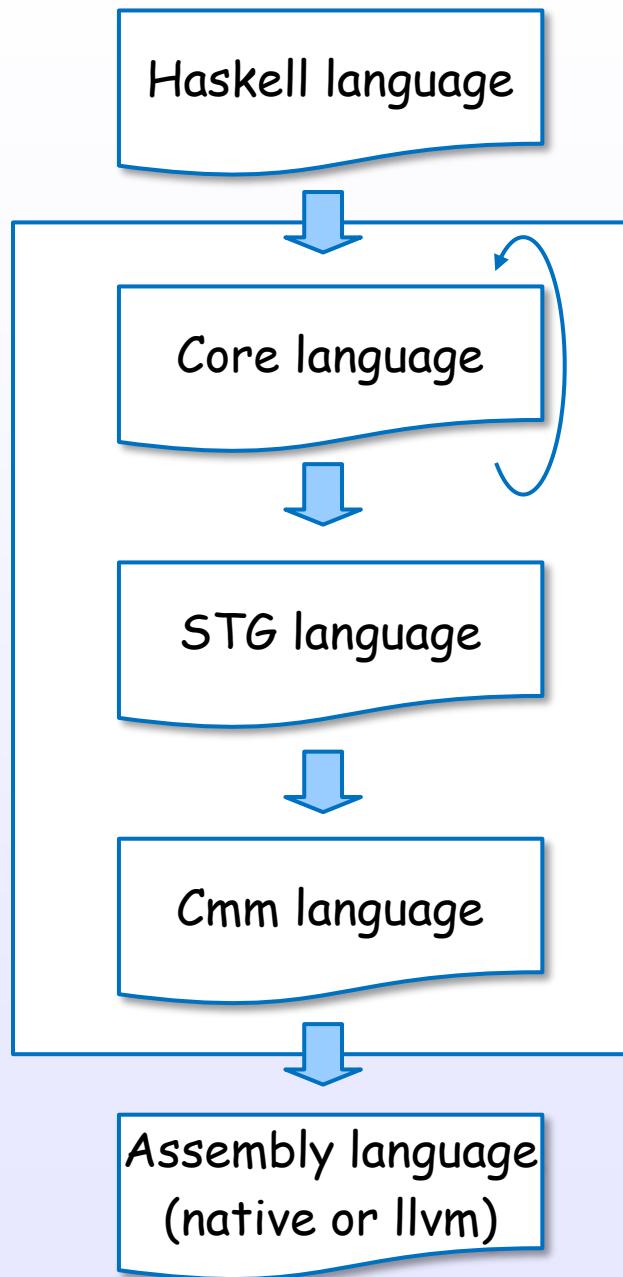
# The GHC = Compiler + Runtime System (RTS)



# Compile steps

# GHC transitions between five representations

GHC  
compile  
steps



each intermediate code can  
be dumped by :

\$ ghc -ddump-parsed  
\$ ghc -ddump-rn

\$ ghc -ddump-ds  
\$ ghc -ddump-simpl  
\$ ghc -ddump-prep

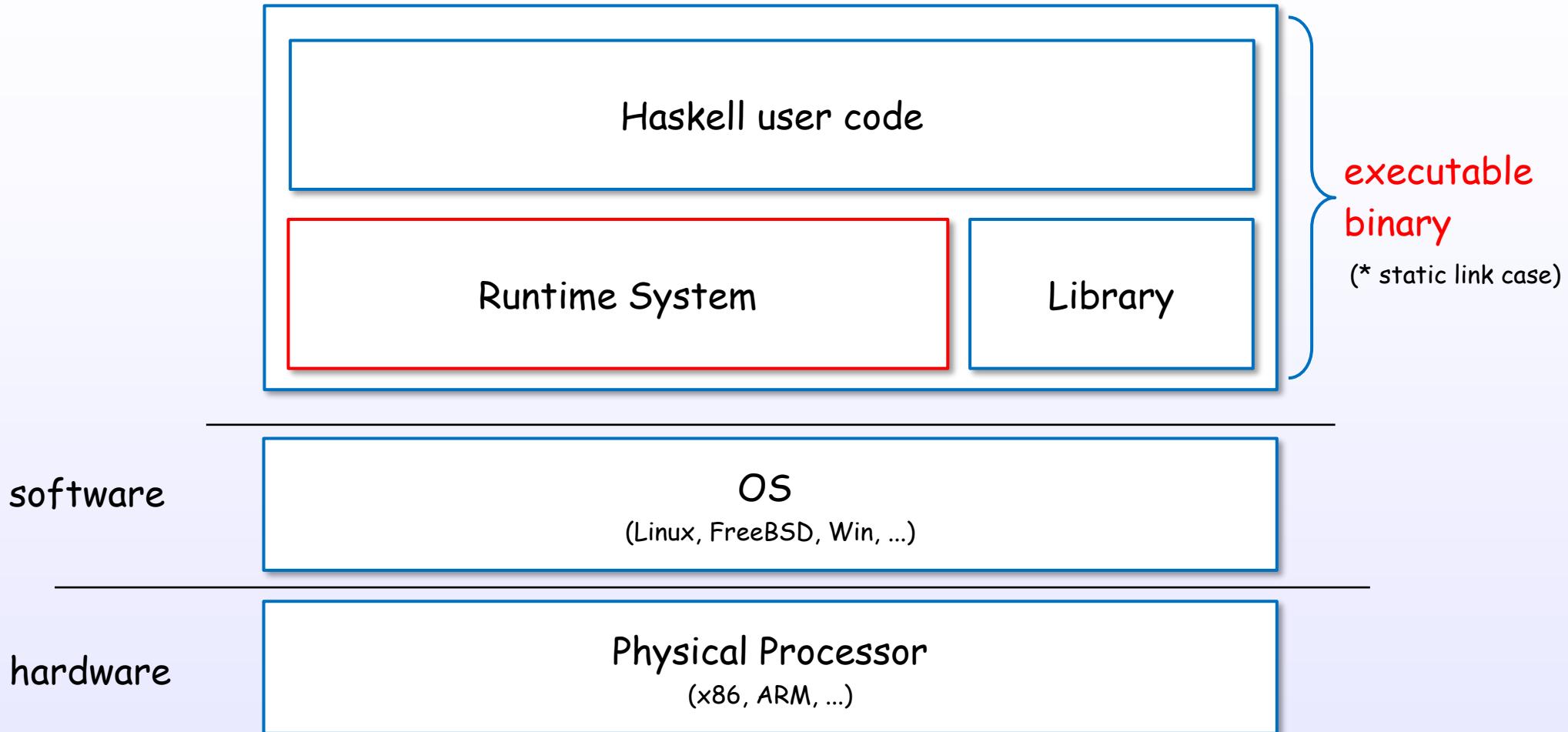
\$ ghc -ddump-stg

\$ ghc -ddump-cmm  
\$ ghc -ddump-opt-cmm

\$ ghc -ddump-llvm  
\$ ghc -ddump-asm

# Runtime System

# Generated binary includes the RTS



# Runtime System includes ...

## Runtime System

Storage Manager

User space  
Scheduler

Byte-code interpreter

Profiling

Software  
Transactional Memory

...

# Development languages

# The GHC is developed by some languages

compiler

( \$(TOP)/**compiler**/\*)

Haskell

+

Alex (lex)

Happy (yacc)

Cmm (C--)

Assembly

runtime system

( \$(TOP)/**rts**/\*)

C

+

Cmm

Assembly

library

( \$(TOP)/**libraries**/\*)

Haskell

+

C

Machine layer/models

# Machine layer

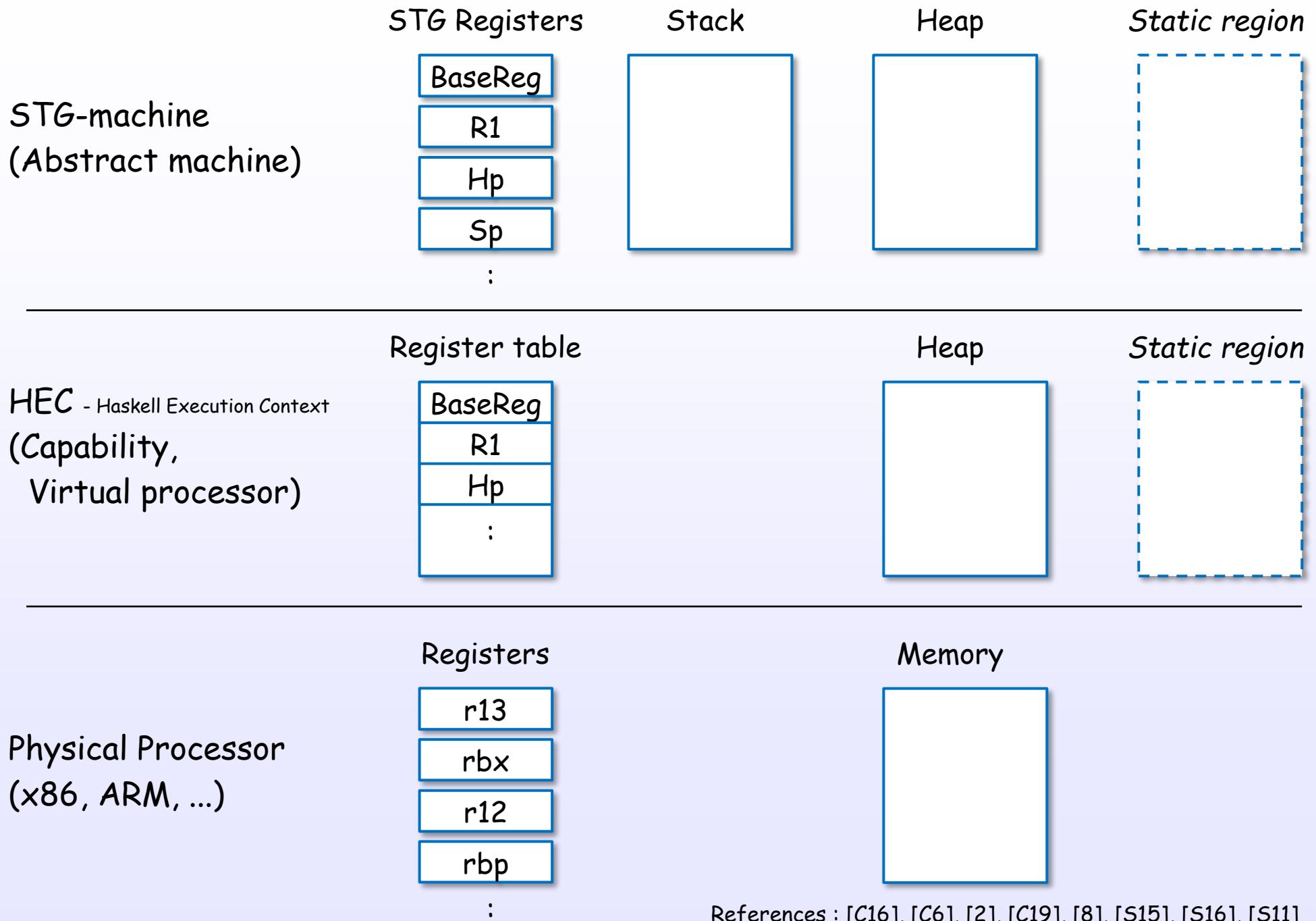
STG-machine  
(Abstract machine)

HEC - Haskell Execution Context  
(Capability, Virtual processor)

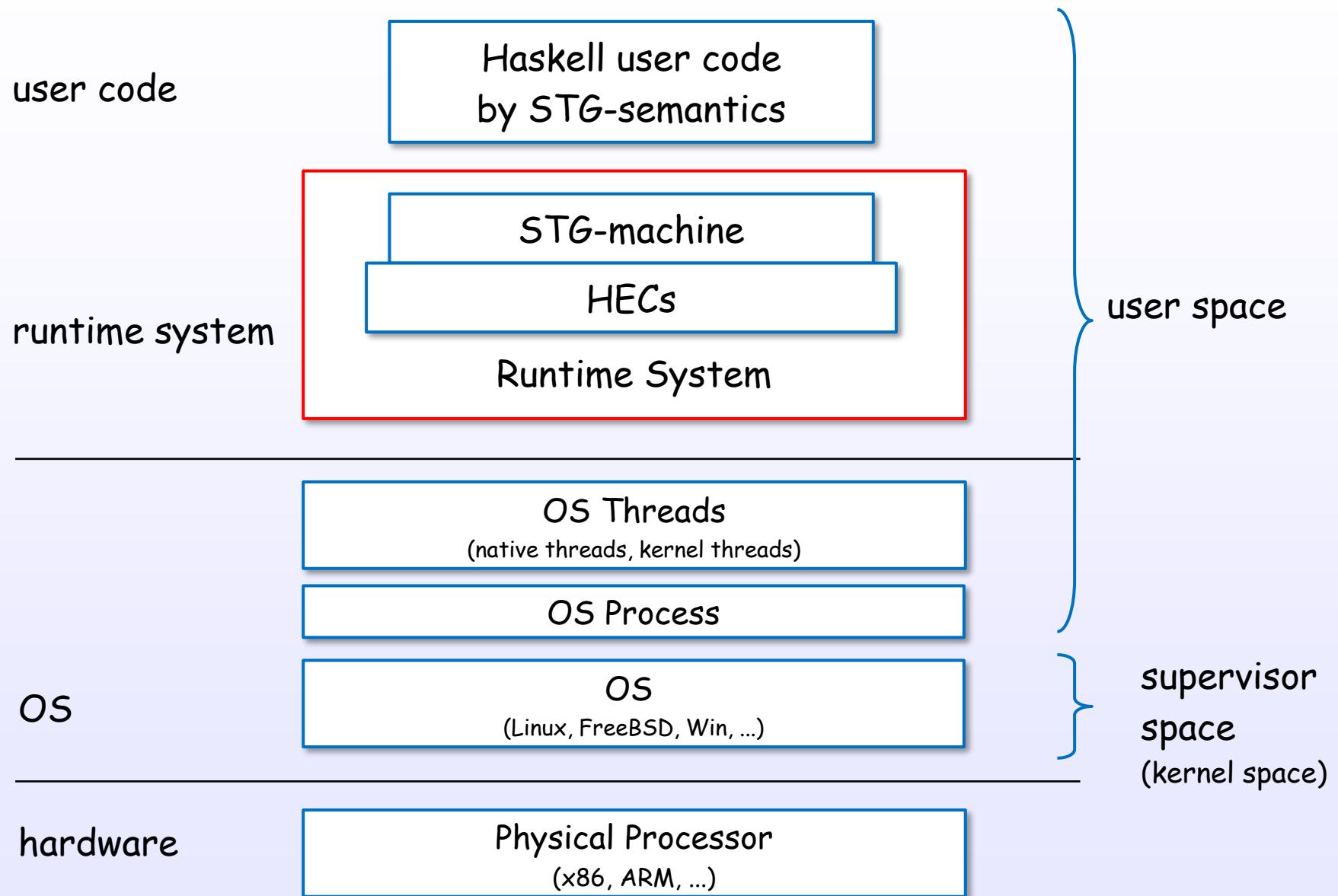
Physical Processor  
(x86, ARM, ...)

Each Haskell code is executed in STG semantics.

# Machine layer



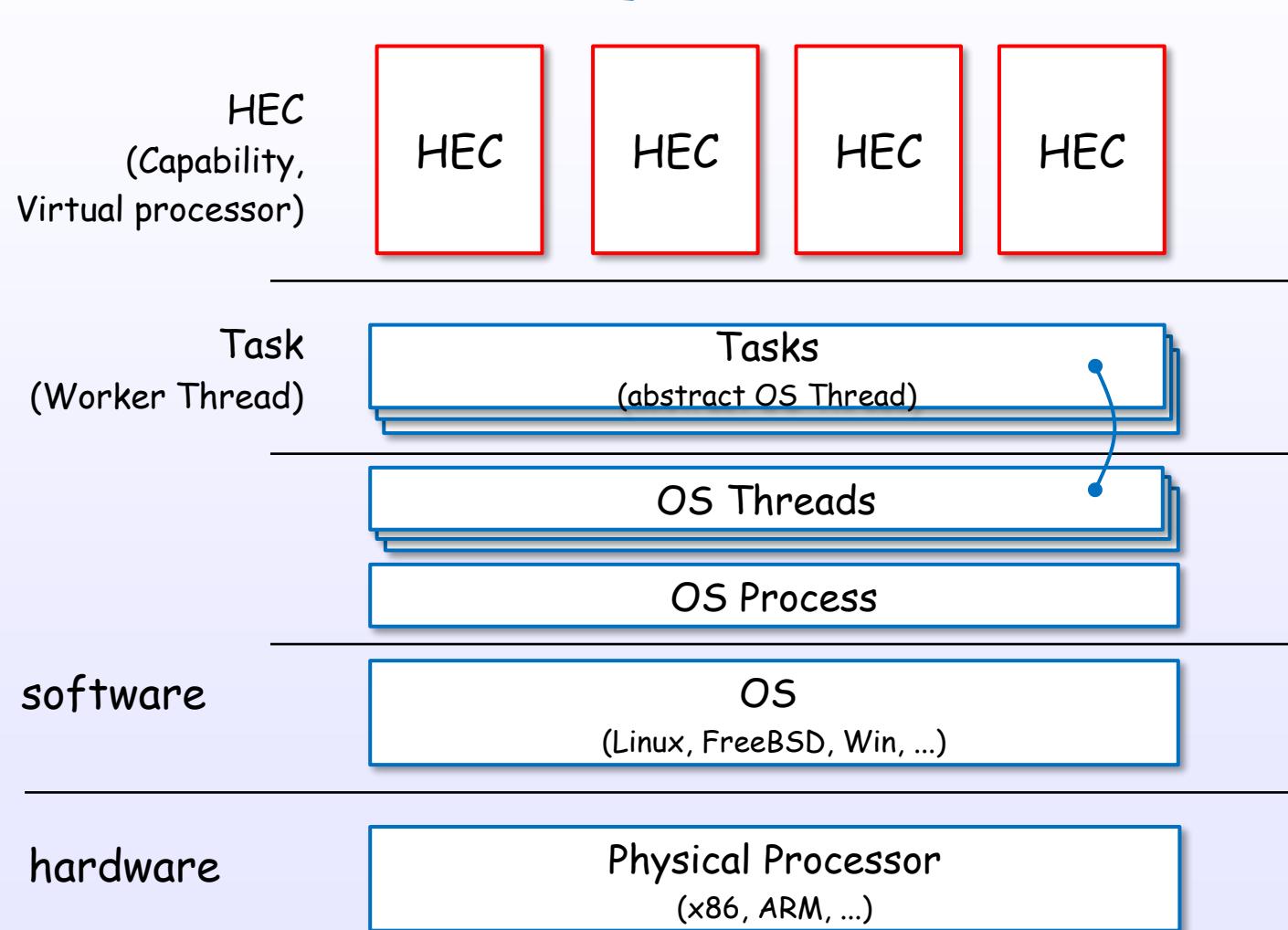
# Runtime system and HEC



# many HECs

Multi HECs can be generated by compile and runtime options :

```
$ ghc -rtsopts -threaded  
$ ./xxx +RTS -N4
```



# HEC (Capability) data structure

[rts/Capability.h] (ghc 9.0)

```
struct Capability_ {
```

```
    StgFunTable f;
```

```
    StgRegTable r;
```

```
    uint32_t no;
```

```
    uint32_t node;
```

```
    Task *running_task;
```

```
    bool in_haskell;
```

```
    uint32_t idle;
```

```
    bool disabled;
```

```
    StgTSO *run_queue_hd;
```

```
    StgTSO *run_queue_tl;
```

```
    uint32_t n_run_queue;
```

```
    InCall *suspended_ccalls;
```

```
    uint32_t n_suspended_ccalls;
```

```
    bdescr **mut_lists;
```

```
    bdescr **saved_mut_lists;
```

```
    bdescr *pinned_object_block;
```

```
    bdescr *pinned_object_blocks;
```

```
    StgWeak *weak_ptr_list_hd;
```

```
    StgWeak *weak_ptr_list_tl;
```

```
    int context_switch;
```

register table

run queue

```
    int interrupt;
```

```
    W_total_allocated;
```

```
#if defined(THREADED_RTS)
```

```
    Task *spare_workers;
```

```
    uint32_t n_spare_workers;
```

```
    Mutex lock;
```

```
    Task *returning_tasks_hd;
```

```
    Task *returning_tasks_tl;
```

```
    uint32_t n_returning_tasks;
```

```
    Message *inbox;
```

```
    struct PutMVar_ *putMVars;
```

```
    SparkPool *sparks;
```

```
    SparkCounters spark_stats;
```

```
#endif
```

```
    StgTVarWatchQueue *free_tvar_watch_queues;
```

```
    StgTRecChunk *free_trec_chunks;
```

```
    StgTRecHeader *free_trec_headers;
```

```
    uint32_t transaction_tokens;
```

```
}
```

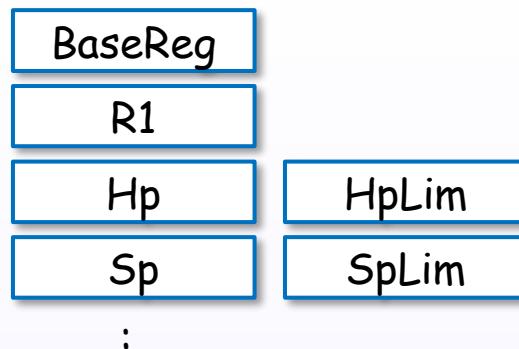
Each HEC (Capability) has a register table and a run queue and ...

Each HEC (Capability) is initialized at initCapabilities [rts/Capability.c]

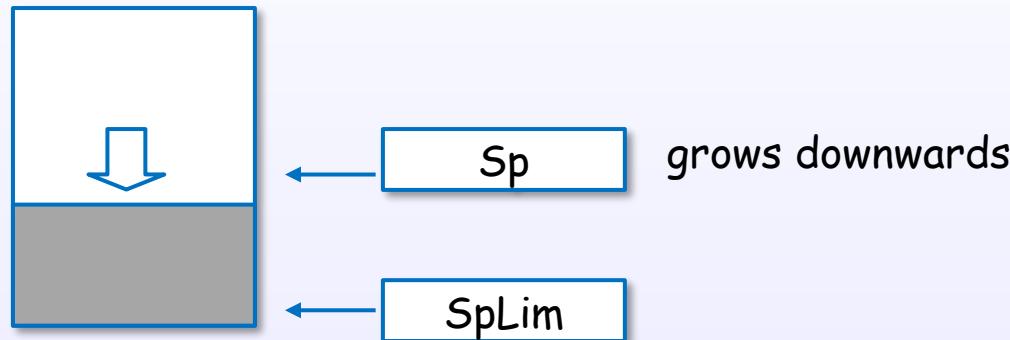
STG-machine

# The STG-machine consists of three parts

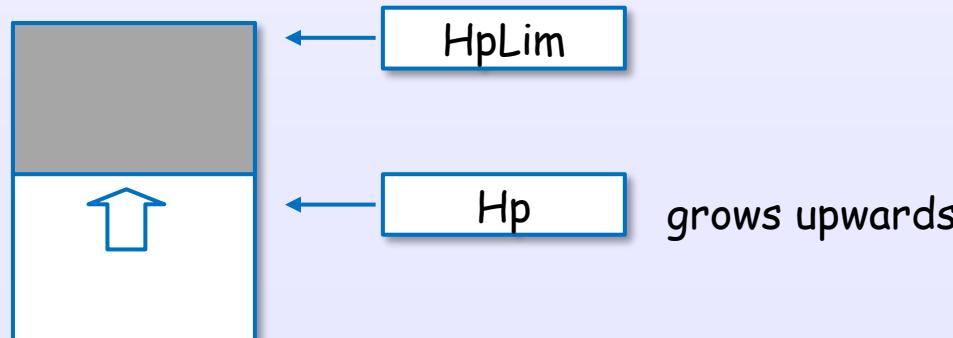
STG Registers



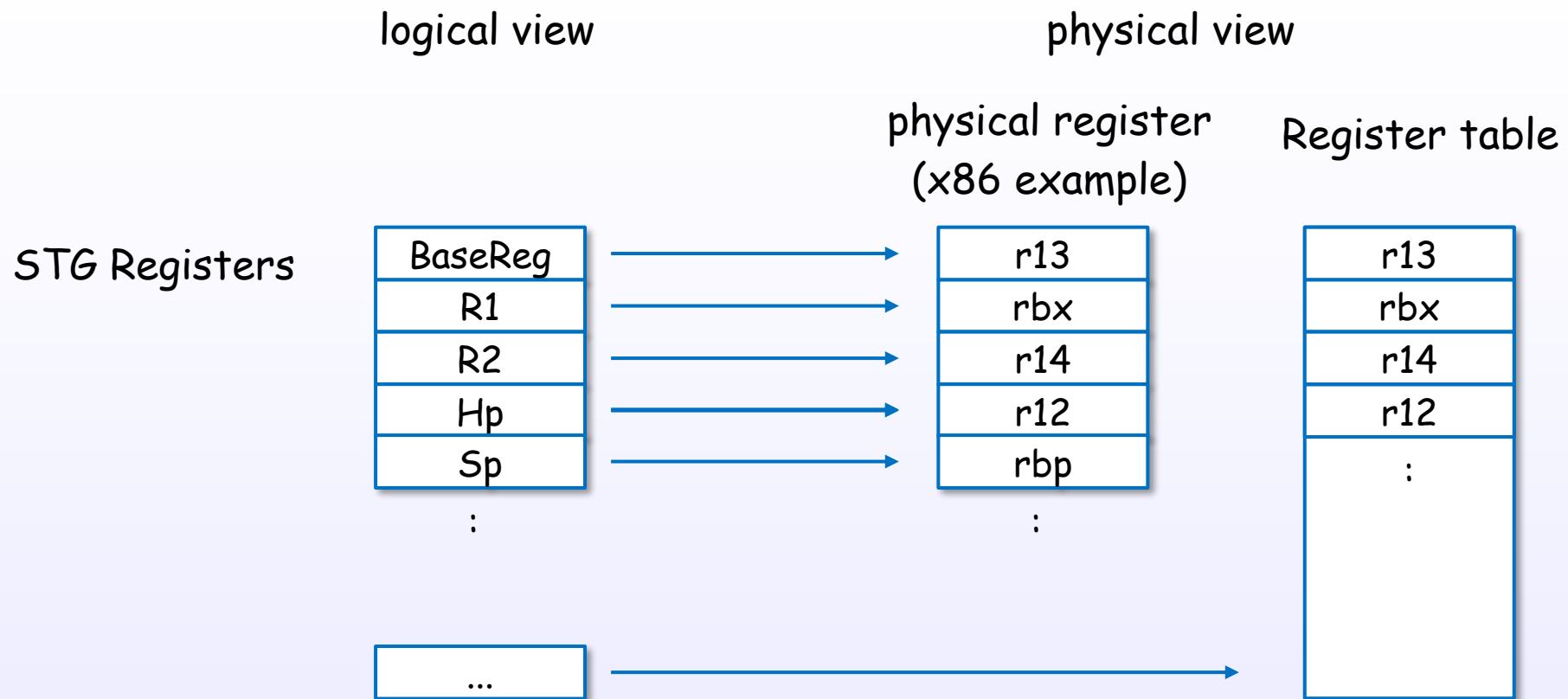
Stack



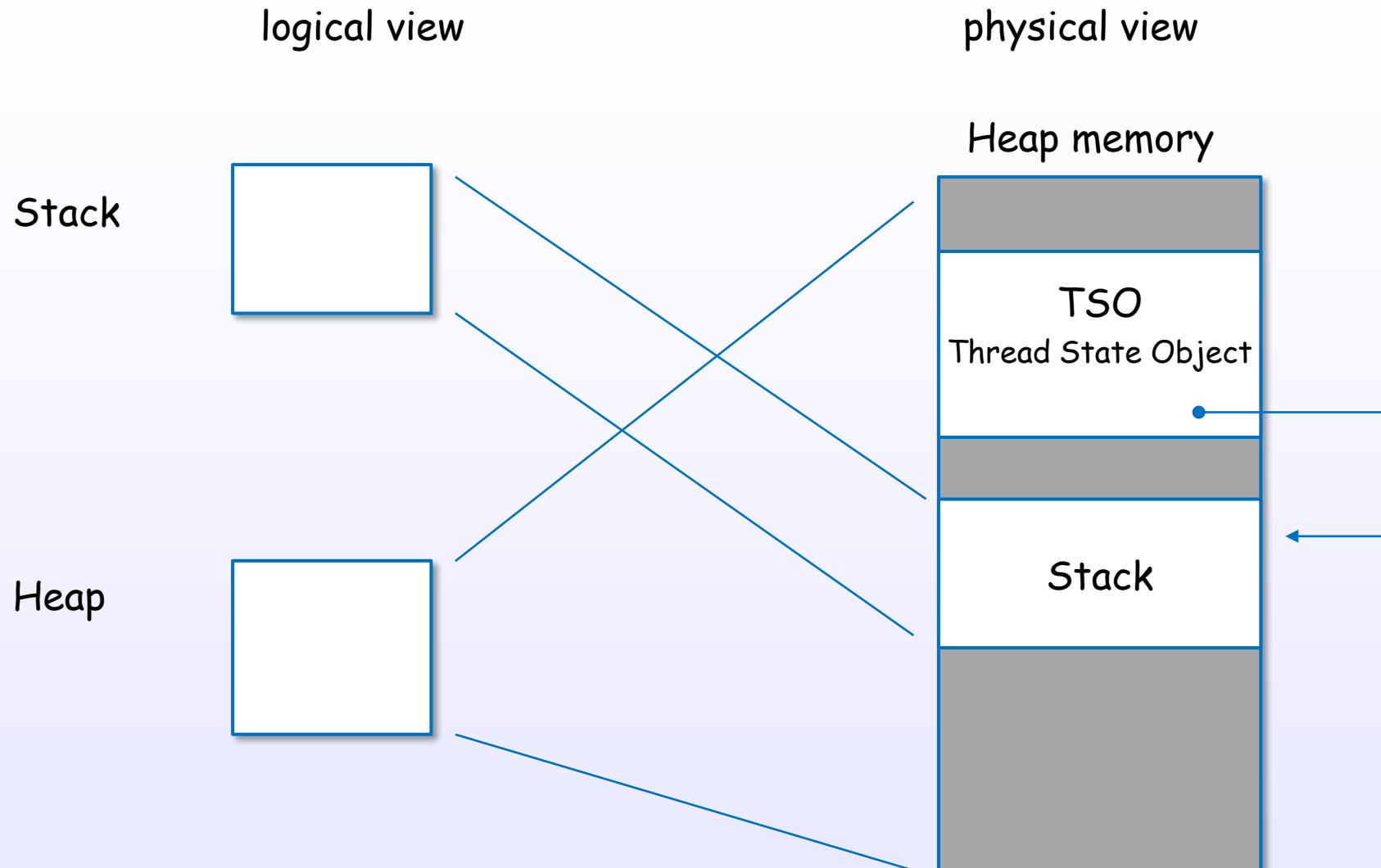
Heap



# STG-machine is mapped to physical processor



# STG-machine is mapped to physical processor



A stack and a TSO object are in the heap.

The stack is stored separately from the TSO for size extension and GC.

# TSO data structure

[includes/rts/storage/TSO.h] (ghc 9.0)

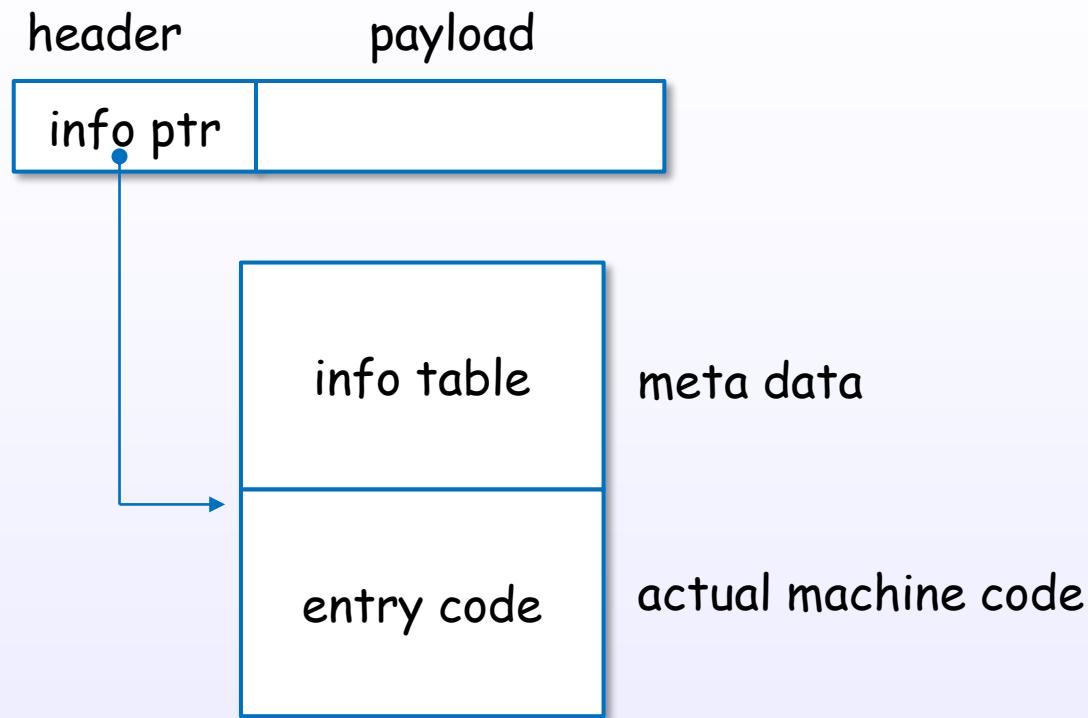
```
typedef struct StgTSO_ {
    StgHeader          header;
    struct StgTSO_*   _link;
    struct StgTSO_*   global_link;
    struct StgStack_* *stackobj; ← link to stack object
    StgWord16          what_next;
    StgWord16          why_blocked;
    StgWord32          flags;
    StgTSOBLOCKINFO   block_info;
    StgThreadID        id;
    StgWord32          saved_errno;
    StgWord32          dirty;
    struct InCall_*    bound;
    struct Capability_* cap;
    struct StgTRecHeader_* trec;
    struct MessageThrowTo_* blocked_exceptions;
    struct StgBlockingQueue_* bq;
    StgInt64           alloc_limit;
    StgWord32          tot_stack_size;
} *StgTSOPtr;
```

A TSO object is **only ~18words + stack**. Lightweight!

References : [S5]

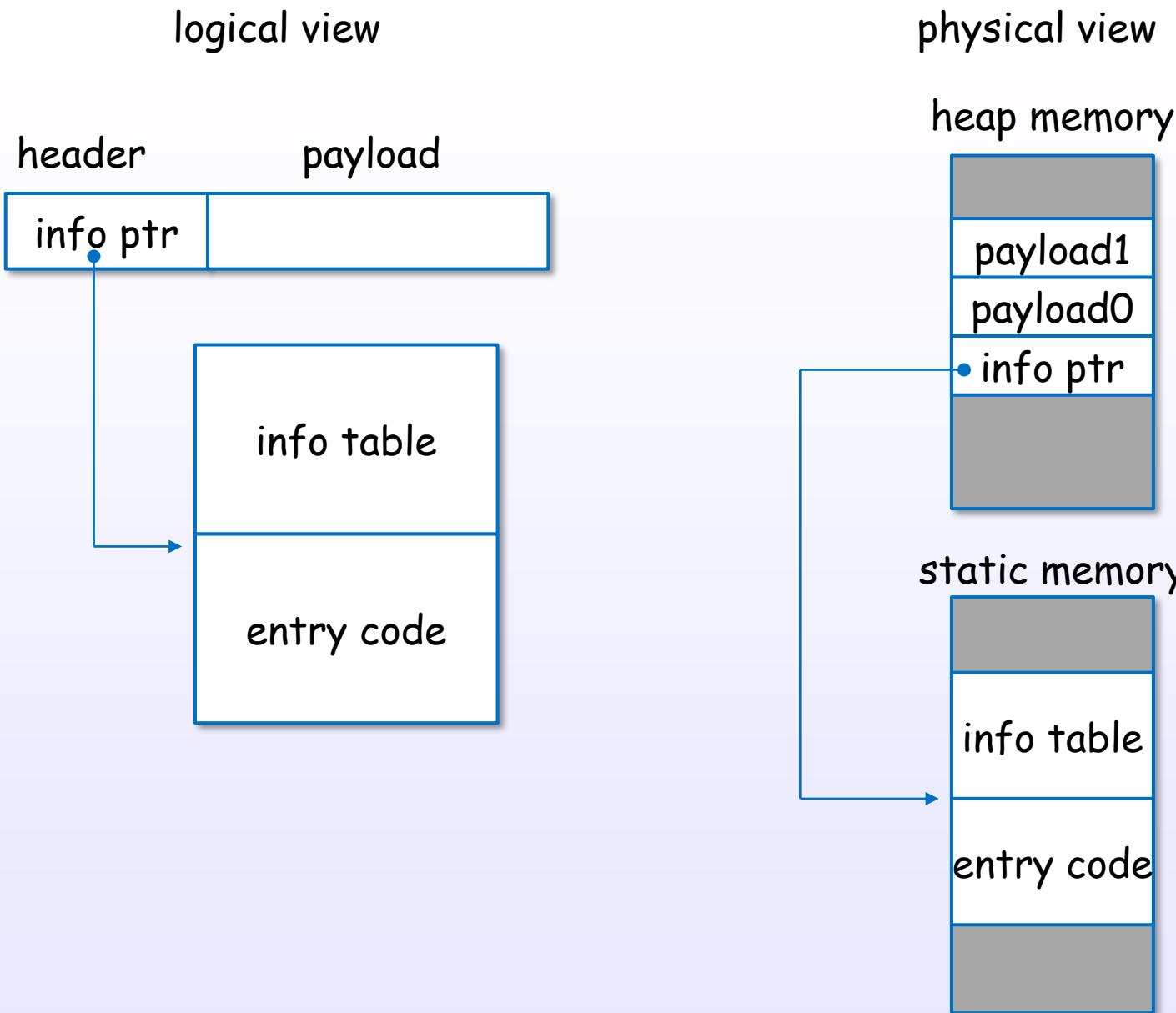
# Heap objects in STG-machine

# Every heap object is represented uniformly



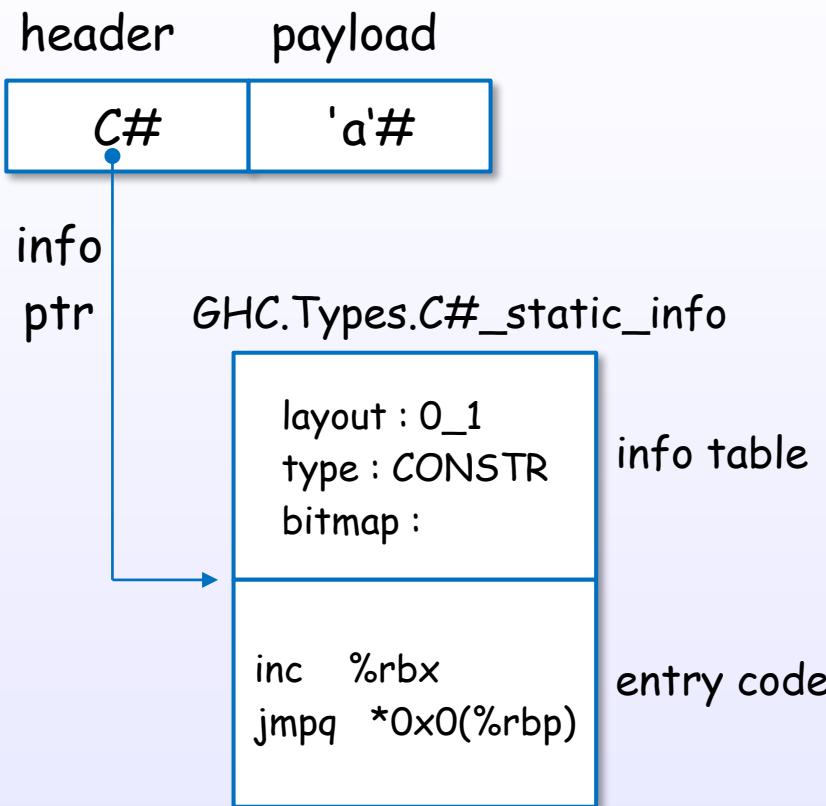
Closure (header + payload) + Info Table + Entry Code

# Heap object (closure)

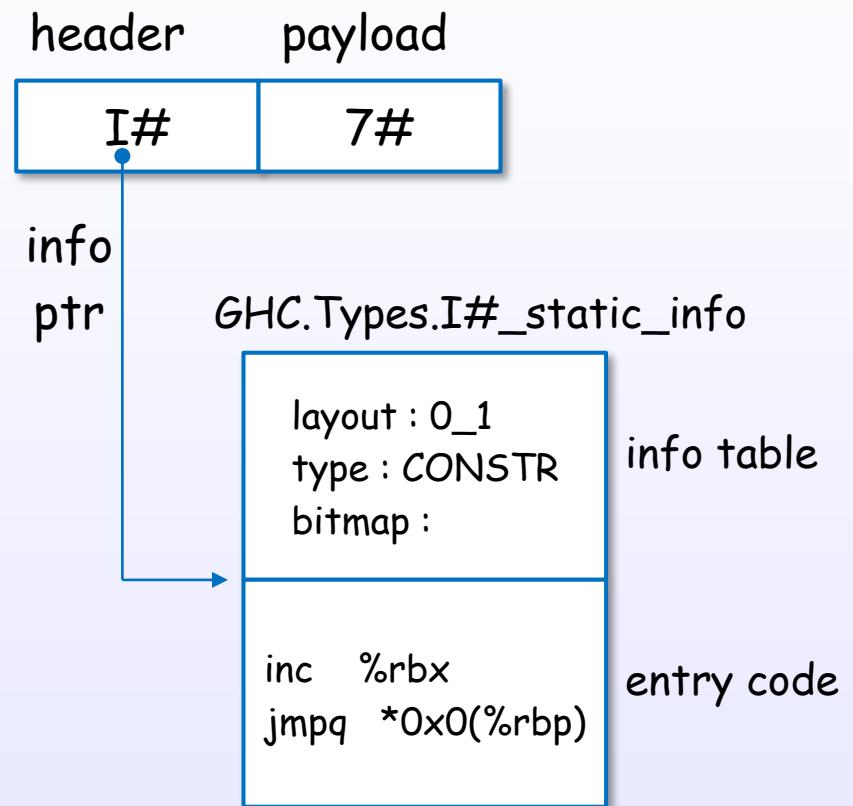


# Closure examples : Char, Int

'a' :: Char



7 :: Int



# Closure example (code)

[Example.hs]

```
module Example where  
value1 :: Int  
value1 = 7
```

STG

Cmm

[ghc -O -ddump-stg Example.hs]

```
Example.value1 :: GHC.Types.Int  
[GblId, Caf=NoCafRefs, Str=m, Unf=OtherCon []] =  
  CCS_DONT_CARE GHC.Types.I#(7#);
```

[ghc -O -ddump-opt-cmm Example.hs]

```
section ""data"" . Example.value1_closure {  
  Example.value1_closure:  
    const GHC.Types.I#_con_info;  
    const 7;  
}
```

asm

[ghc -O -ddump-asm Example.hs]

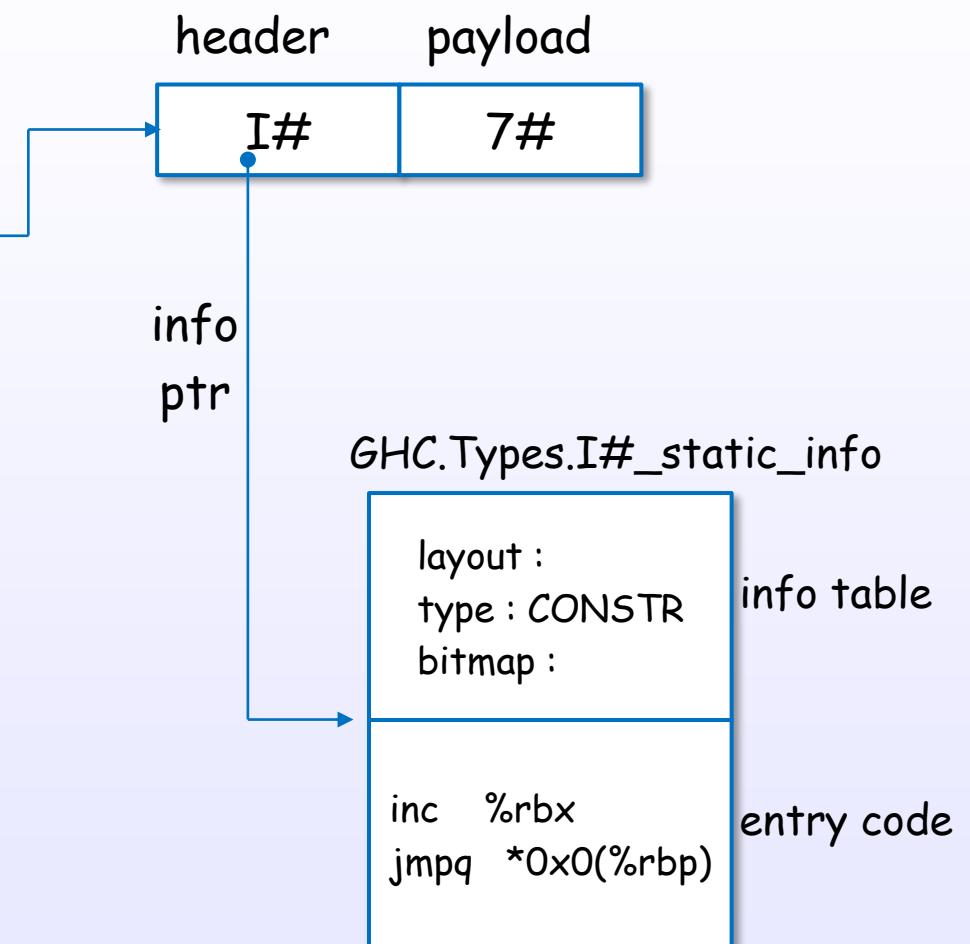
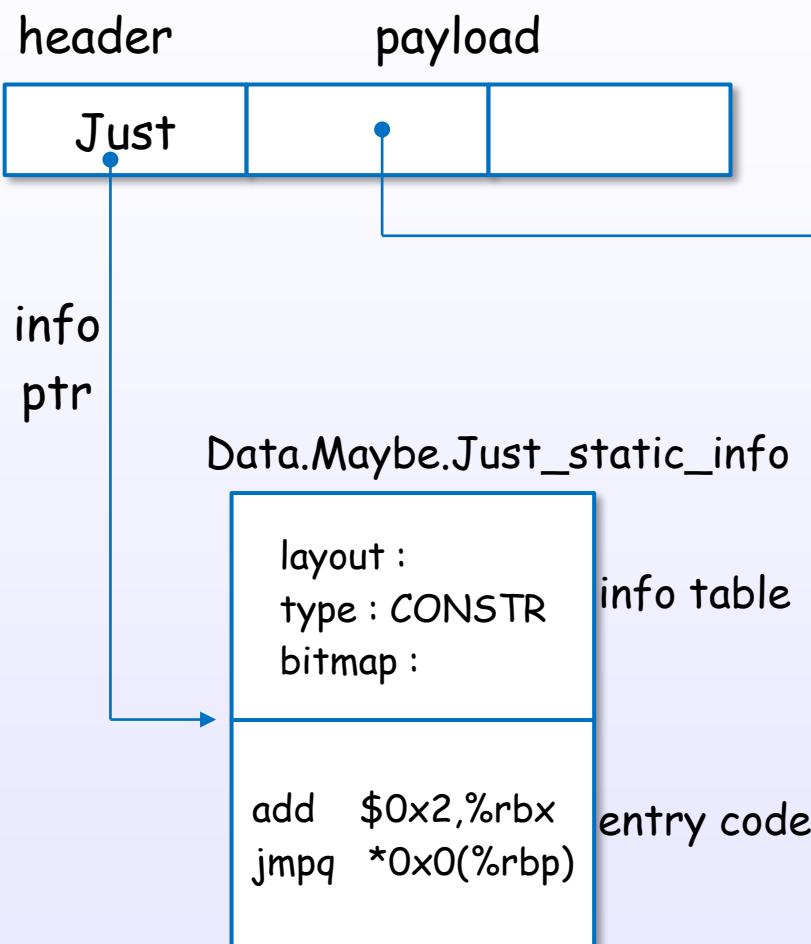
```
.section .data  
.align 8  
.align 1  
.globl Example.value1_closure  
.type Example.value1_closure, @object  
Example.value1_closure:  
.quad GHC.Types.I#_con_info  
.quad 7
```

header payload

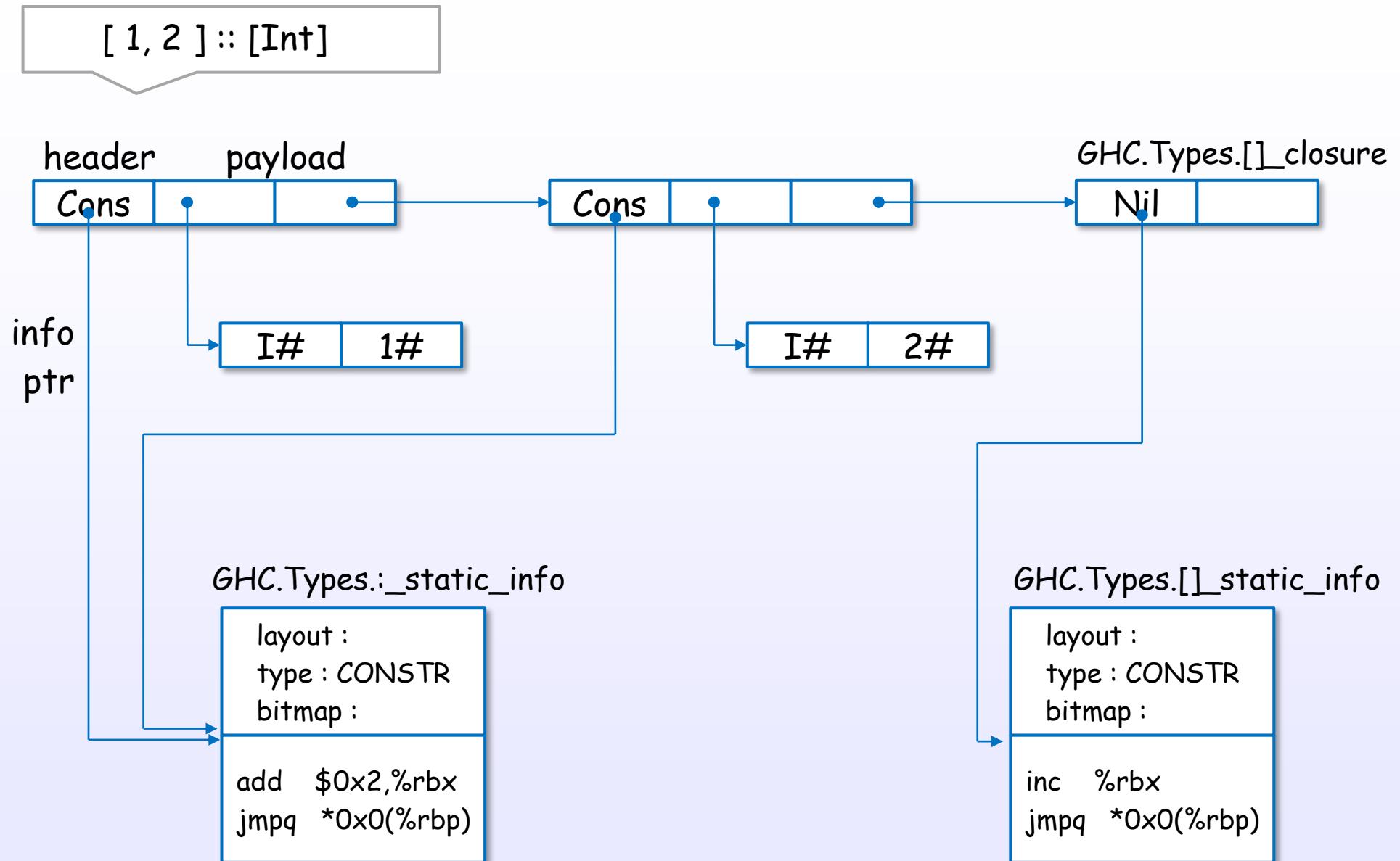
I# 7#

# Closure examples : Maybe

Just 7 :: Maybe Int



# Closure examples : List



# Closure examples : Thunk

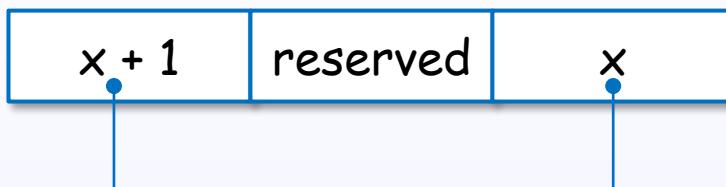
"thunk"

$x + 1 :: \text{Int}$

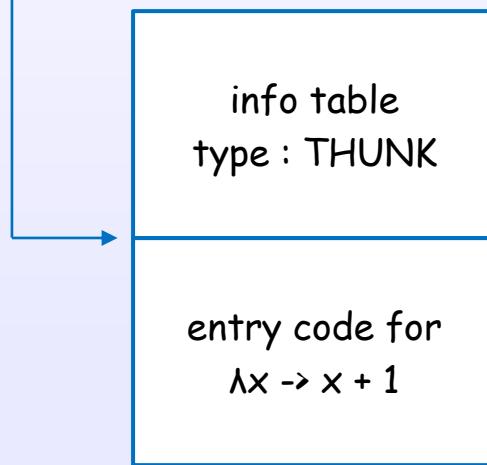
(free variable :  $x = 7$ )

header

payload



info  
ptr

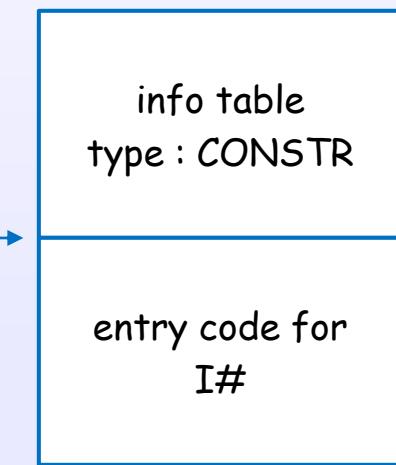


header

payload

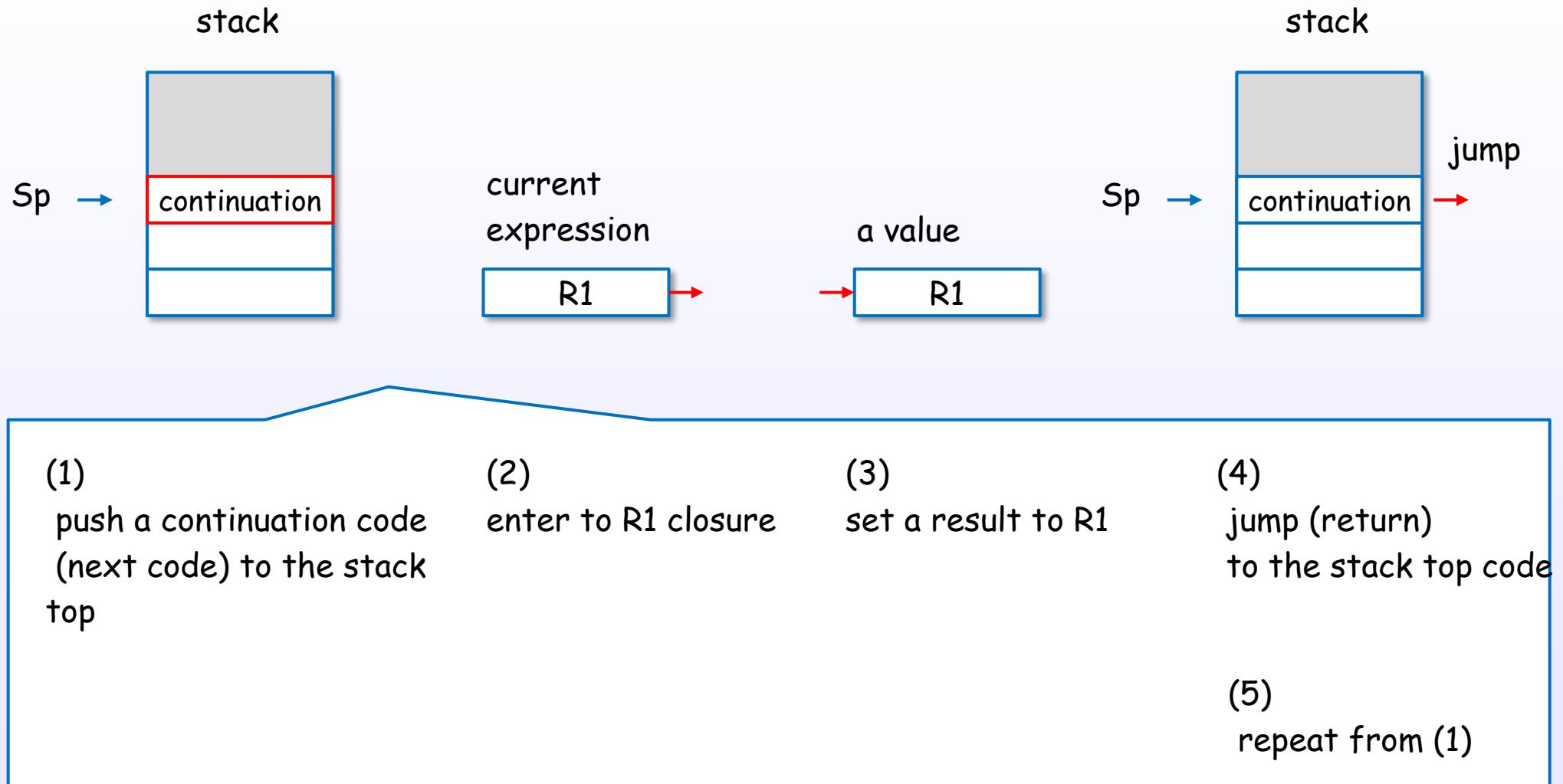


info  
ptr

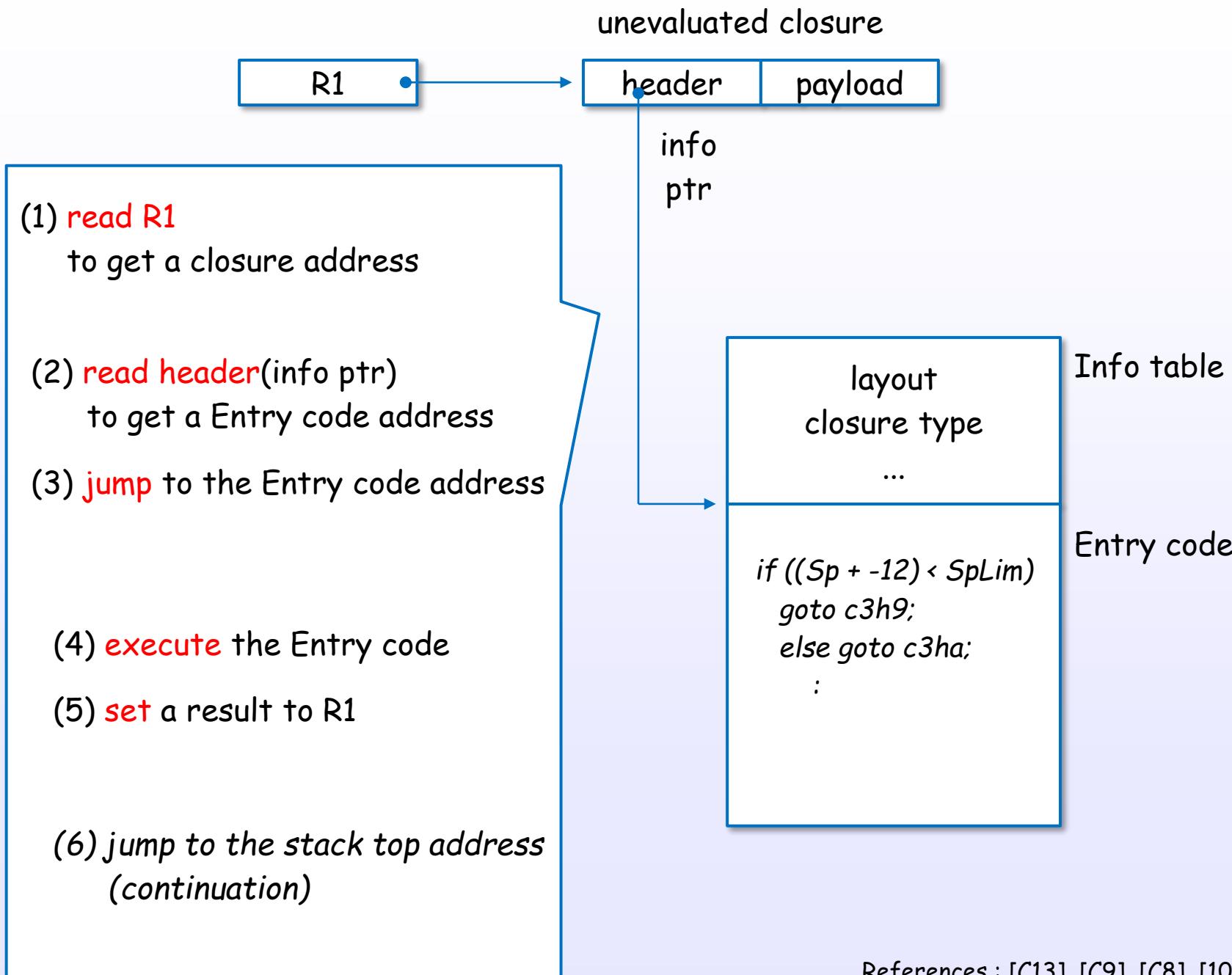


STG-machine evaluation

# STG evaluation flow

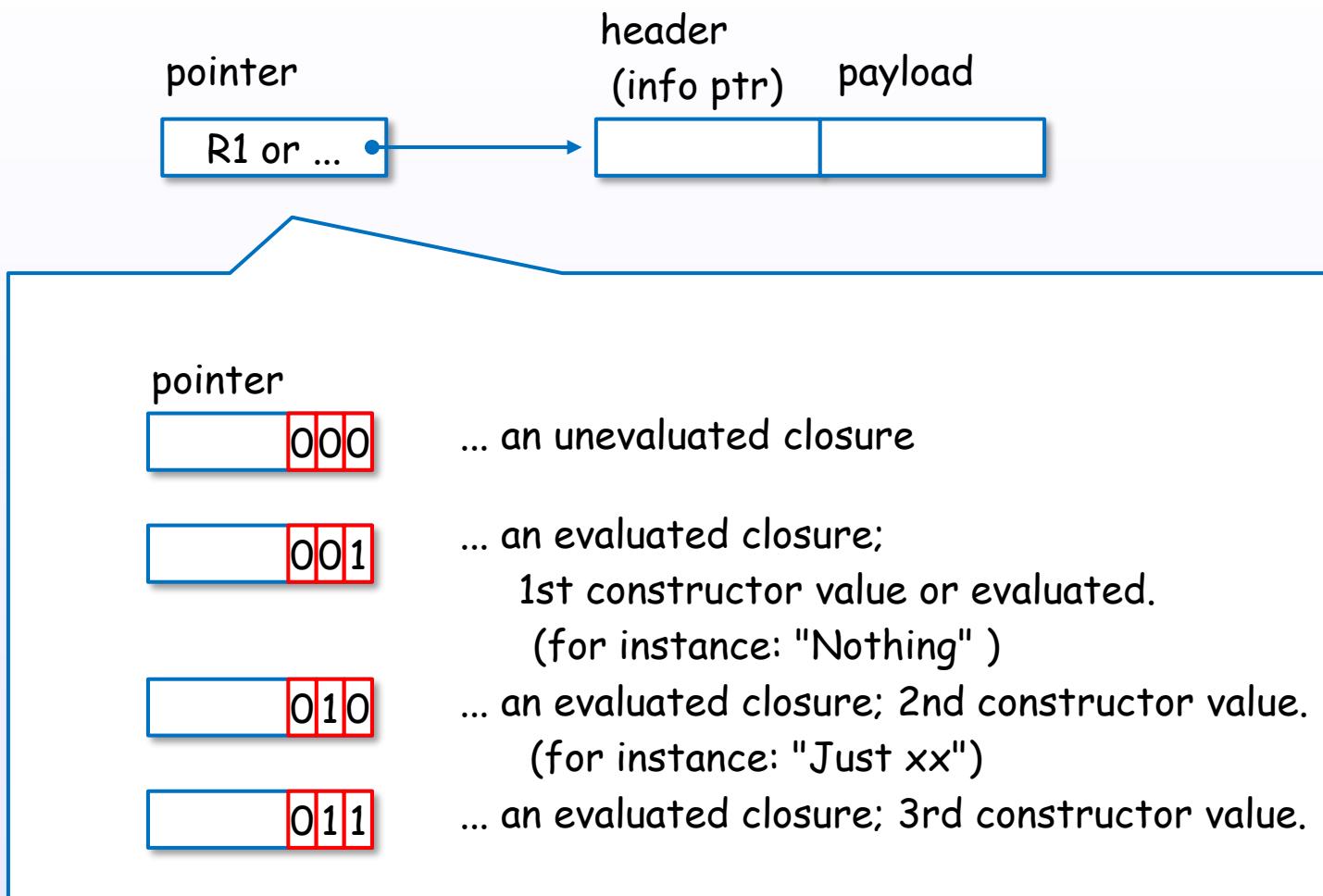


# Enter to a closure



# Pointer tagging

# Pointer tagging



\* 64bit machine case

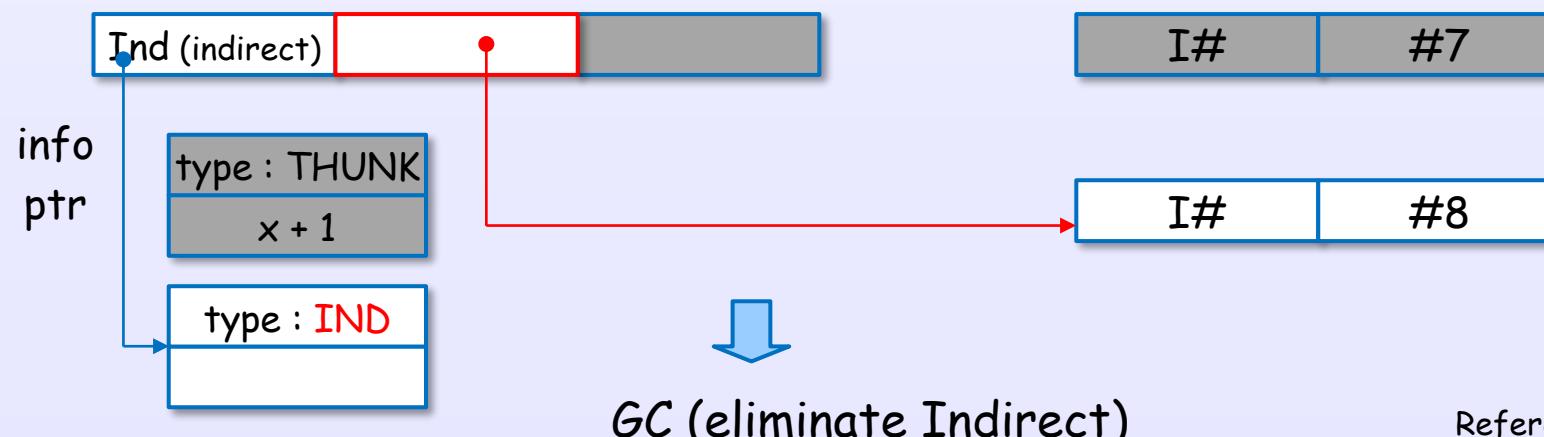
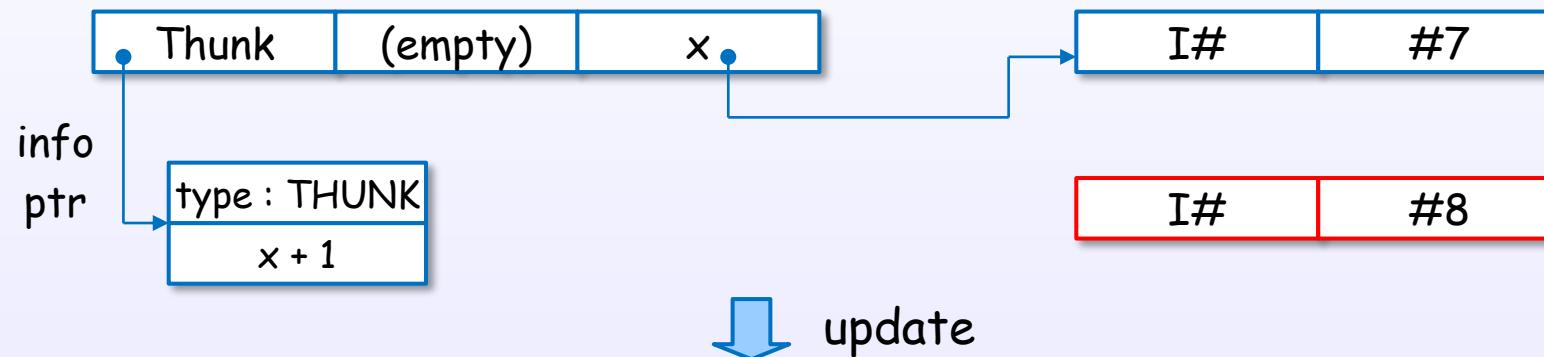
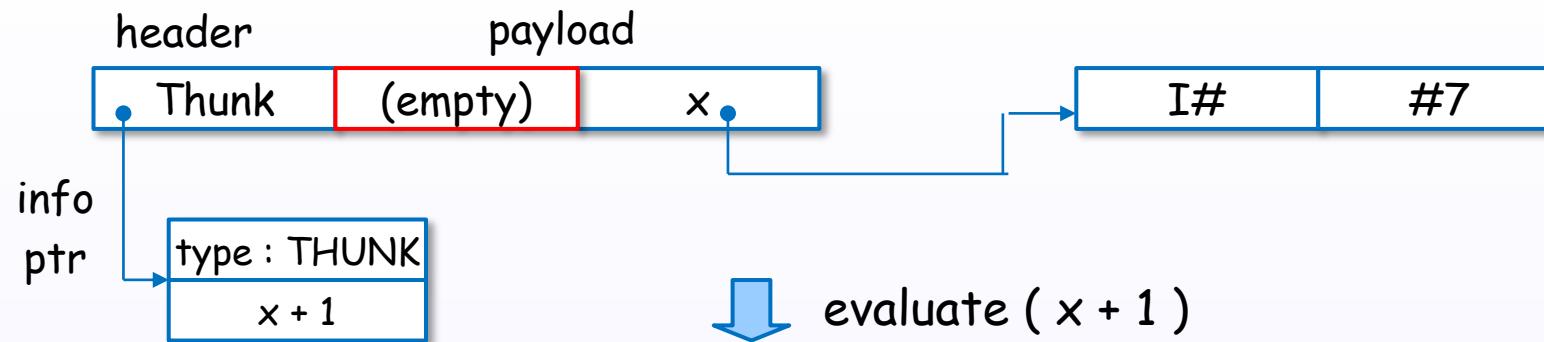
fast judgment!

check only pointer's lower bits without evaluating the closure.

Thunk and update

# Thunk and update

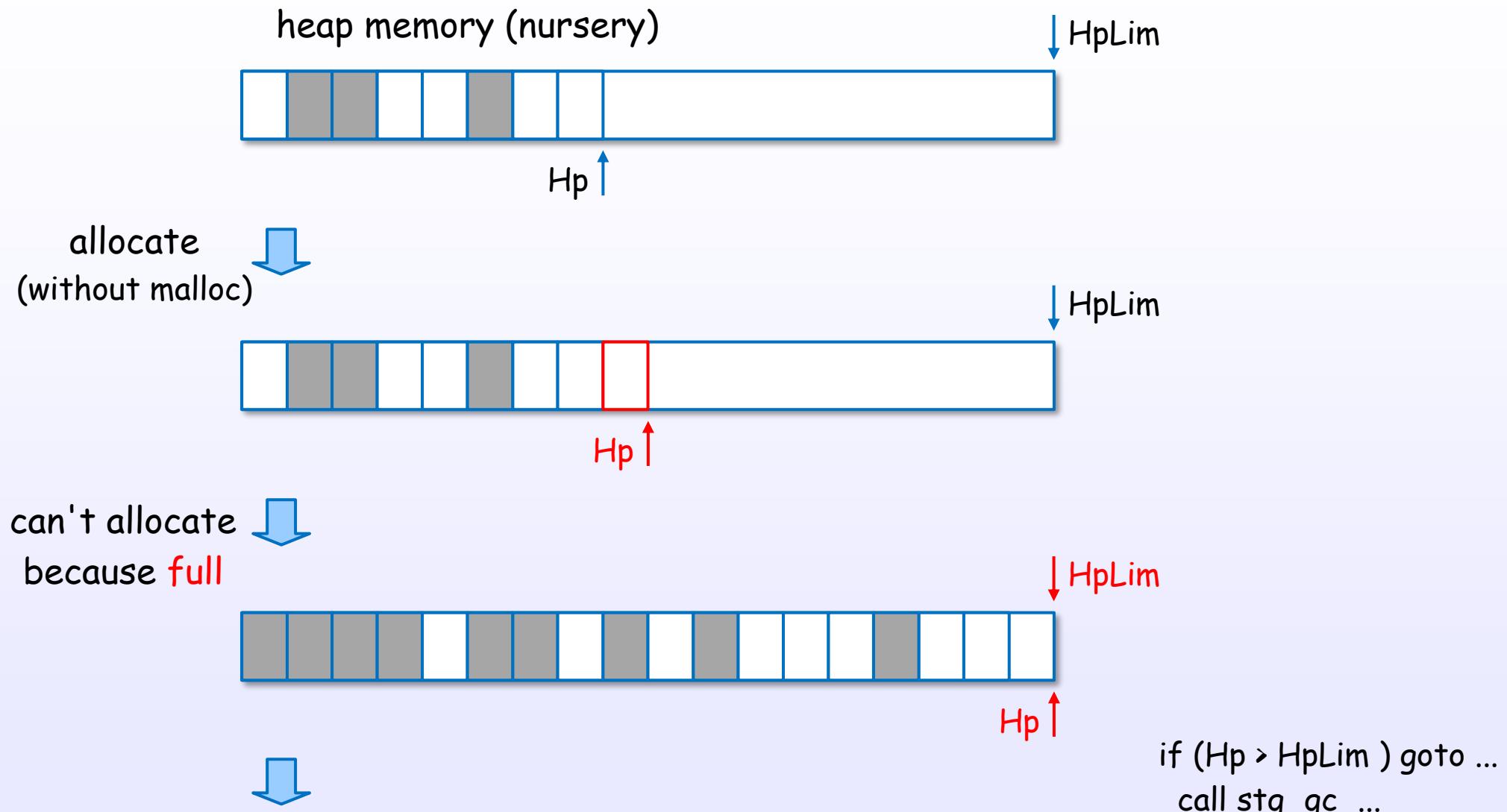
"thunk"       $x + 1 :: \text{Int}$  (free variable :  $x = 7$ )



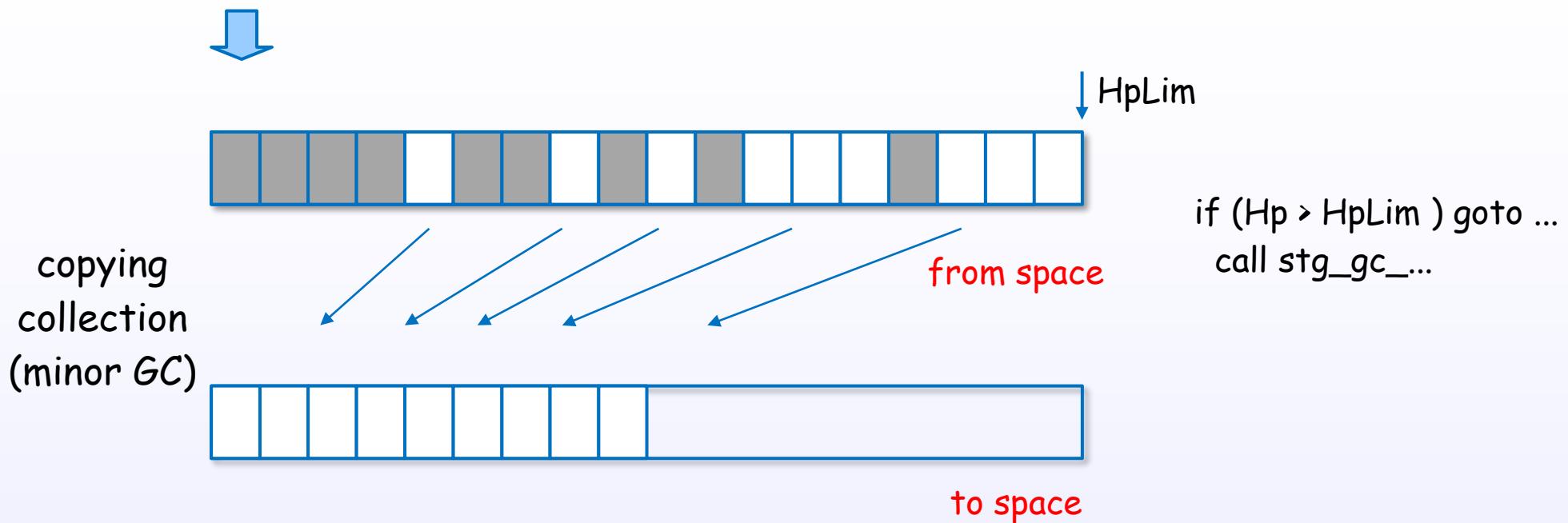
lock free

Allocate and free heap objects

# Allocate heap objects

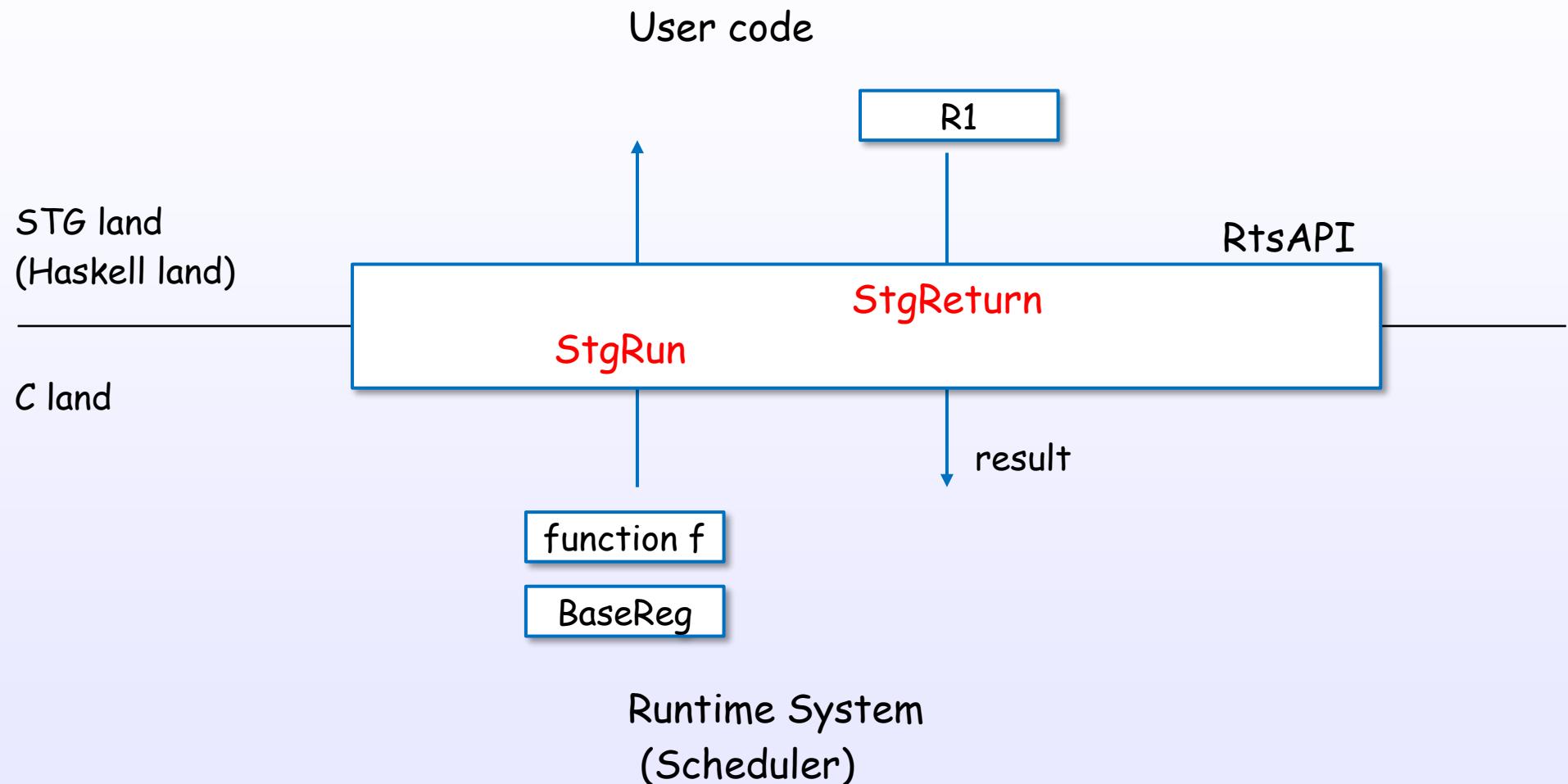


# free and collect heap objects



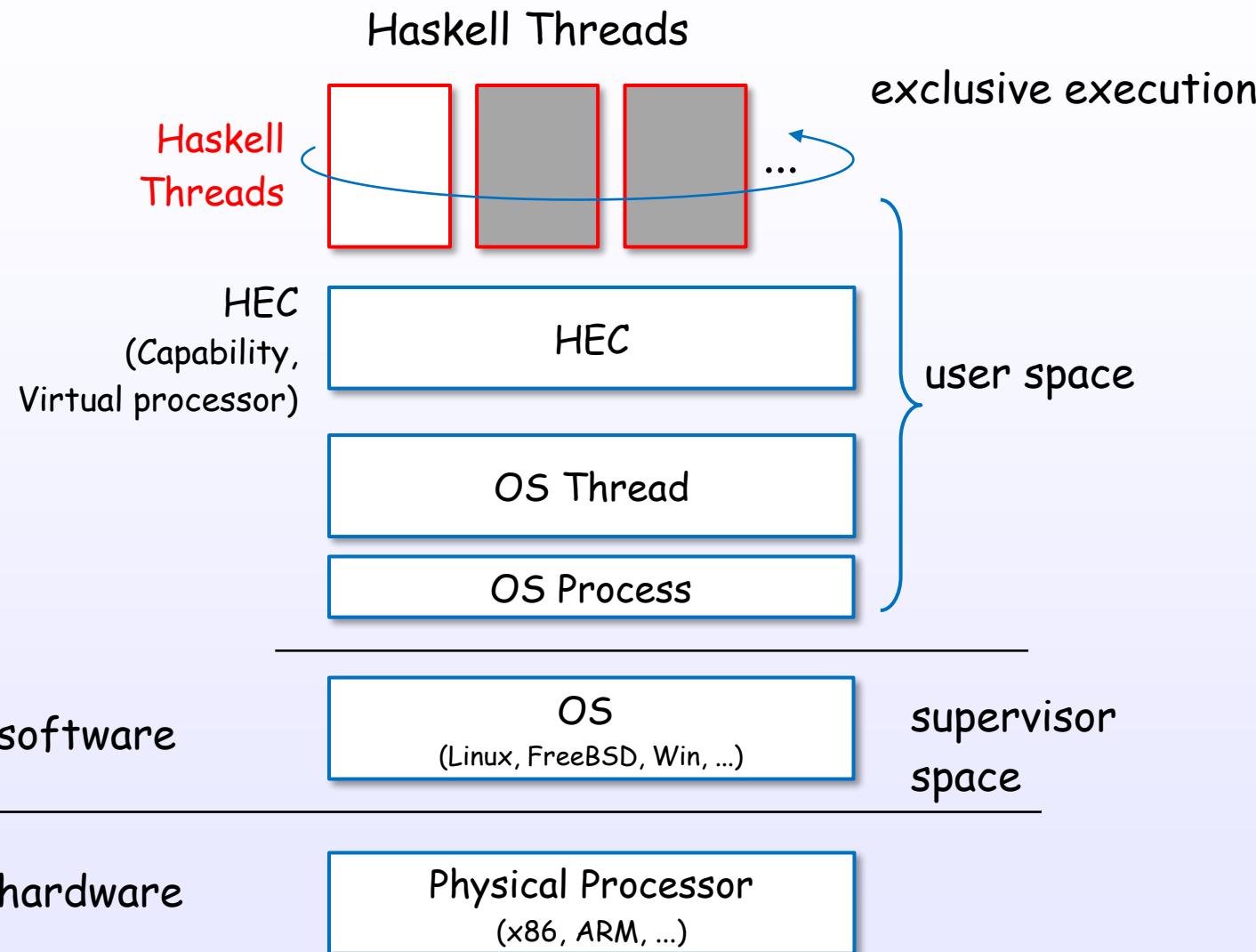
STG - C land interface

# STG (Haskell) land - C land interface

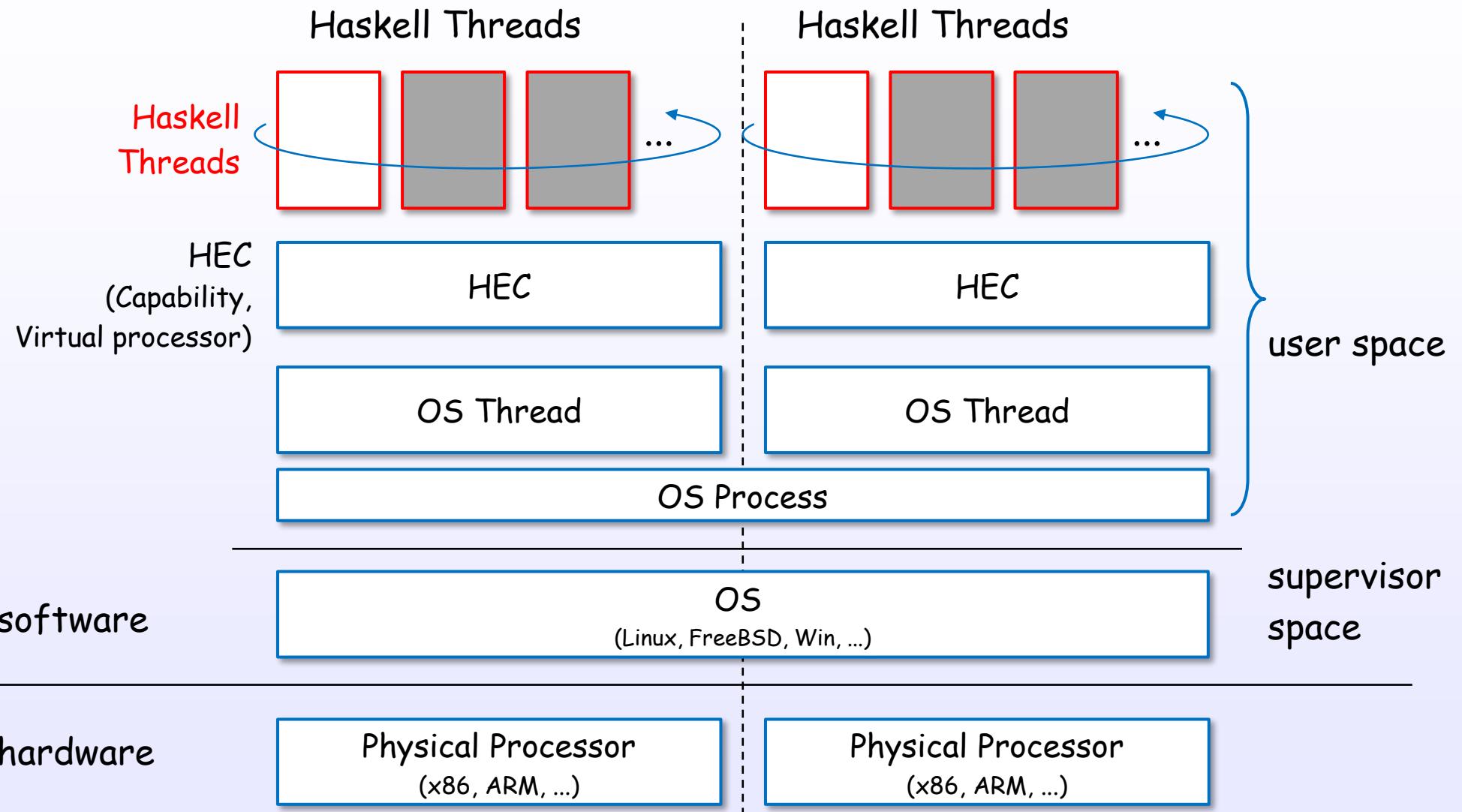


Thread

# Thread layer (single core)



# Thread layer (multi core)

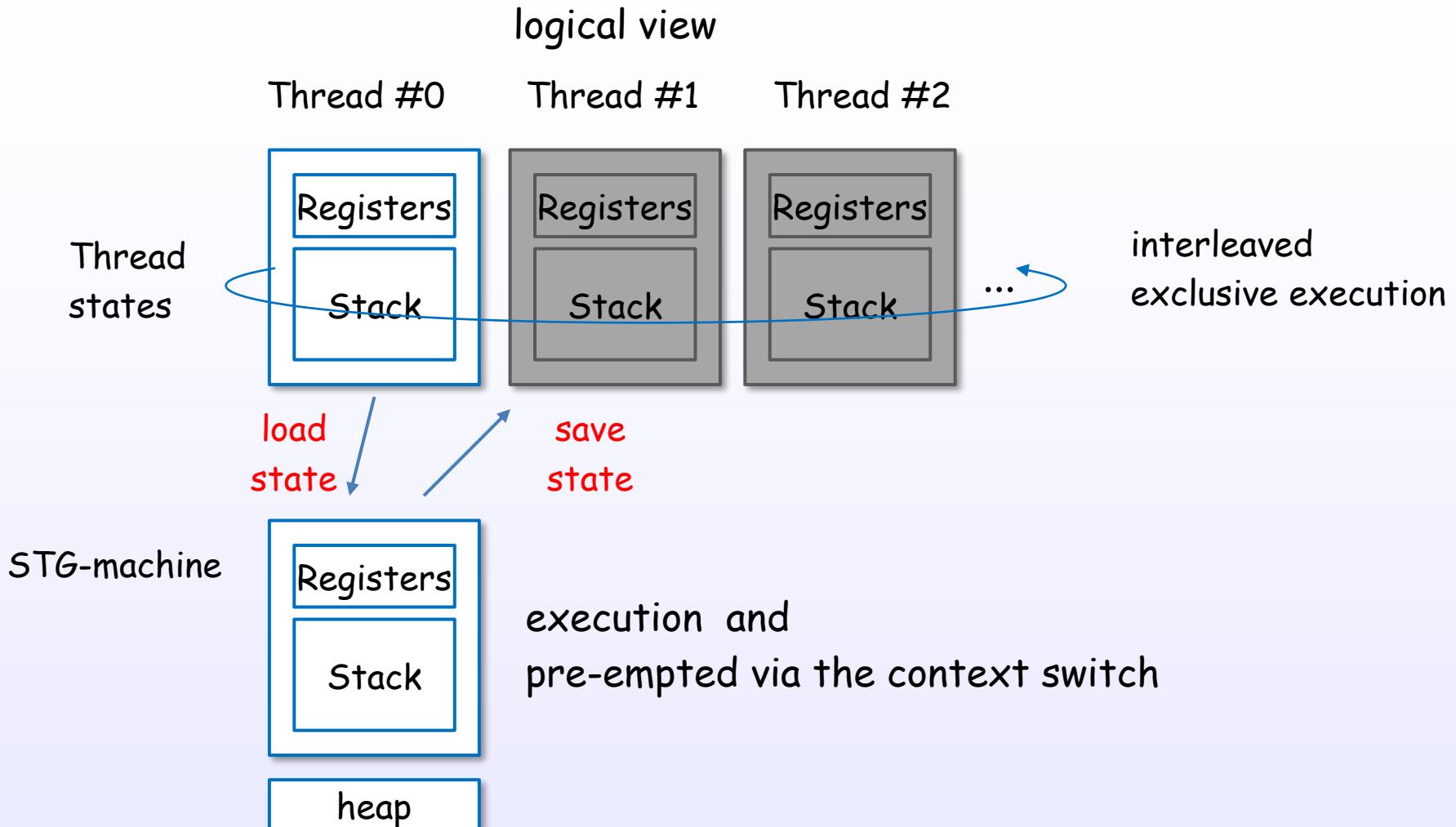


\*Threaded option case (ghc -threaded)

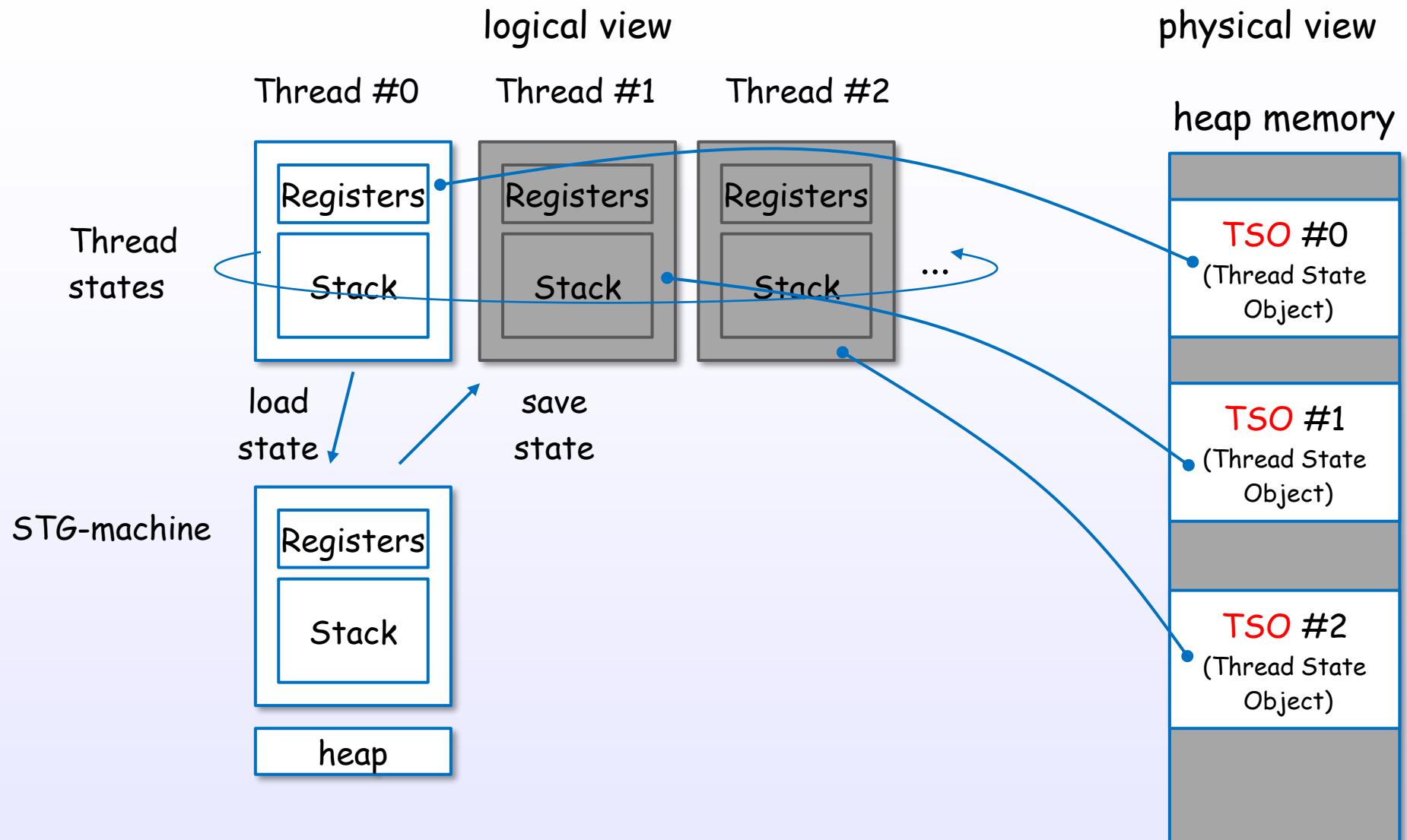
References : [5], [8], [9], [14], [C19], [C13], [19], [S17], [S16], [S23], [S22], [S14]

# Thread context switch

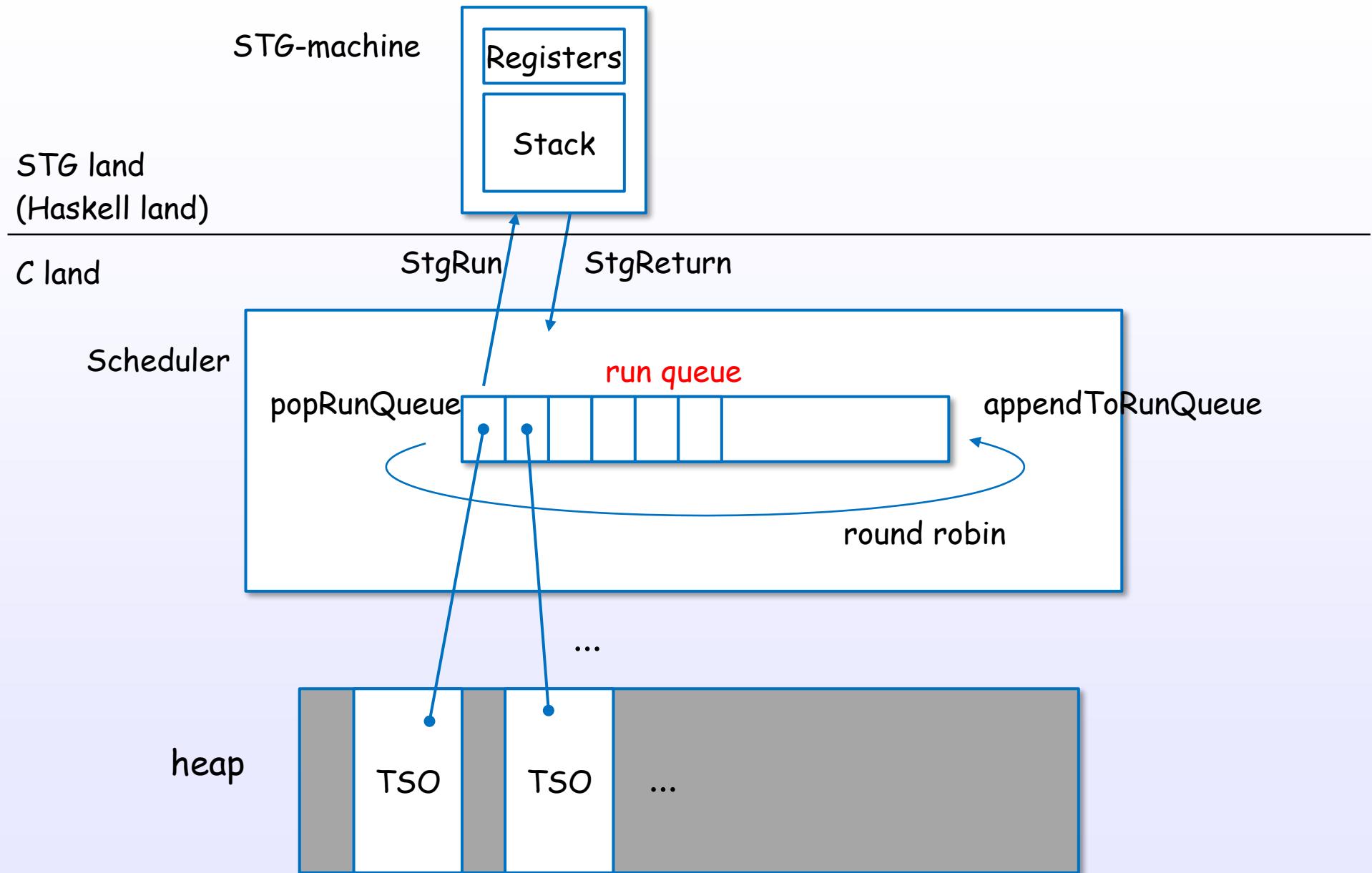
# Threads and context switch



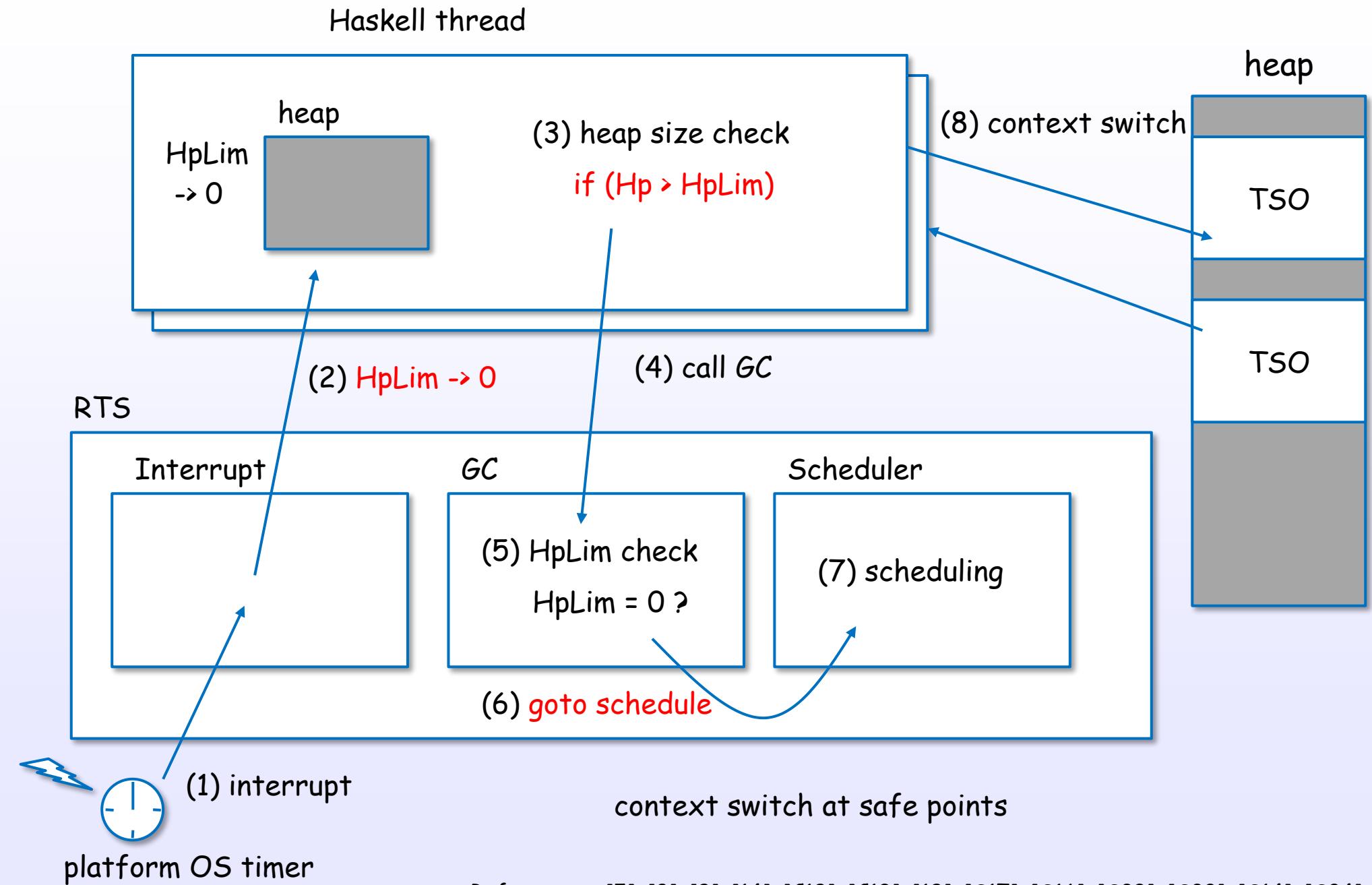
# Threads and TSOs



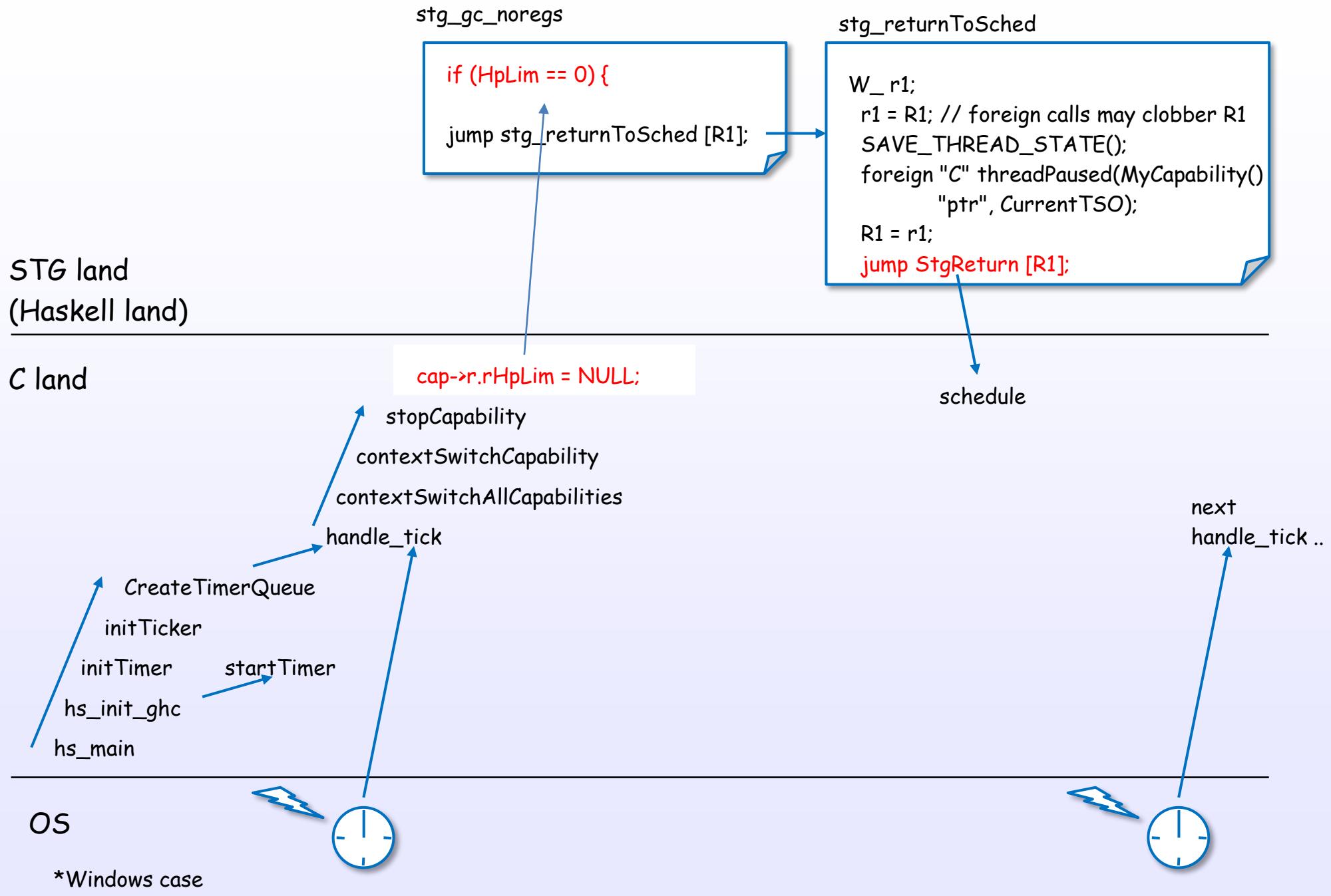
# Scheduling by run queue



# Context switch flow



# Context switch flow (code)



# Creating main and sub threads

# Create a main thread

Runtime  
System

Runtime system bootstrap code [rts/RtsAPI.c]

```
rts_evalLazyIO
  createIOThread
    createThread ... (1), (2), (3)
    pushClosure ... (4)
  scheduleWaitThread
    appendToRunQueue ... (5)
```

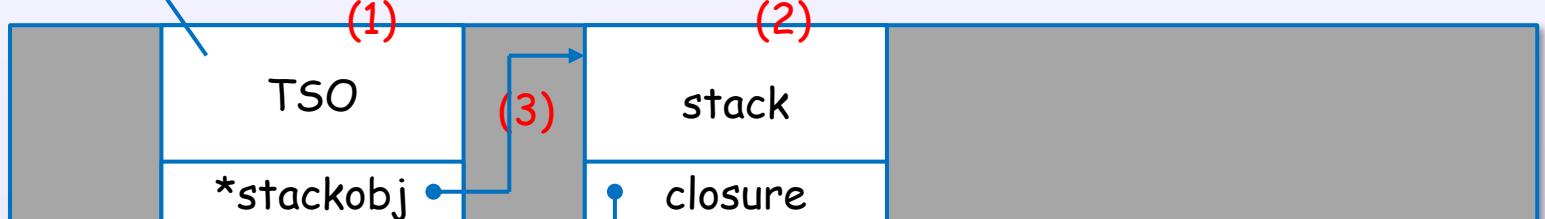
scheduler

run queue

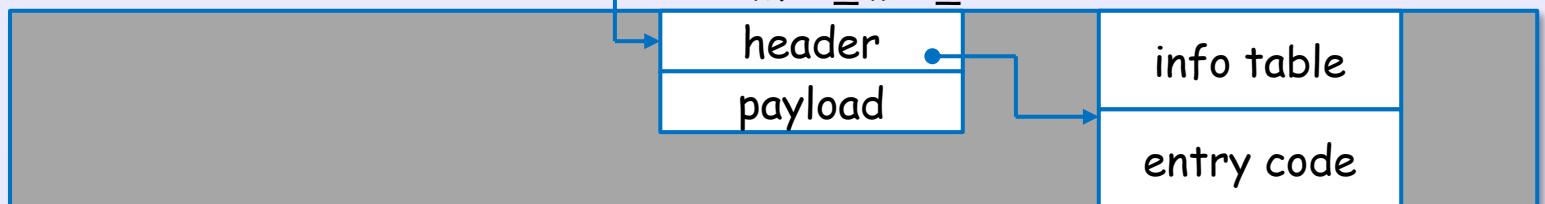


(5)

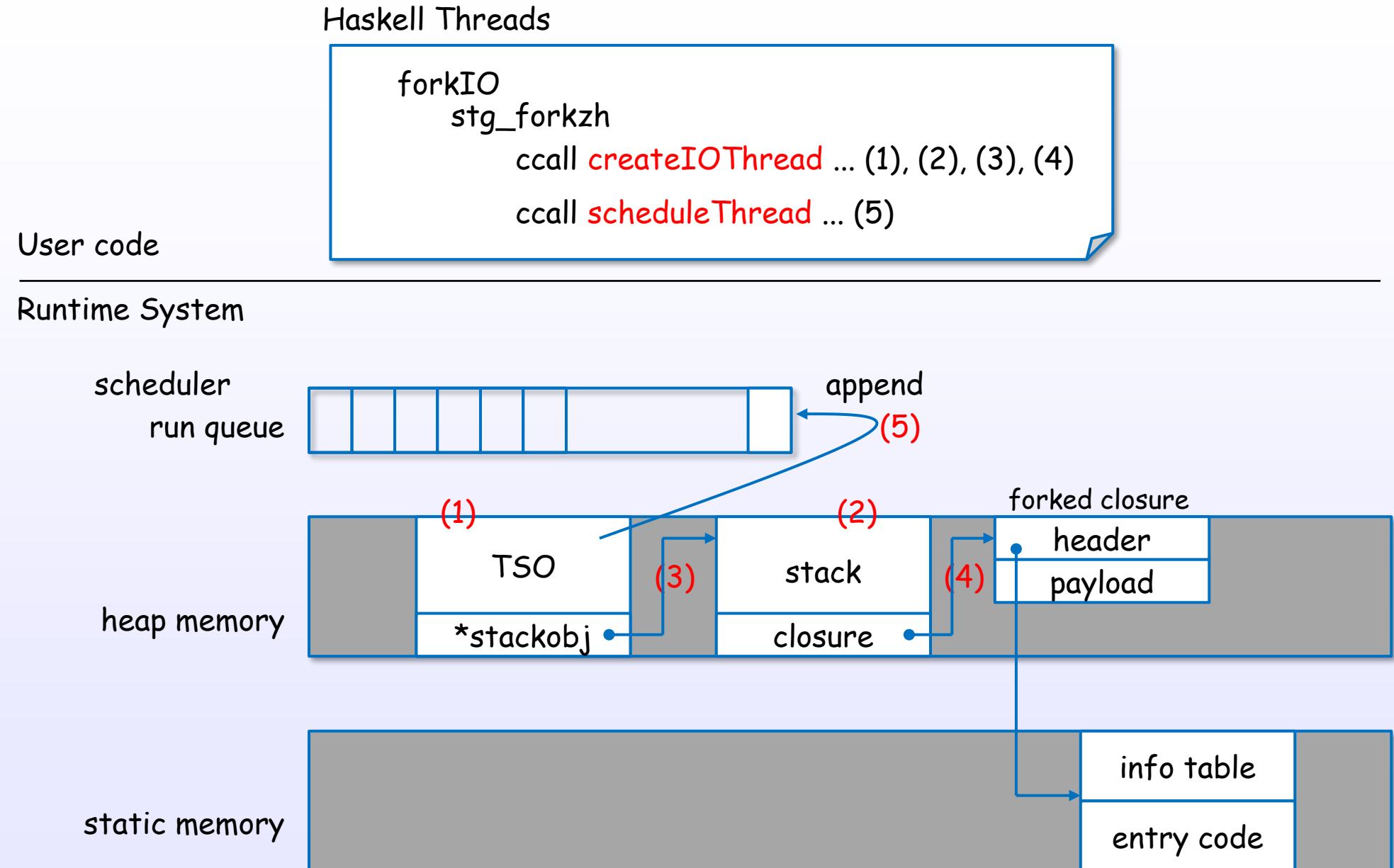
heap memory



static memory

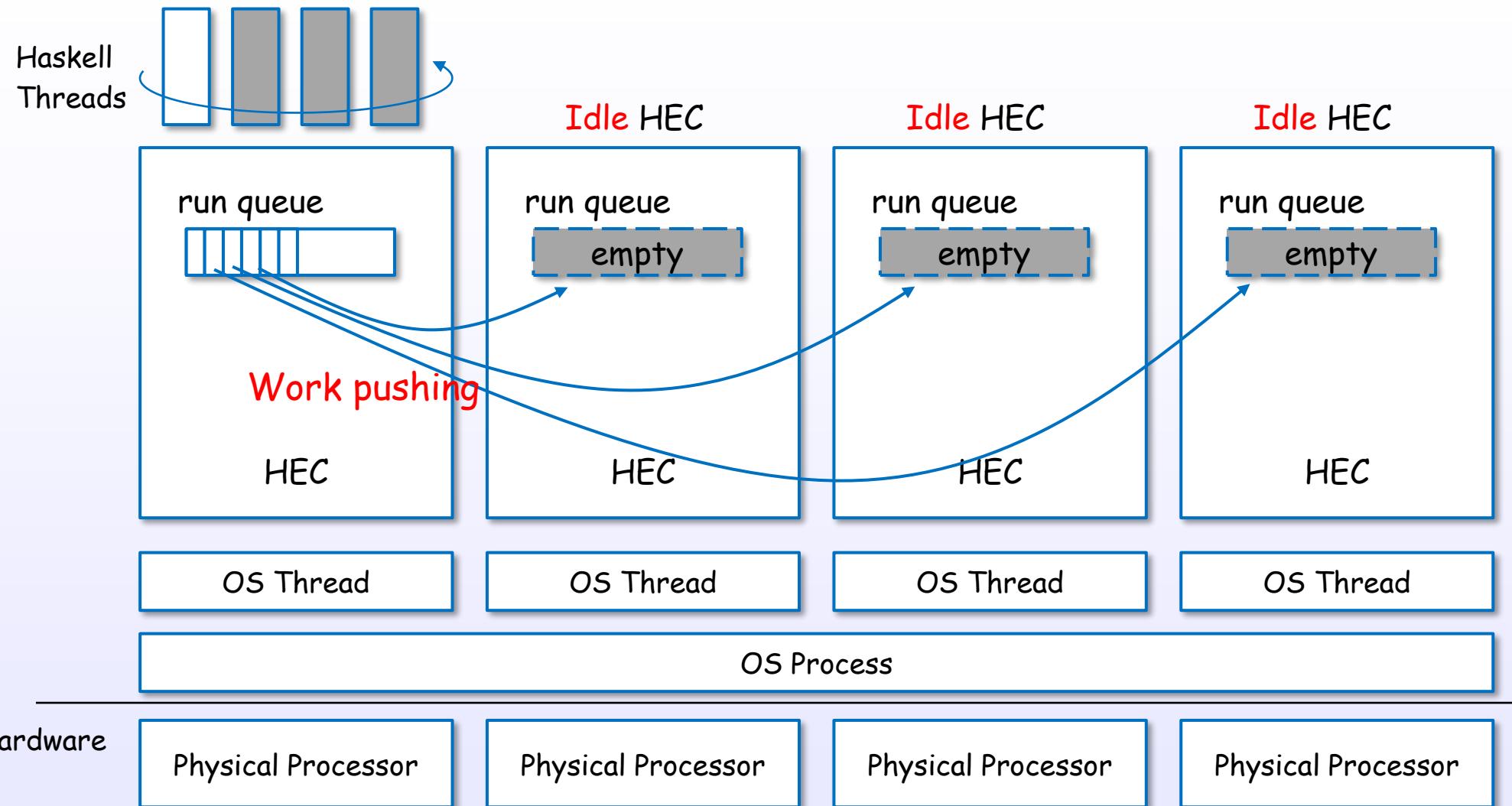


# Create a sub thread using forkIO



# Thread migration

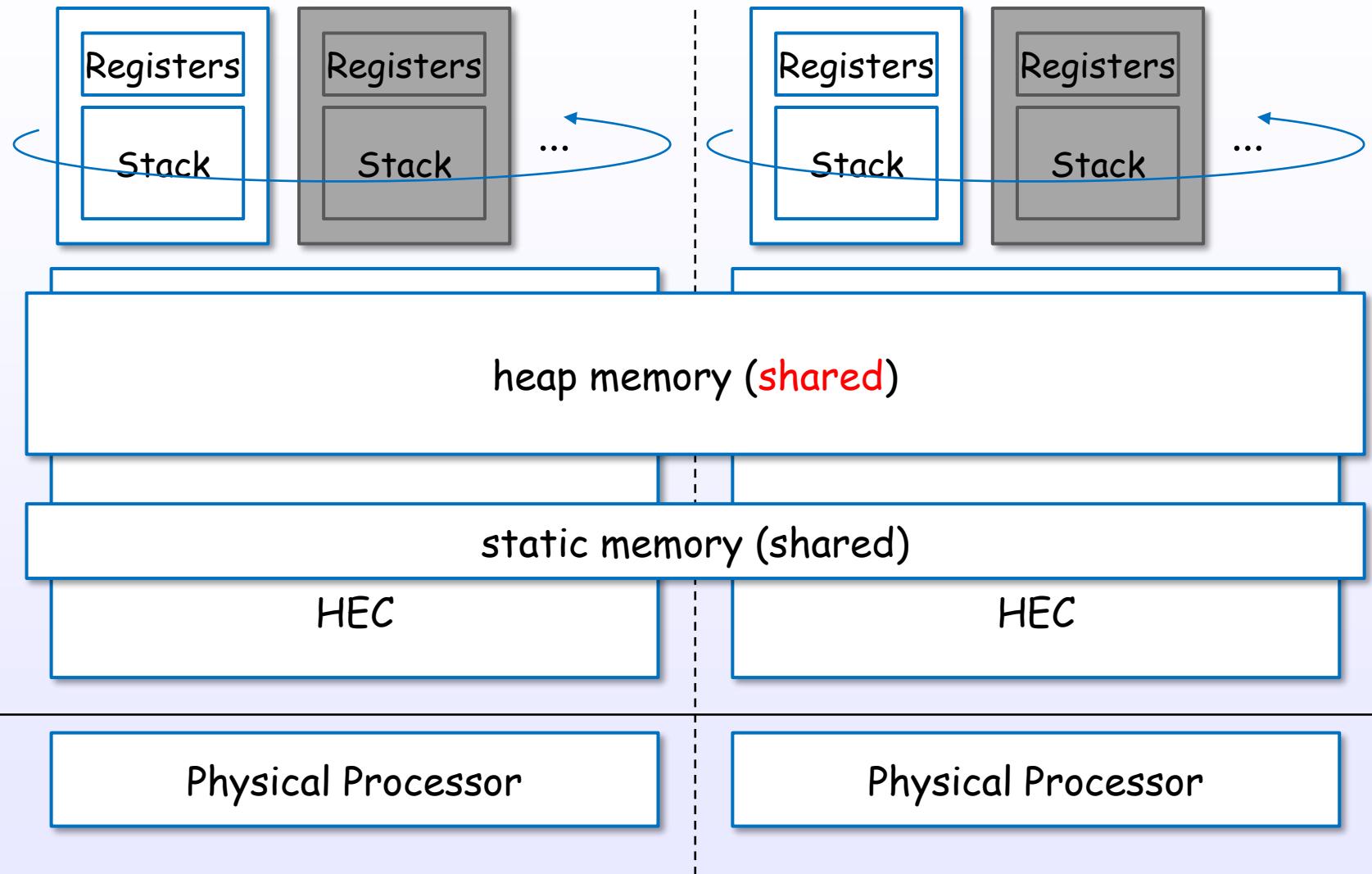
# Threads are migrated to idle HECs



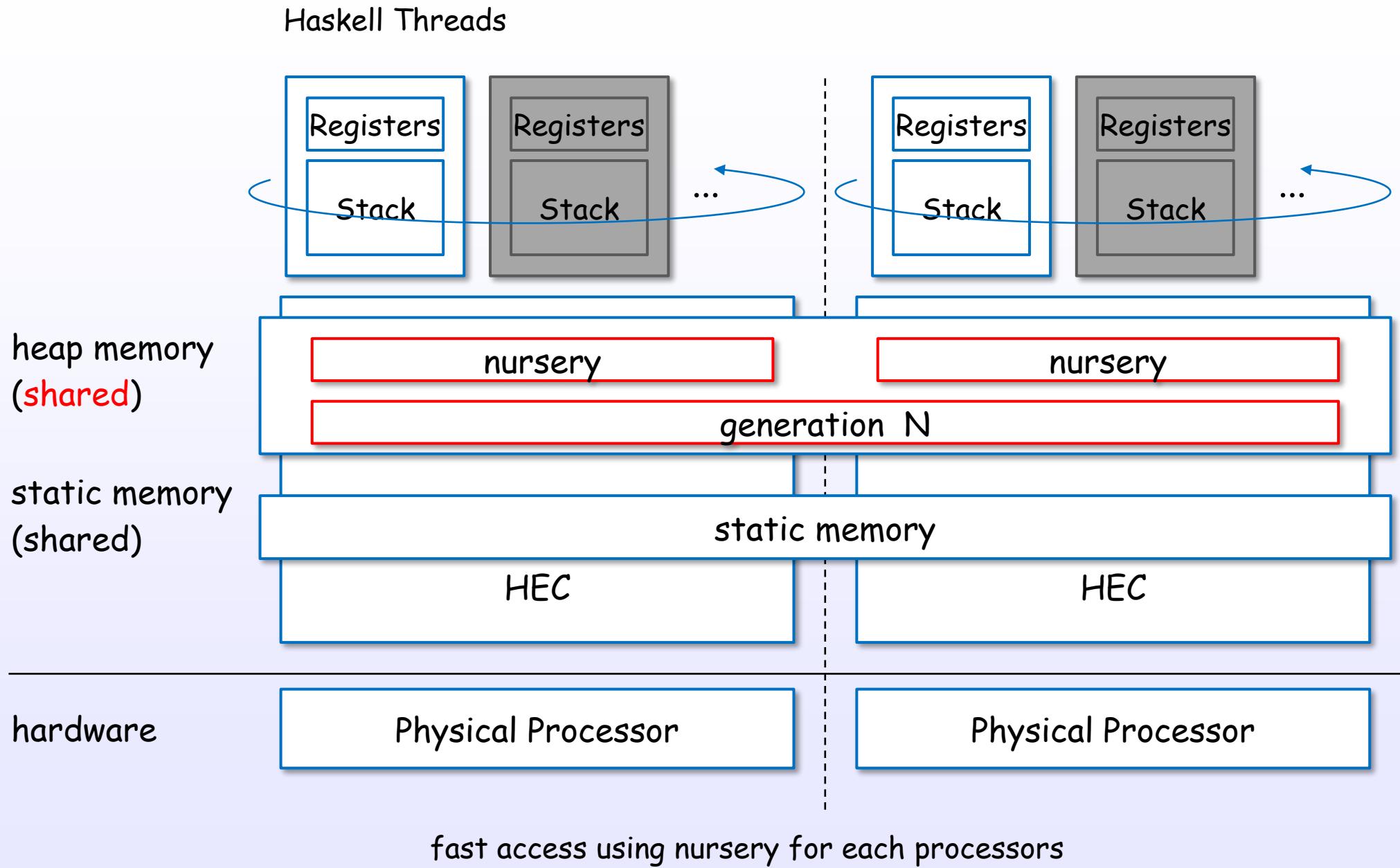
# Heap and Threads

# Threads share a heap

Haskell Threads

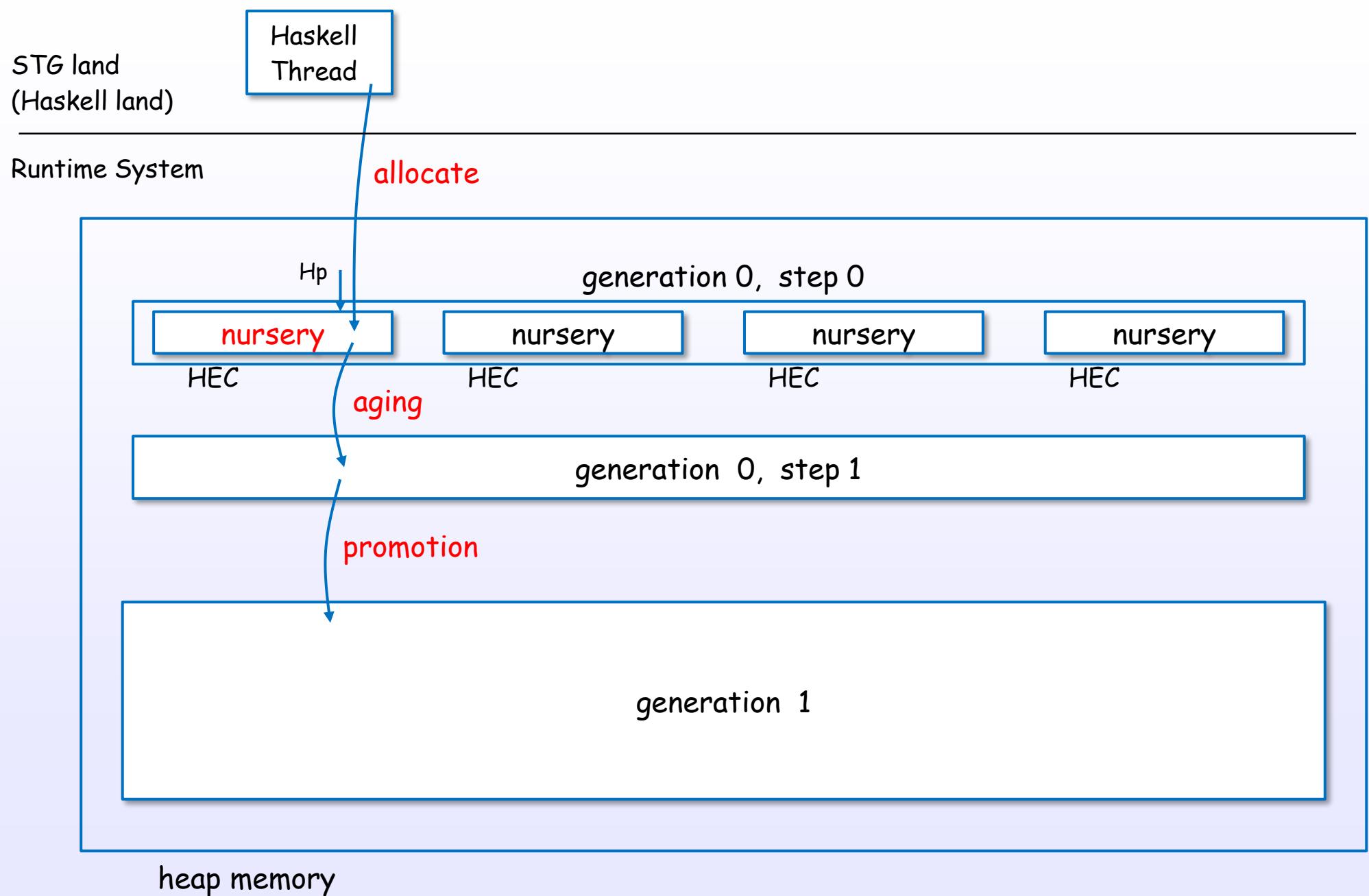


# Local allocation area (nursery)



# Threads and GC

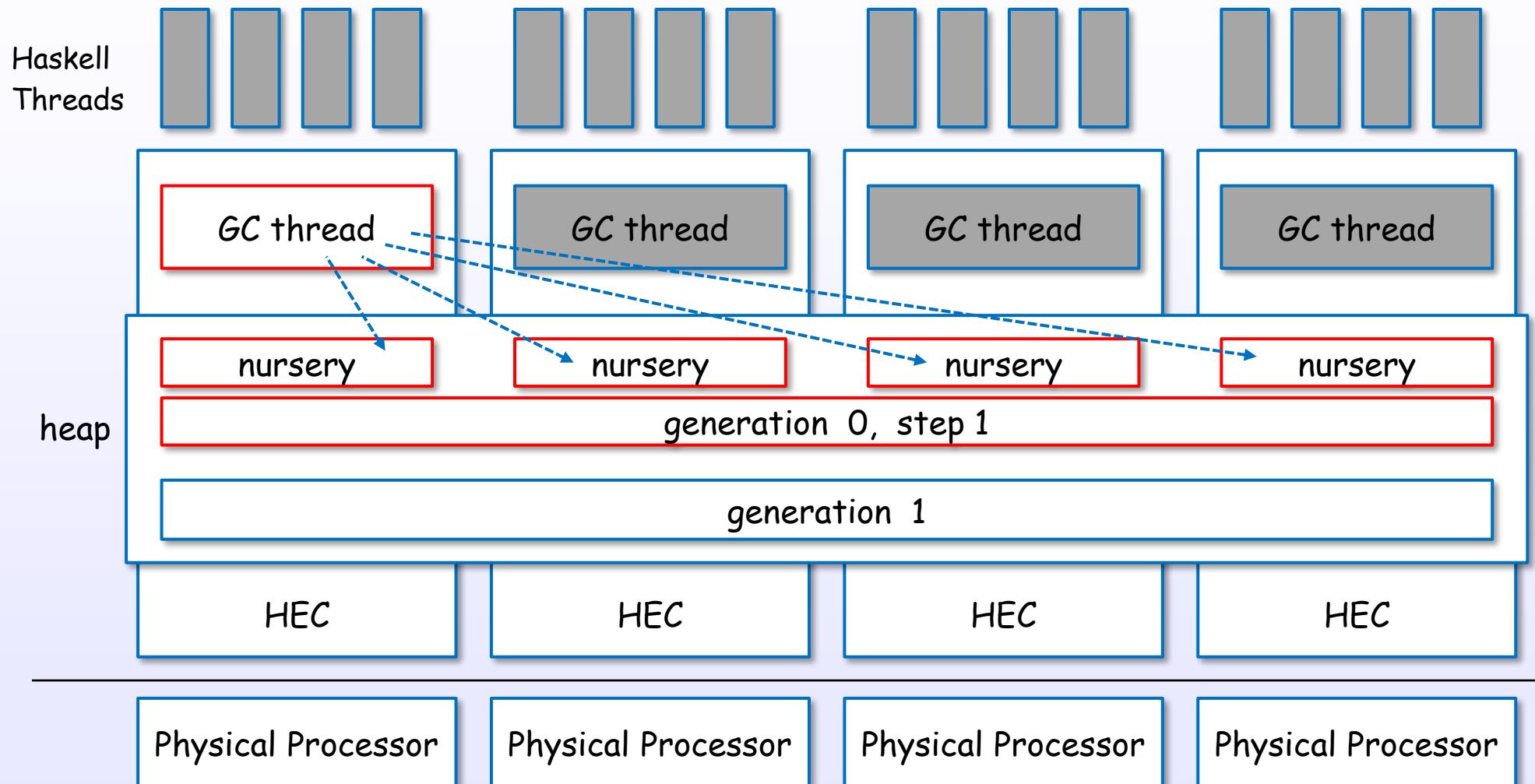
# GC, nursery, generation, aging, promotion



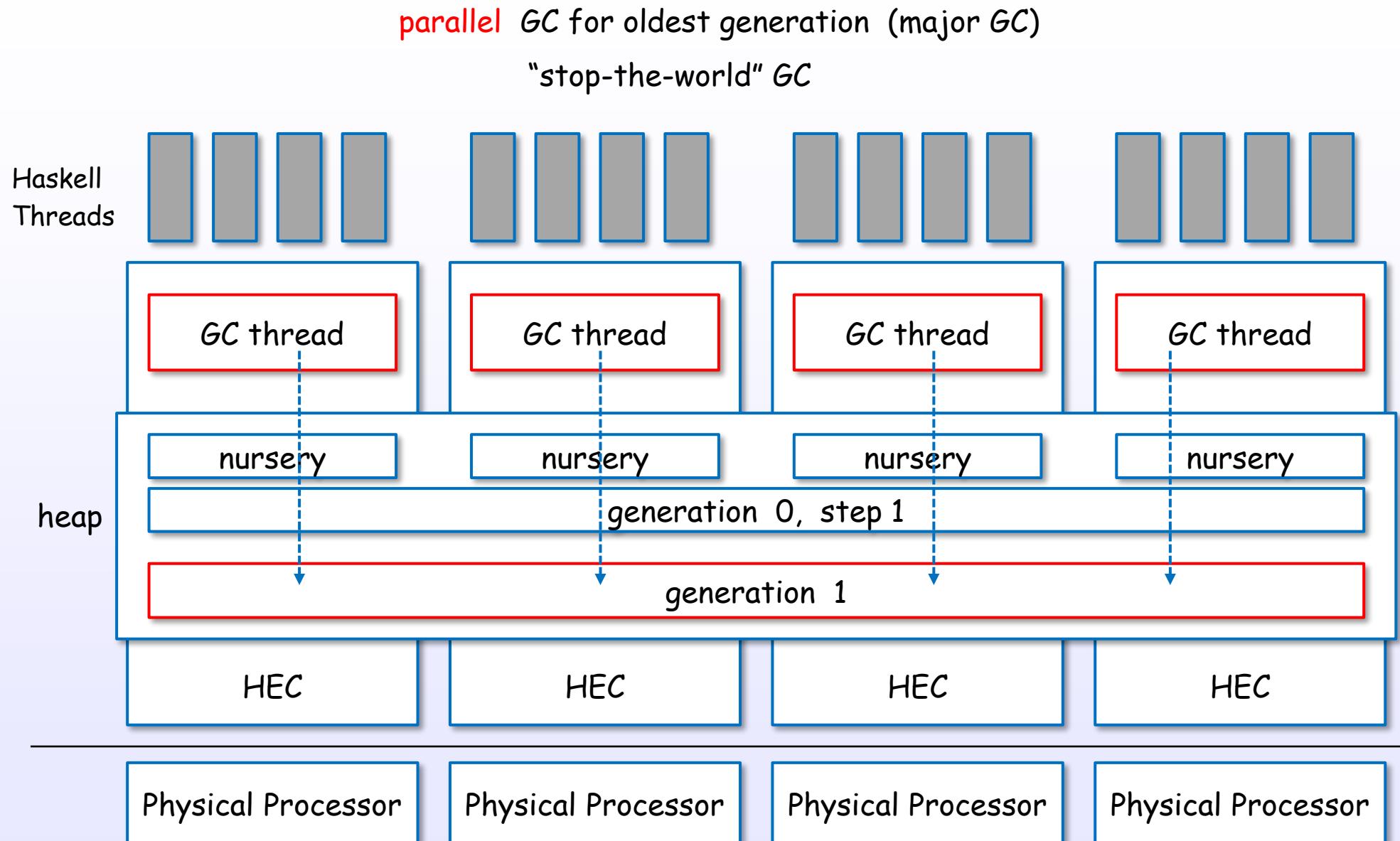
# Threads and minor GC

sequential GC for young generation (minor GC)

"stop-the-world" GC

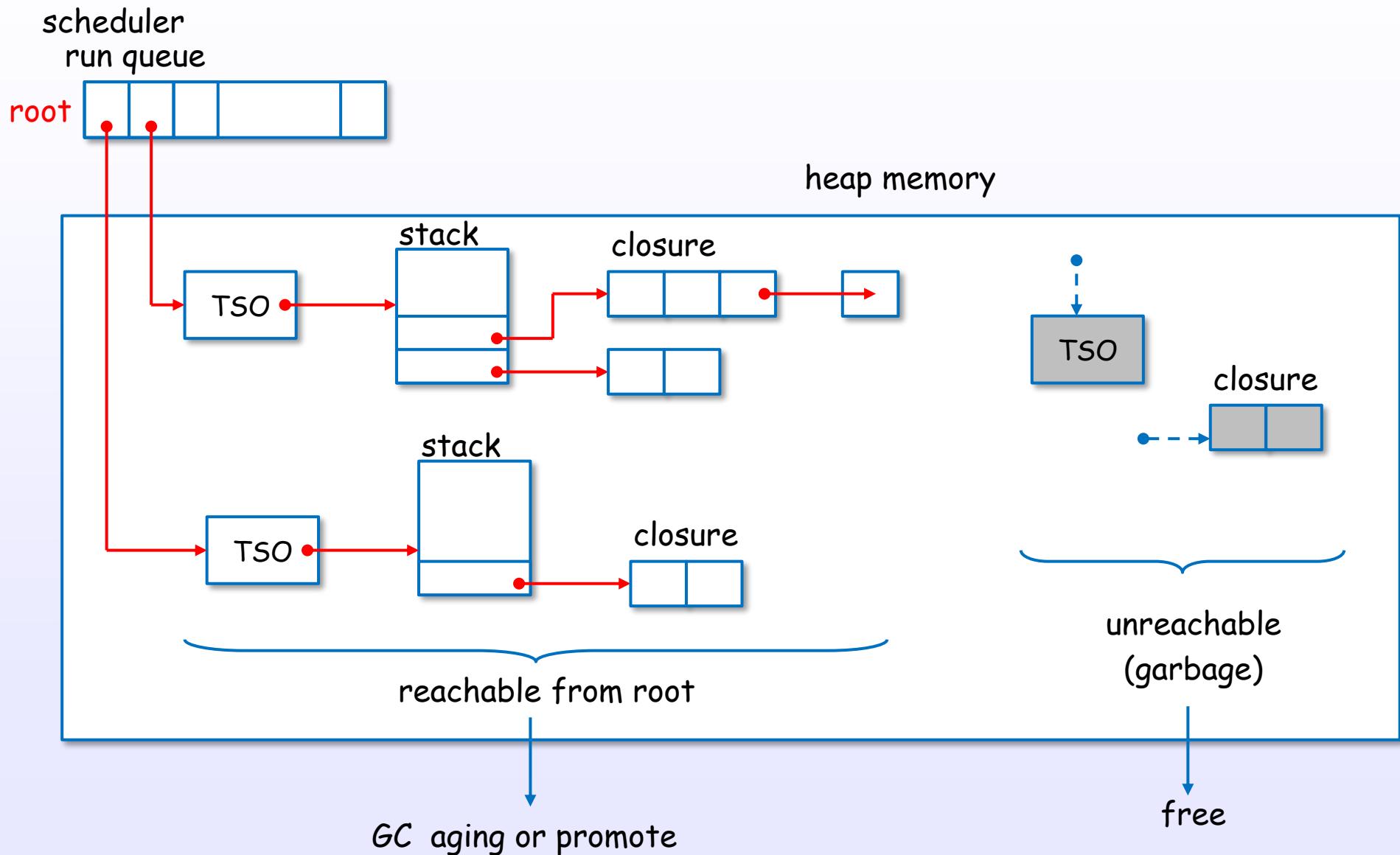


# Threads and major GC



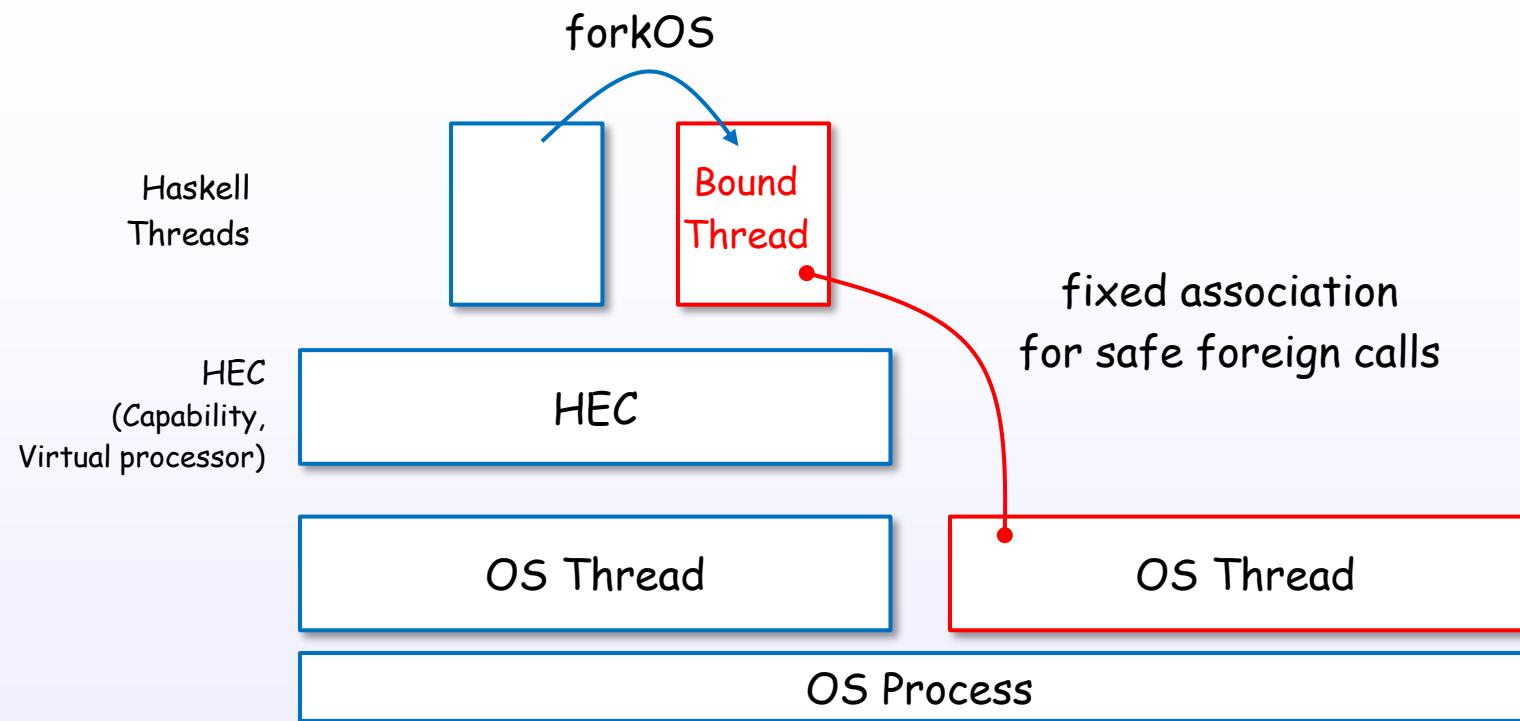
# GC discovers live objects from the root

## Runtime System



Bound thread

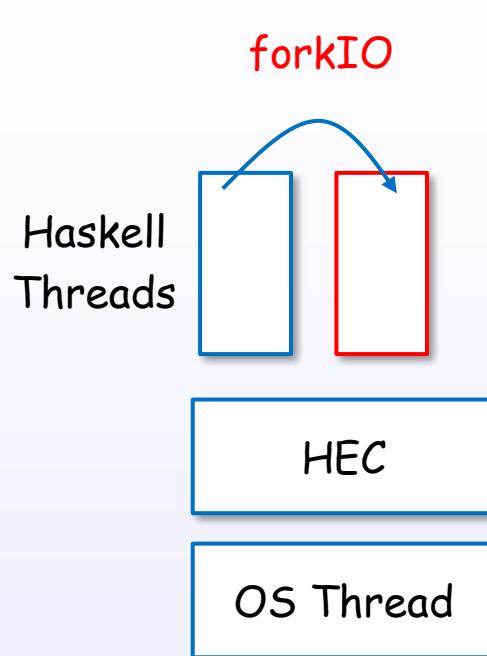
# A bound thread has a fixed associated OS Thread



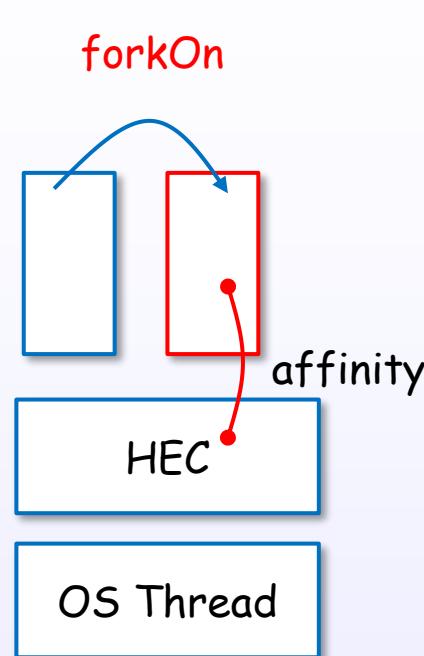
Foreign calls from a bound thread are all made by the same OS thread.  
A bound thread is created using forkOS.

The main thread is bound thread.

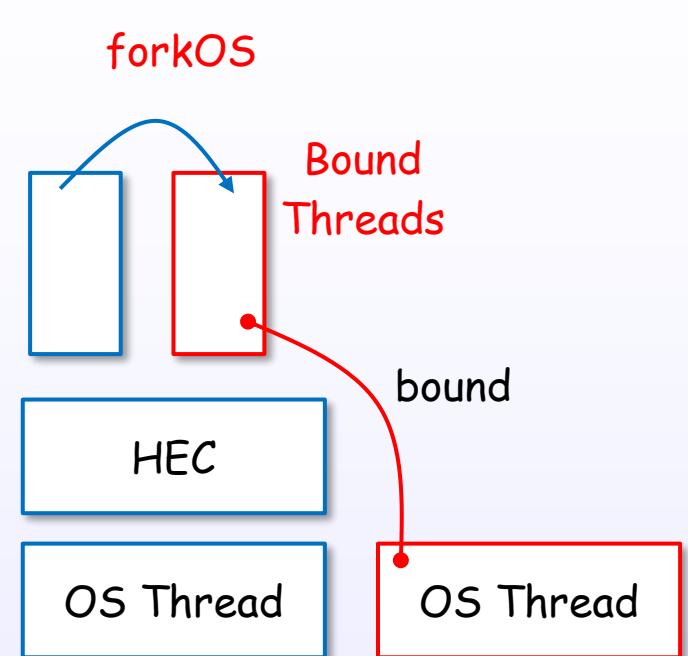
# forkIO, forkOn, forkOS



create a haskell unbound  
thread



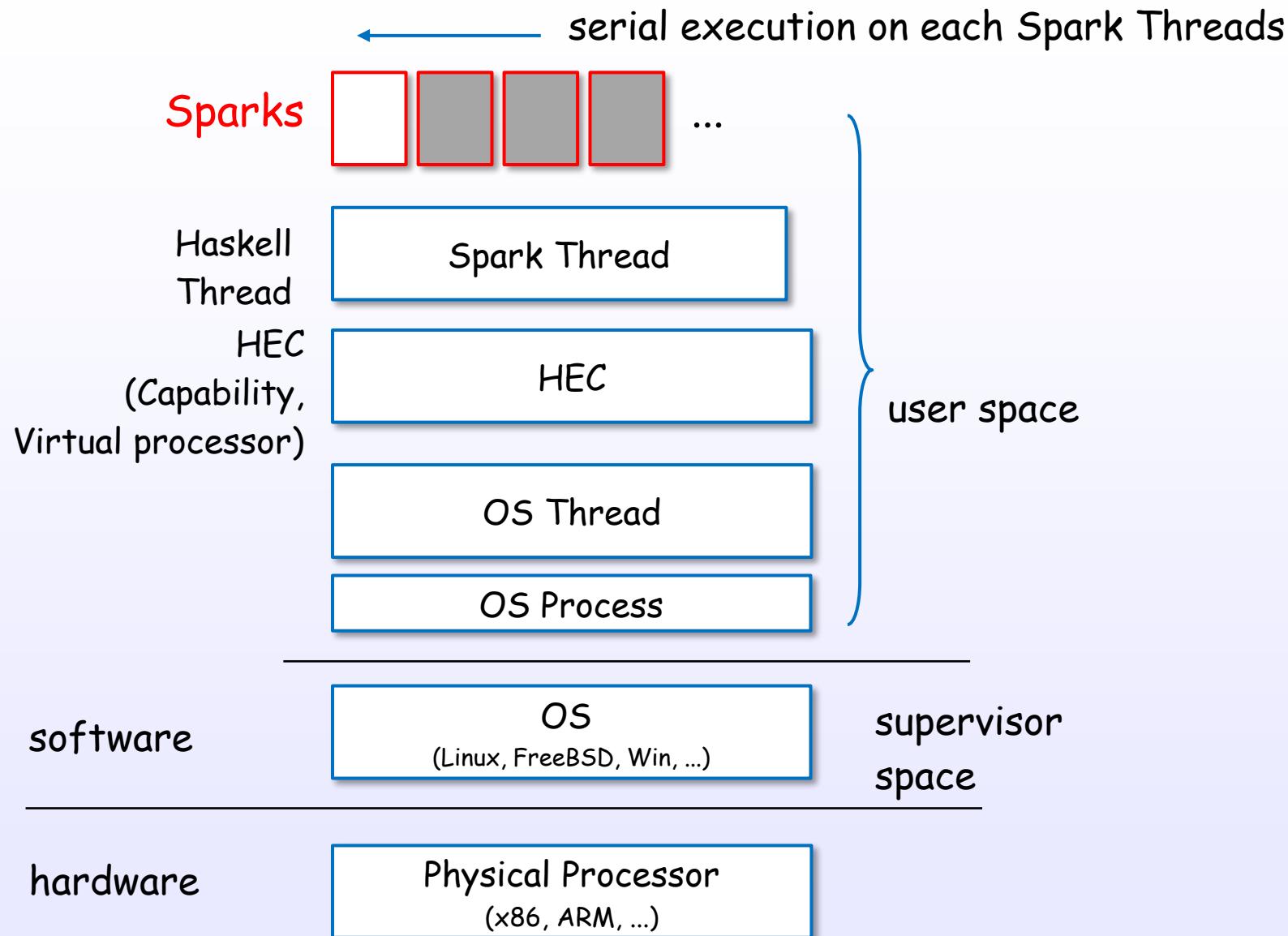
create a haskell unbound  
thread  
on the specified HEC



create a haskell **bound** thread  
and an OS thread

Spark

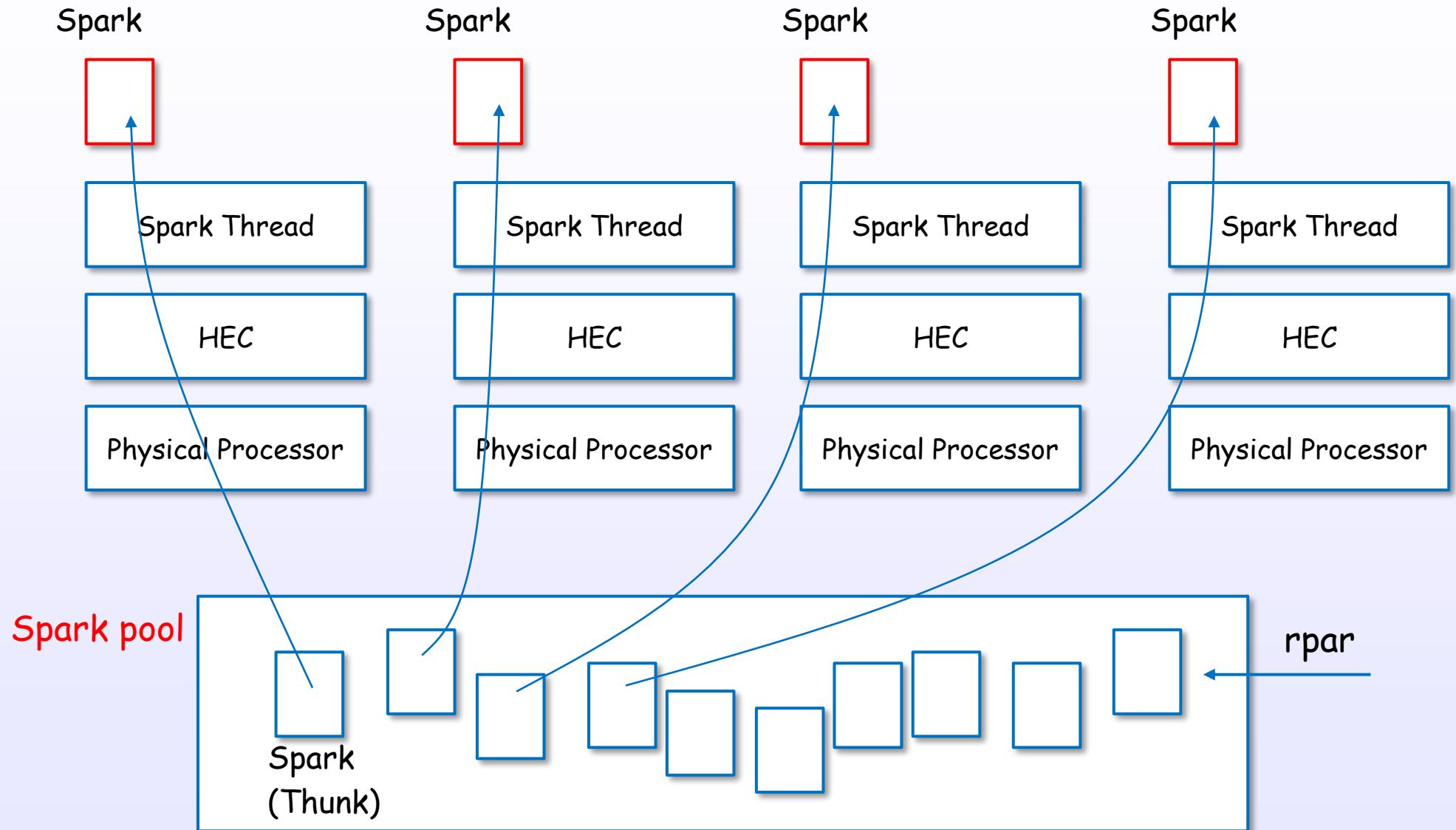
# Spark layer



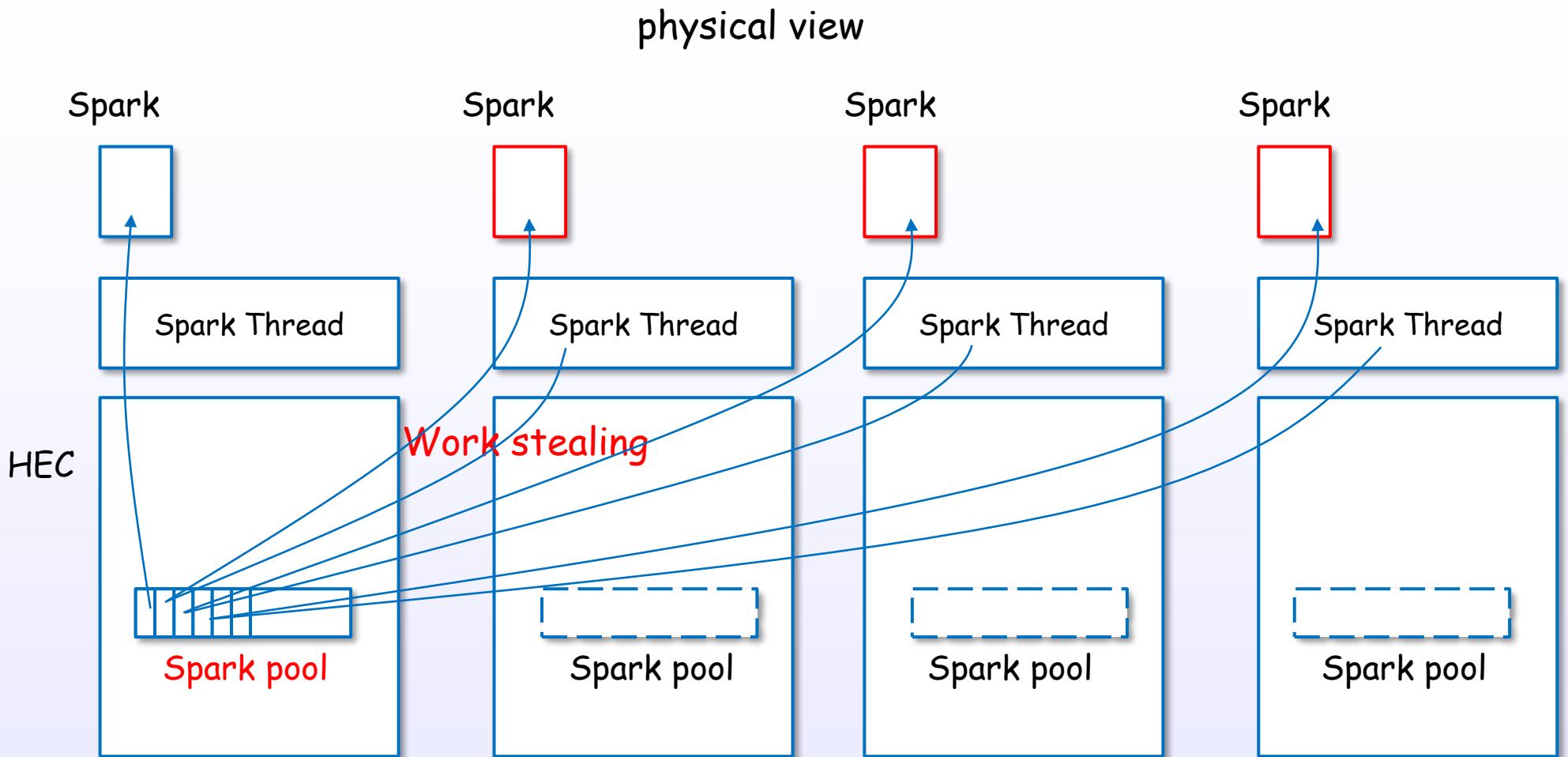
Spark Threads are generated on idle HECs.

# Sparks and Spark pool

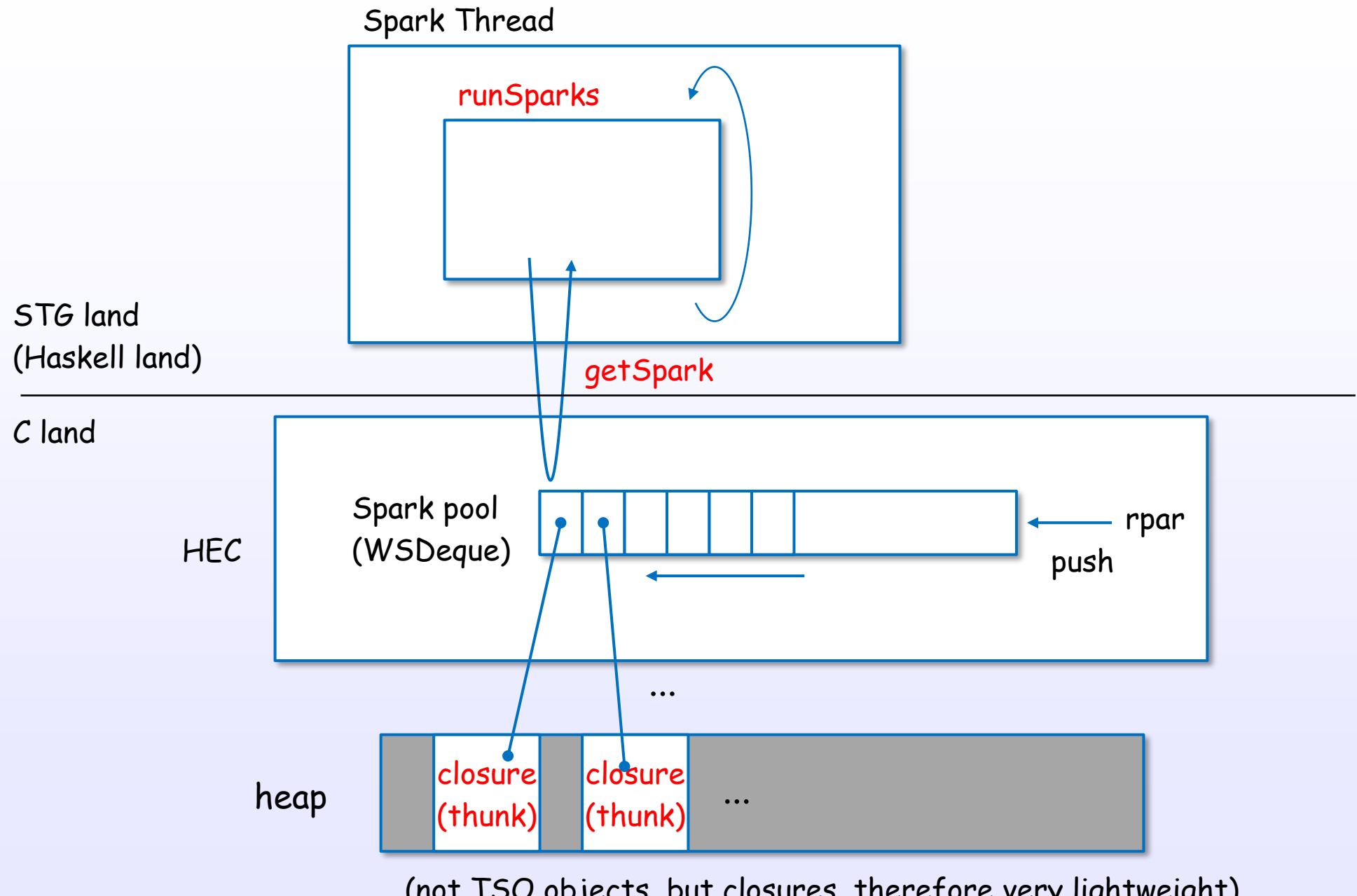
logical view



# Spark pool and work stealing



# Sparks and closures



MVar

# MVar

Haskell Thread #0

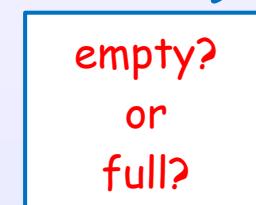


putMVar

Haskell Thread #1



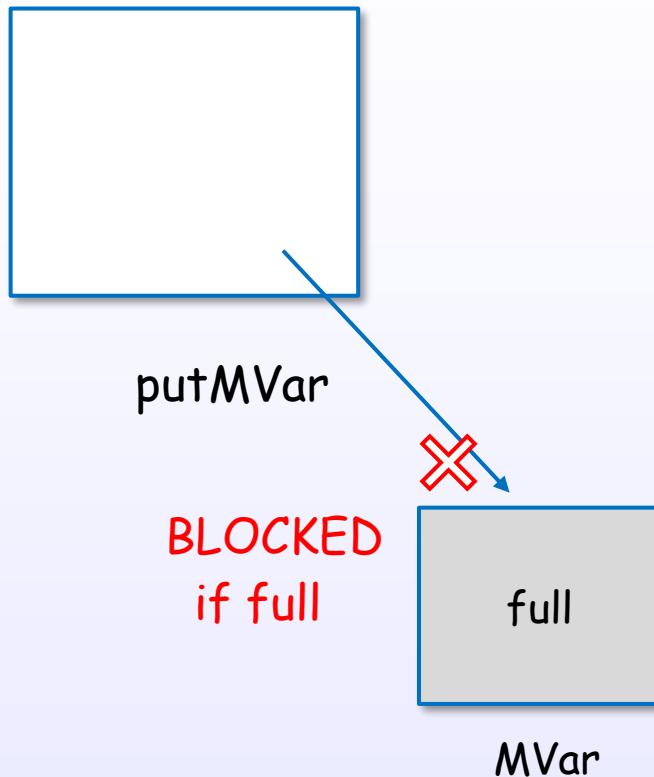
takeMVar



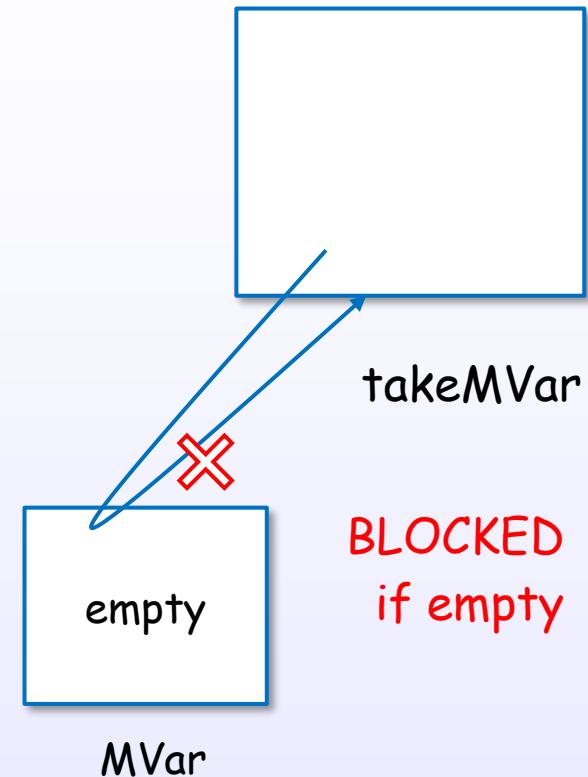
MVar

# MVar and blocking

Haskell Thread

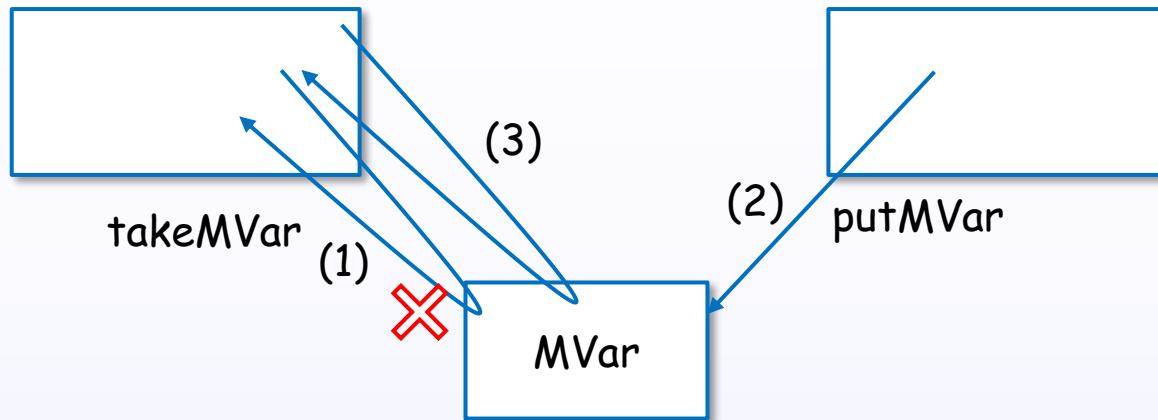


Haskell Thread

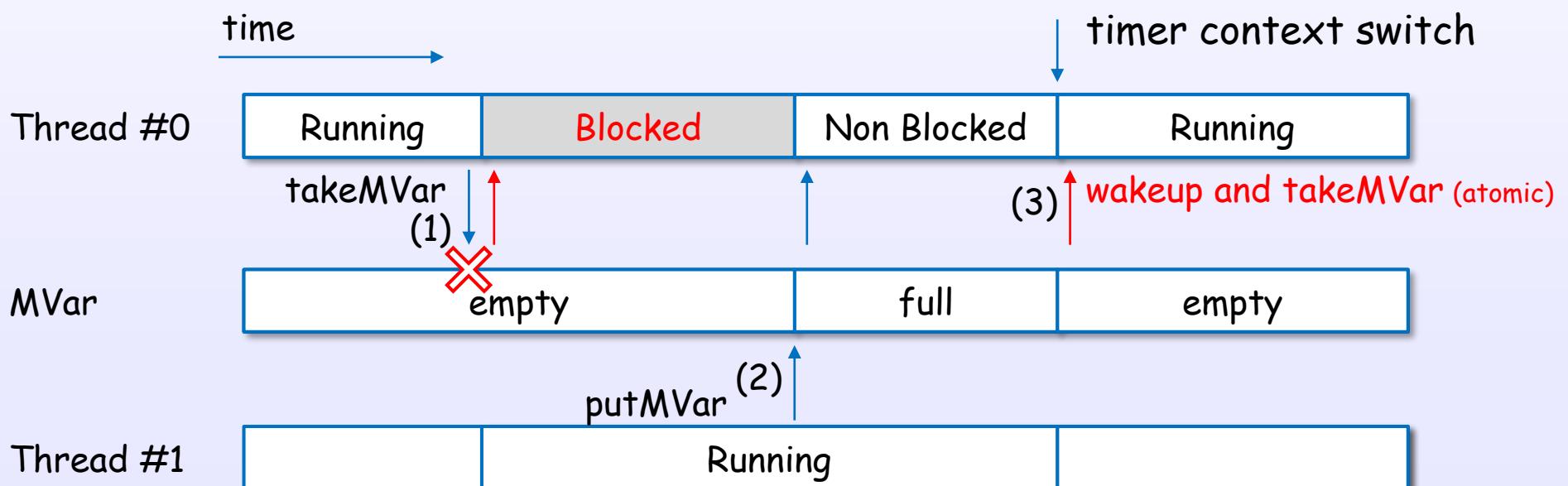


# MVar example

Haskell Thread #0



Haskell Thread #1



\* single core case

References : [16], [18], [19], [S32], [S12]

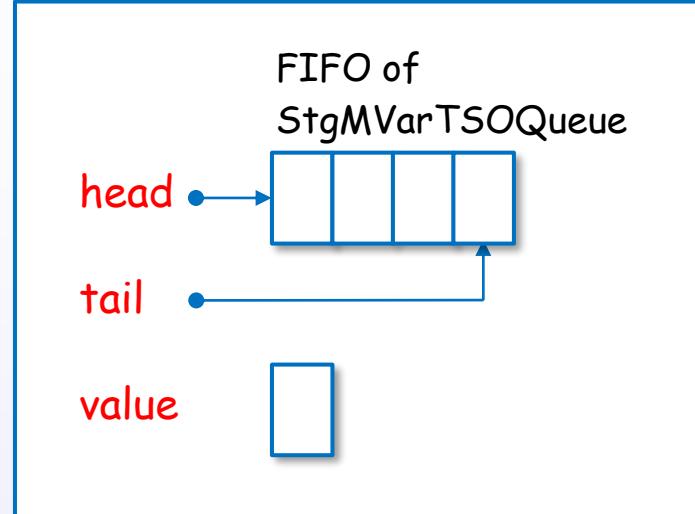
# MVar object view

User view

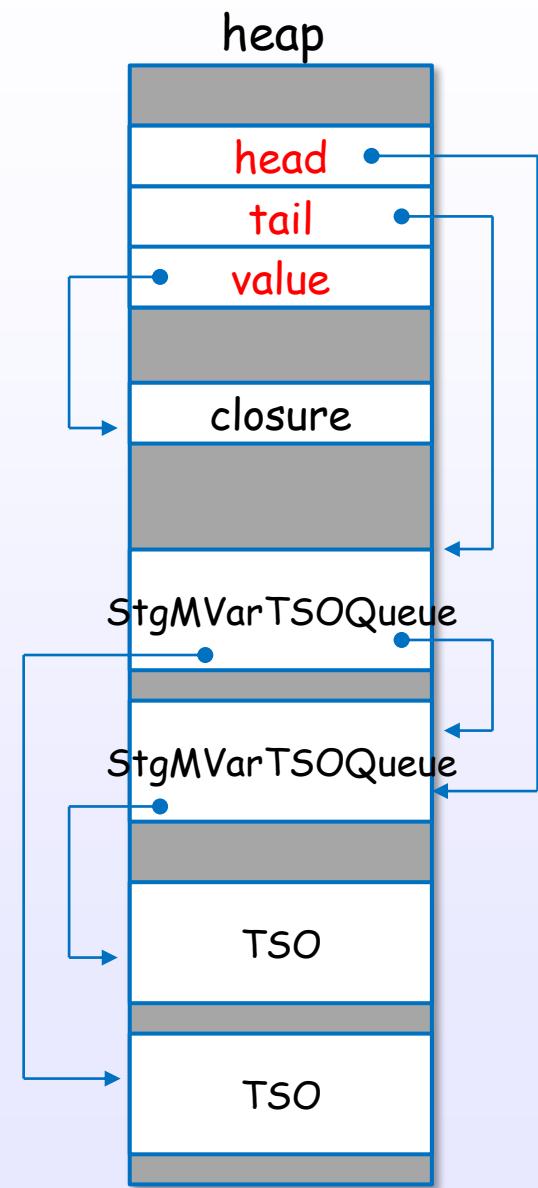
MVar

empty?  
or  
full?

logical MVar object



physical MVar object



References : [16], [18], [19], [S32], [S12]

# newEmptyMVar

Haskell Threads

newEmptyMVar  
newMVar#

(1) call the Runtime primitive

Runtime System

stg\_newMVarzh  
ALLOC\_PRIM\_  
SET\_HDR  
StgMVar\_head  
StgMVar\_tail  
StgMVar\_value

(2) create a MVar object in the heap

MVar object

stg\_END\_TSO\_QUEUE\_closure

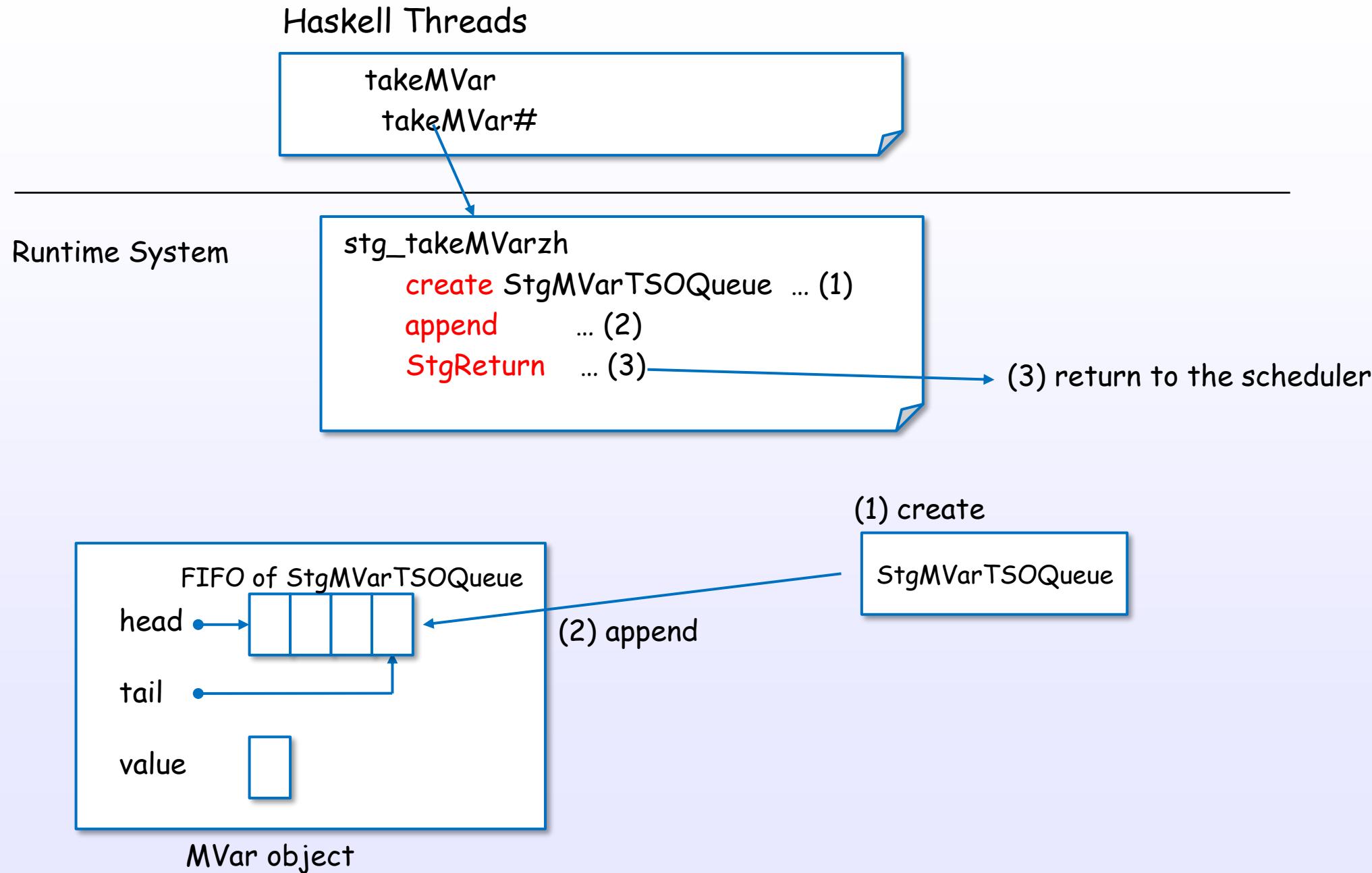
heap

head  
tail  
value

(3) link each fields

References : [16], [18], [19], [S32], [S12]

# takeMVar (empty case)



# takeMVar (full case)

## Haskell Threads

takeMVar  
takeMVar#

## Runtime System

stg\_takeMVarzh

- (1) **get** value
- (2) **set** empty
- (3) **remove head**
- (4) **tryWakeupThread**

head

(3) remove head



tail

value

- (1) get value
- (2) set empty

MVar object

scheduler

run queue

fairness round robin



append

(4) wakeup

Only one of the blocked threads becomes unblocked.

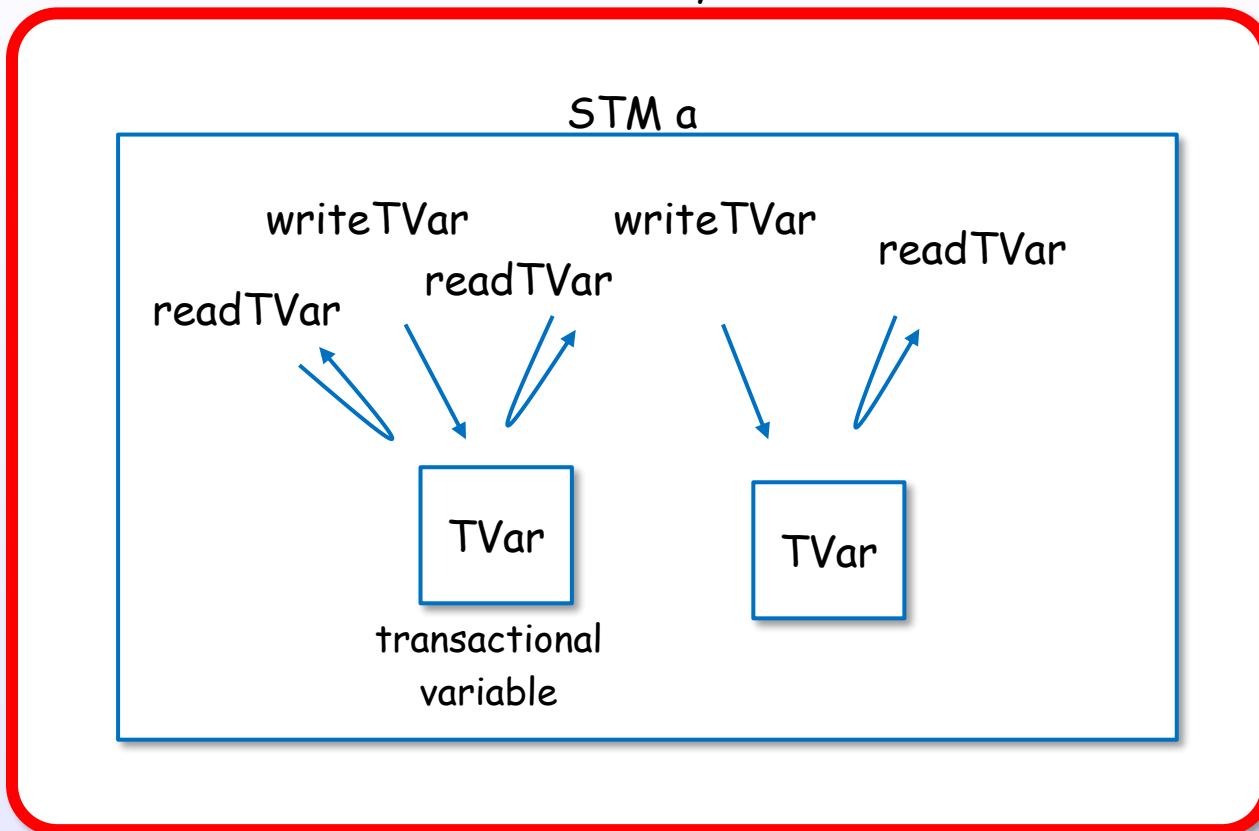
References : [16], [18], [19], [S32], [S12]

# Software transactional memory

# Create a atomic block using atomically

atomically :: STM a -> IO a

atomically

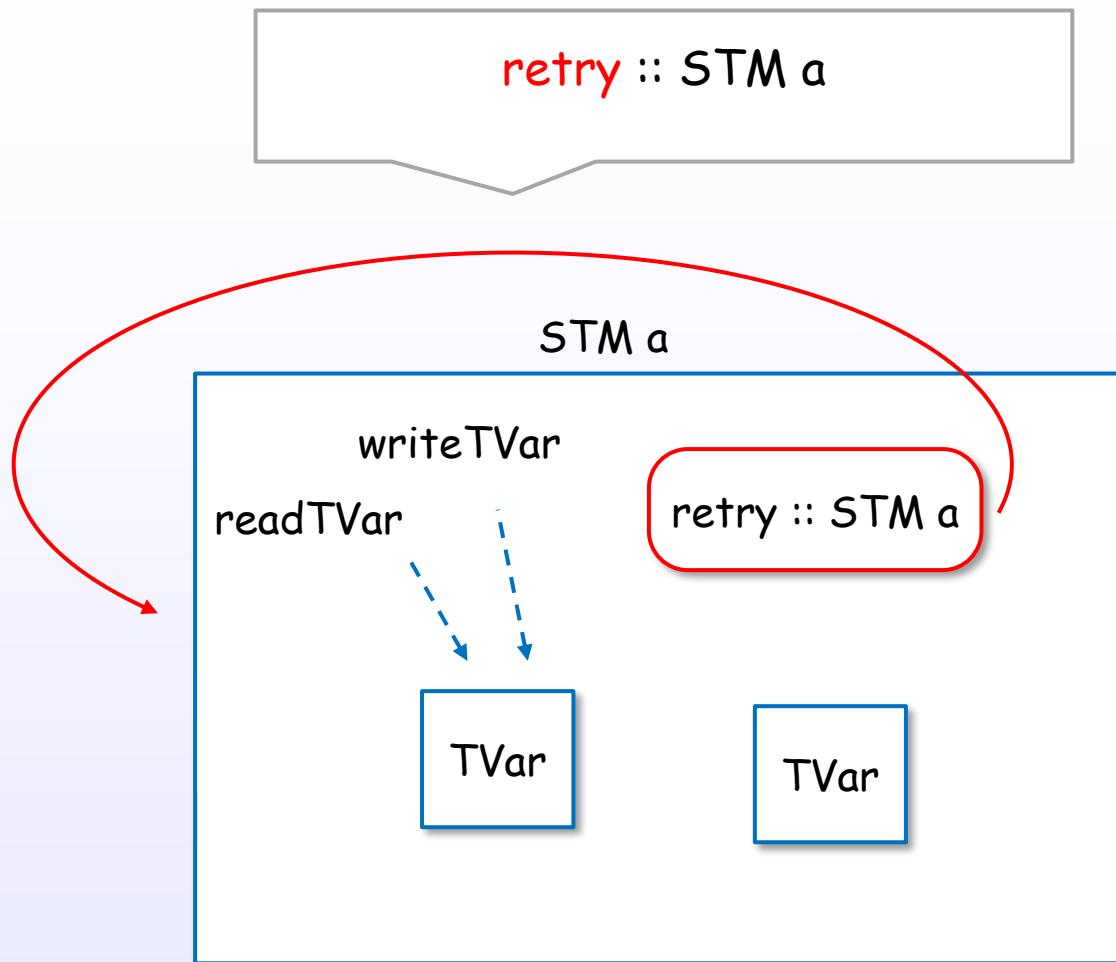


Create and evaluate a **composable “atomic block”**

Atomic block = All or Nothing

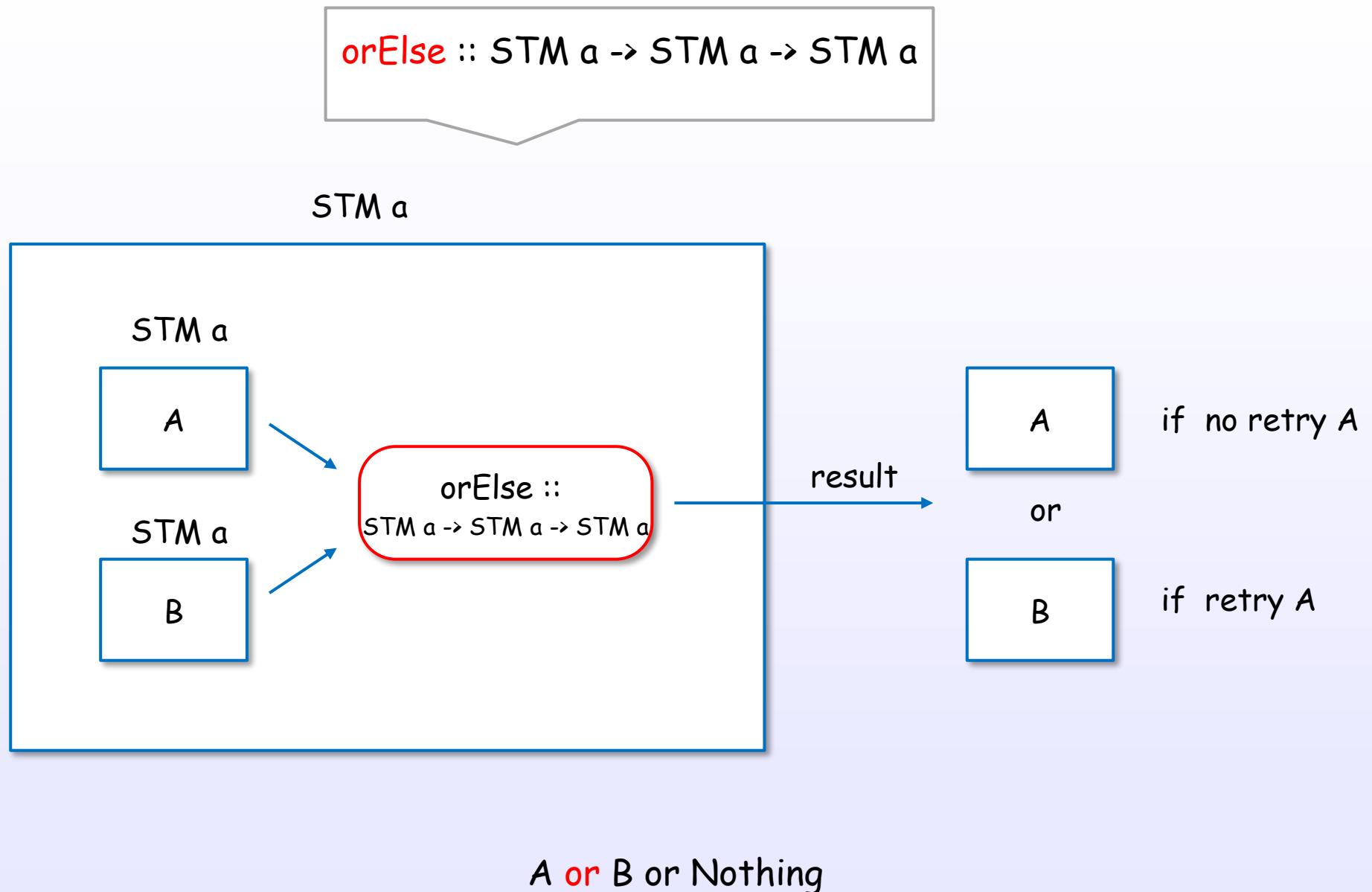
References : [17], [19], [20], [C20], [S12], [S28]

# Rollback and blocking control using retry

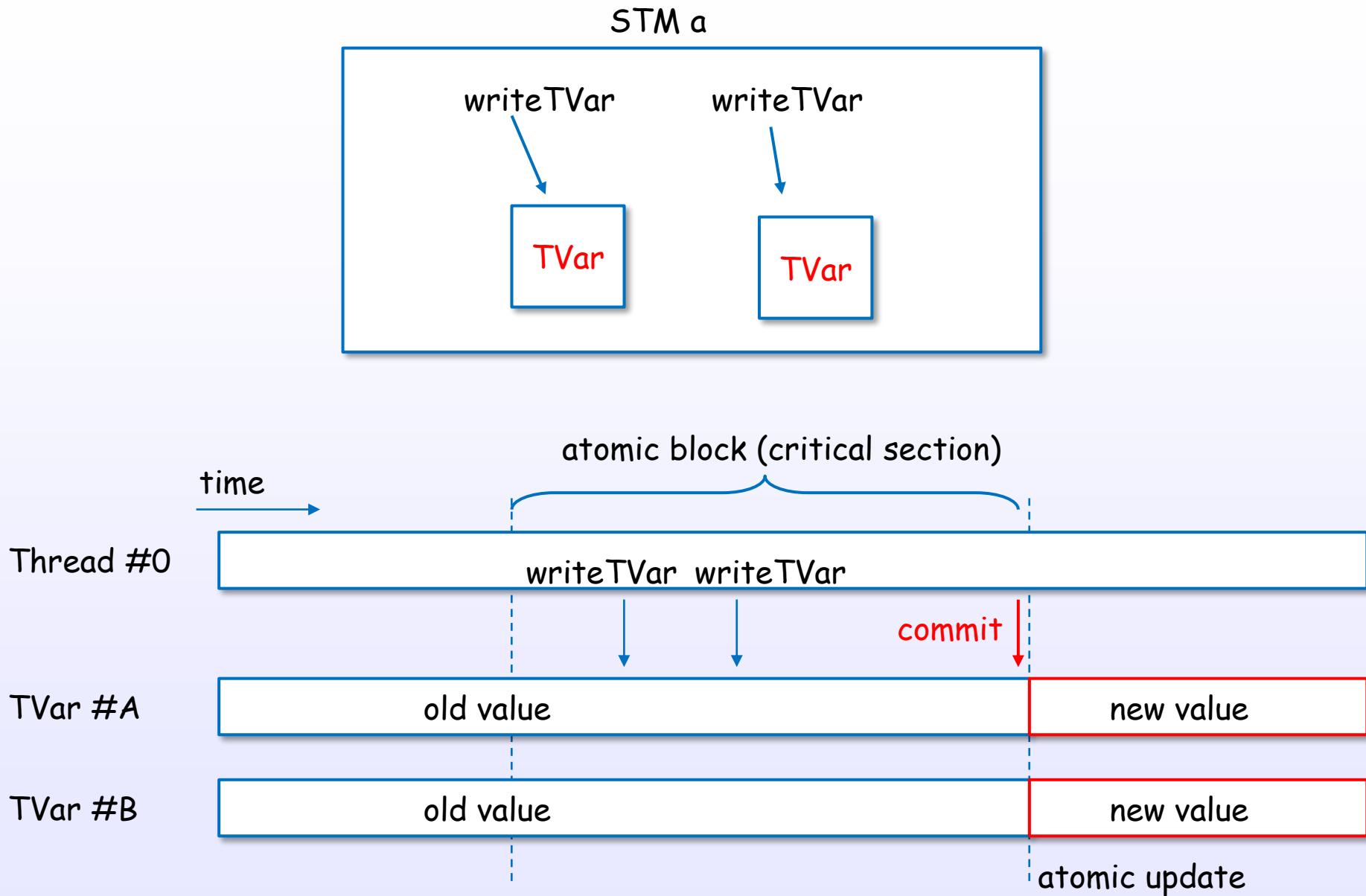


Discard, blocking and try again

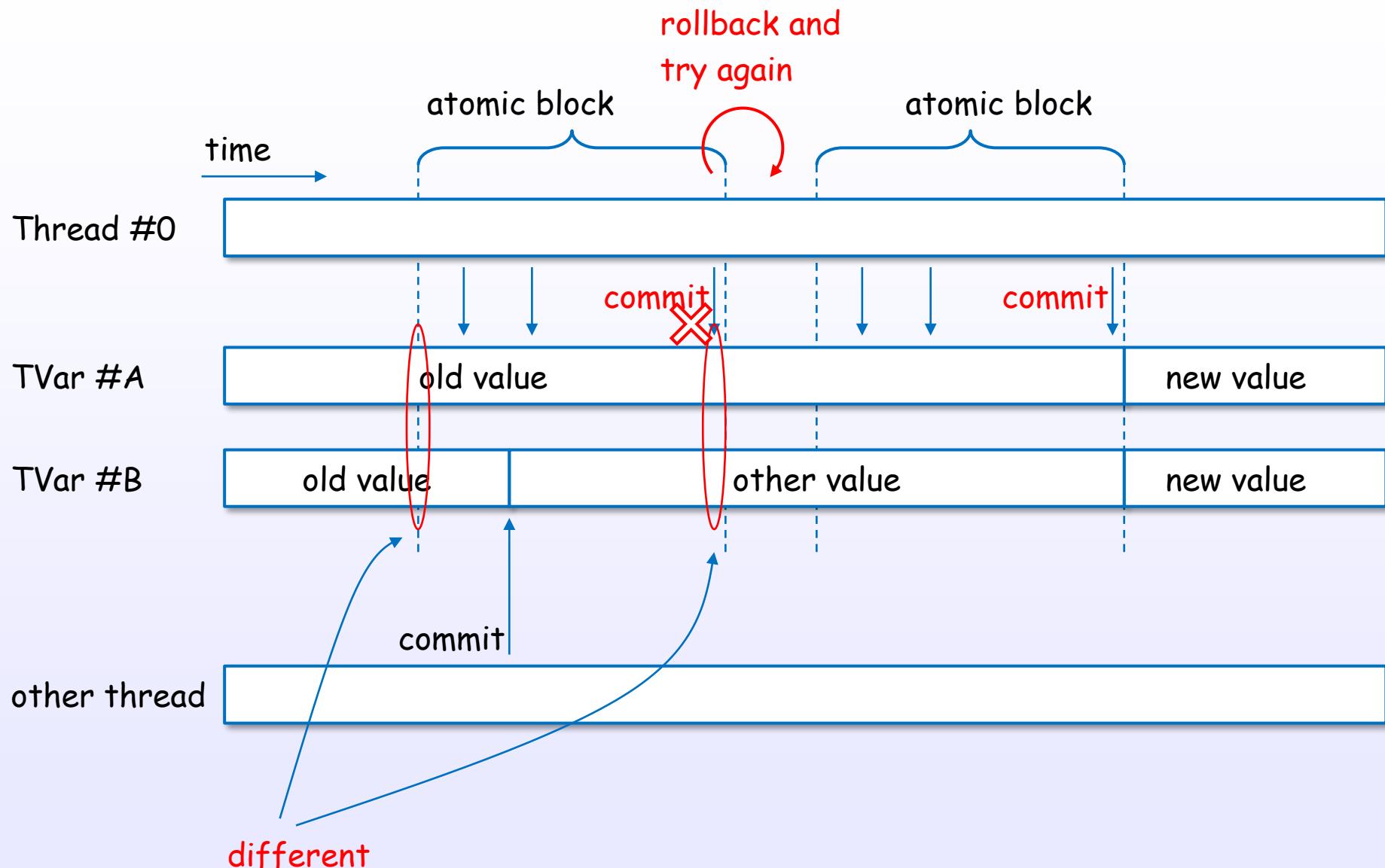
# Compose OR case using orElse



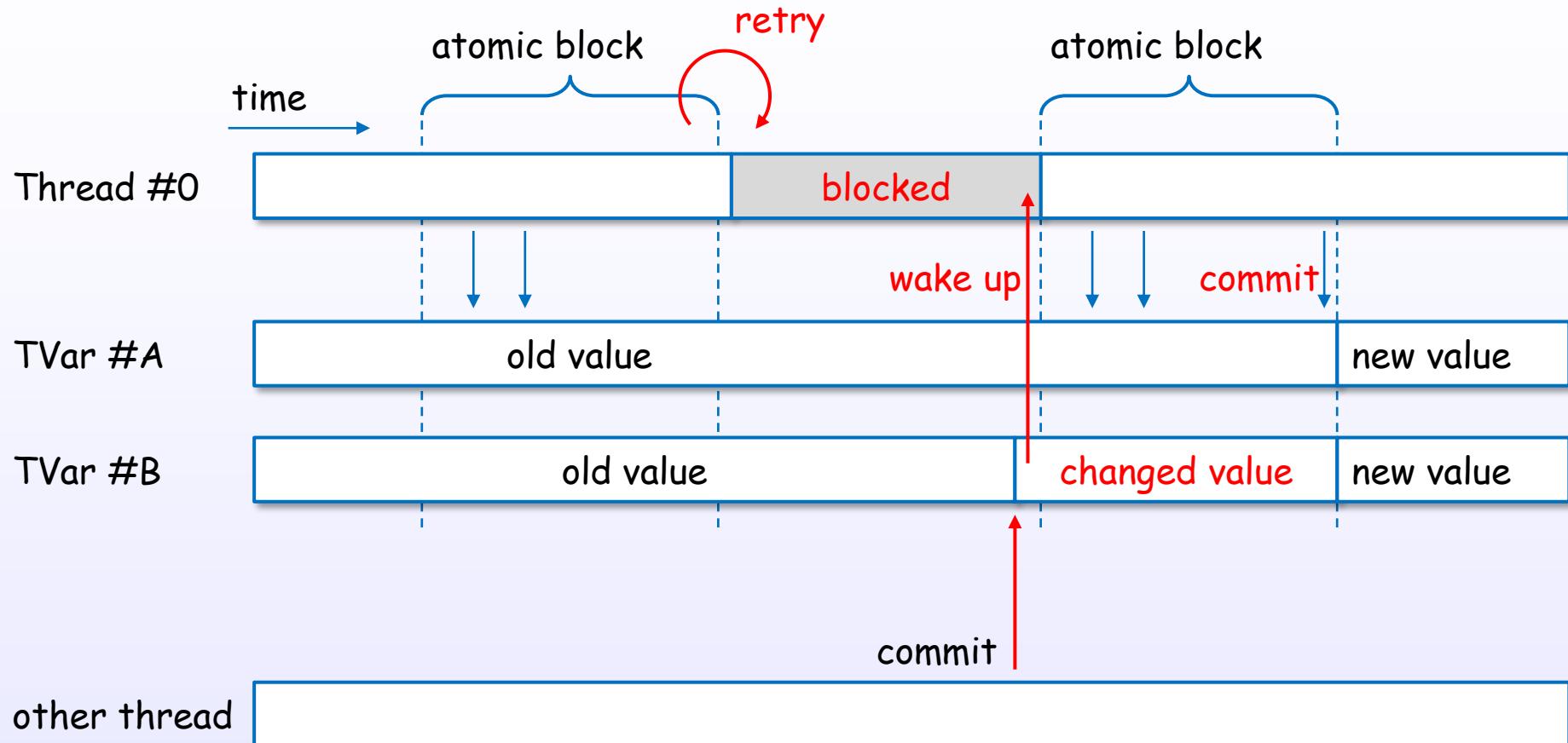
# STM, TVar example (normal case)



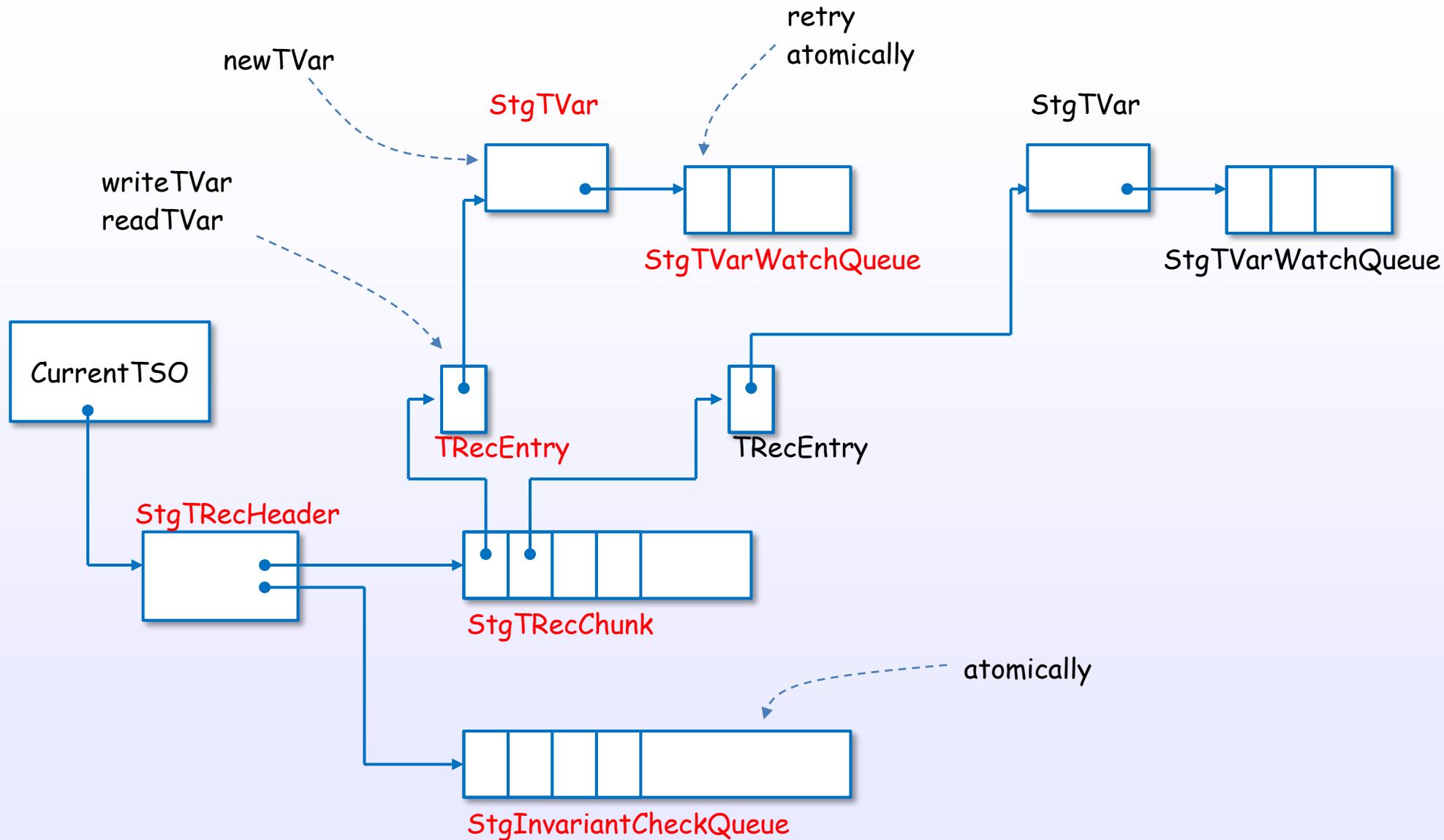
# STM, TVar example (conflict case)



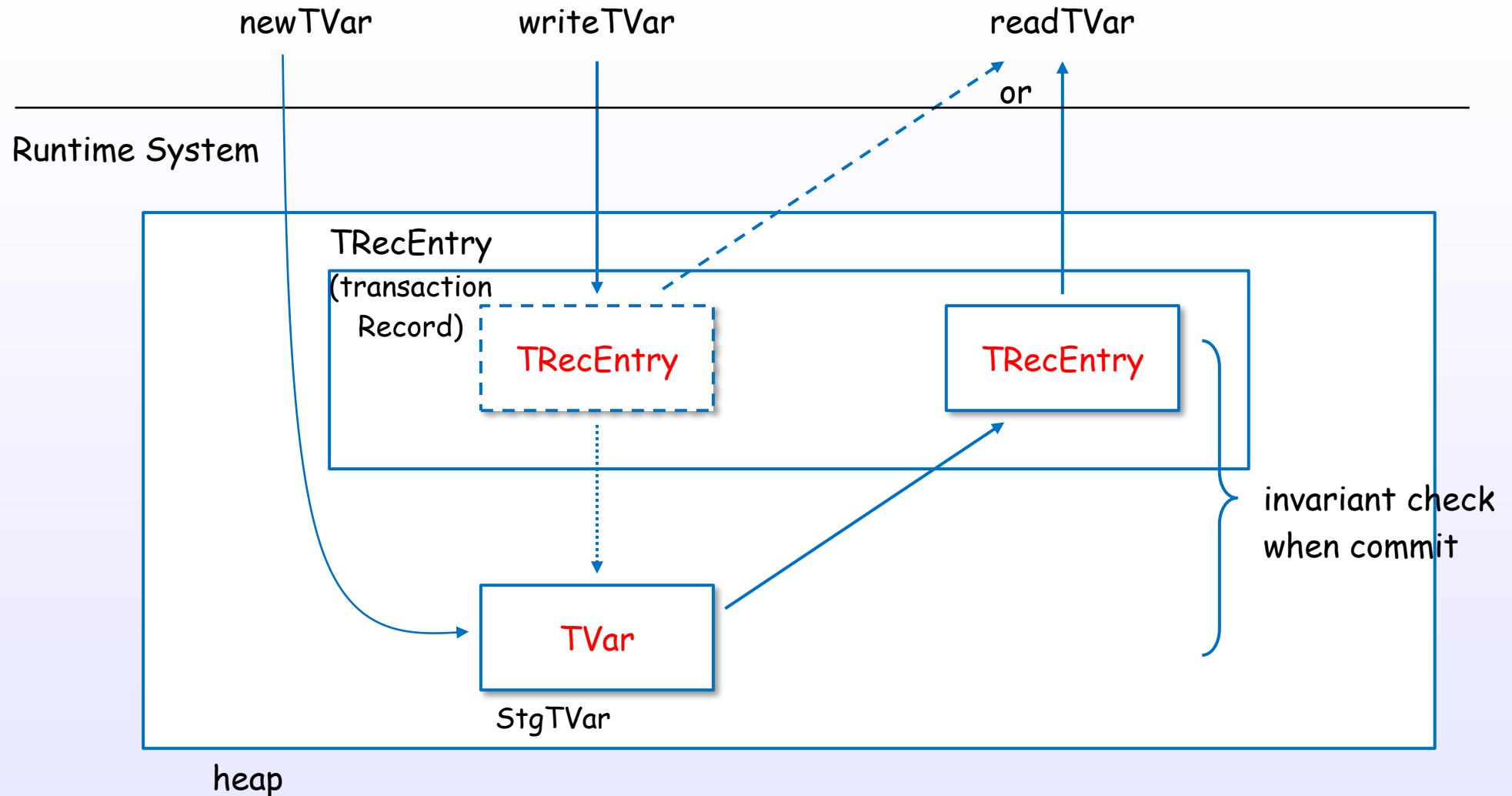
# retry example



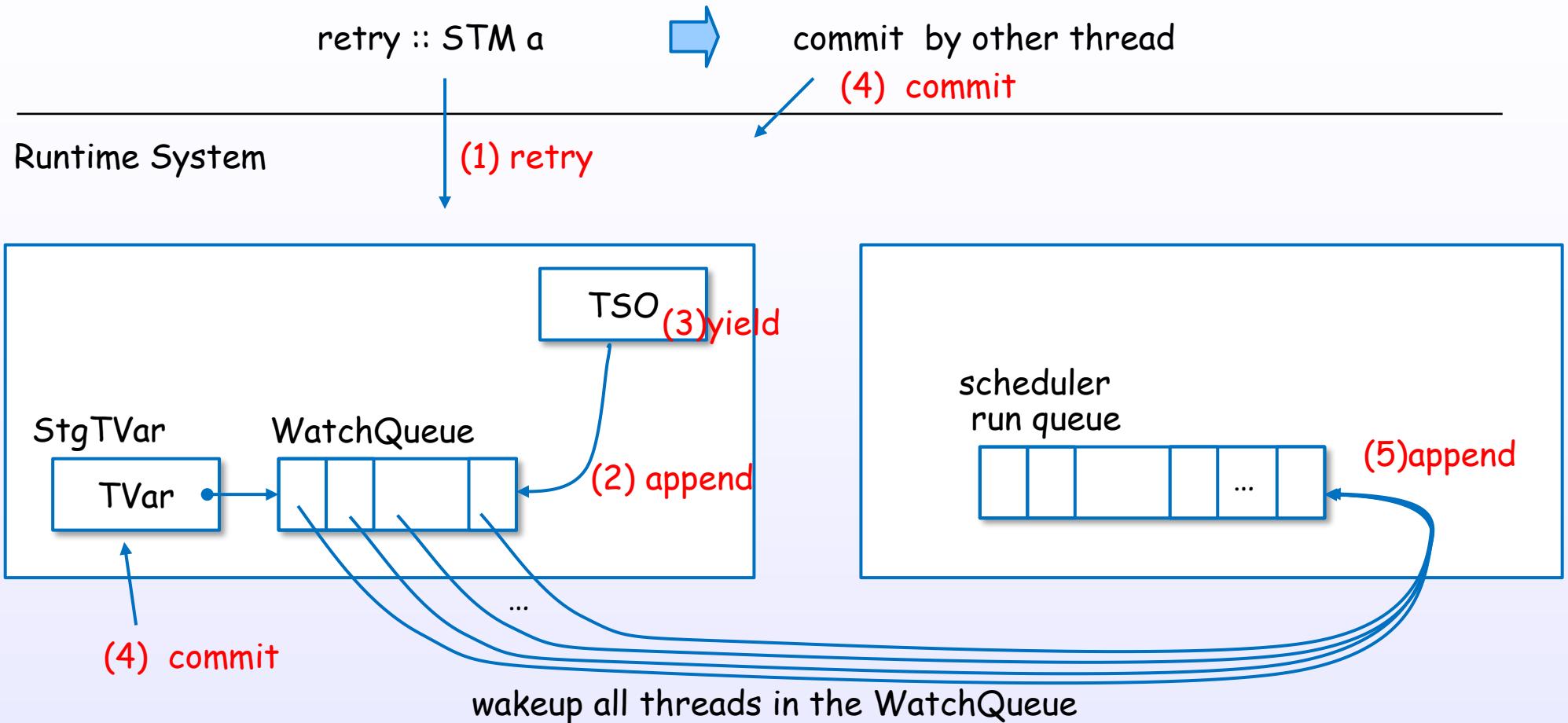
# STM, TVar data structure



# newTVar, writeTVar, readTVar



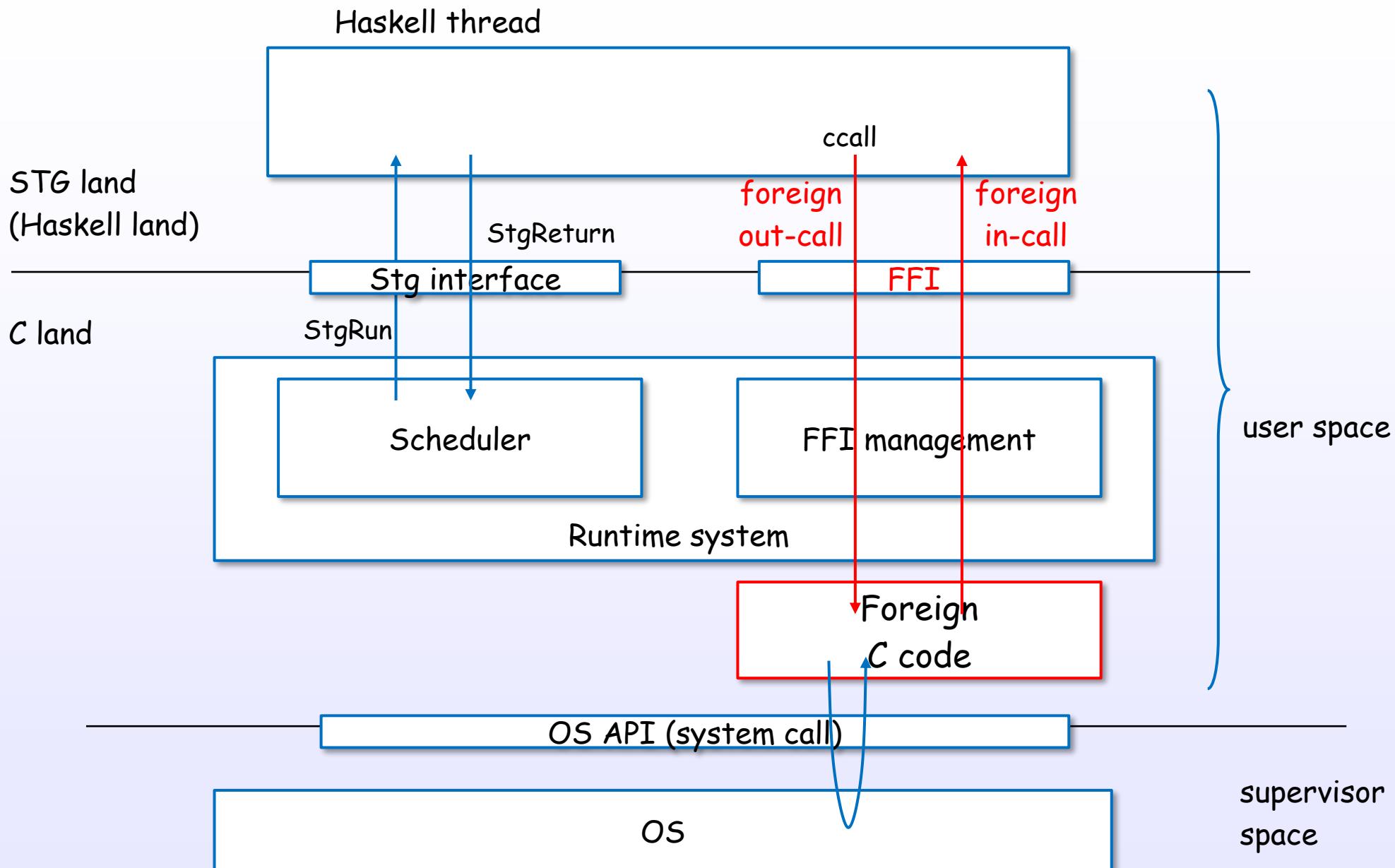
# block by retry, wake up by commit



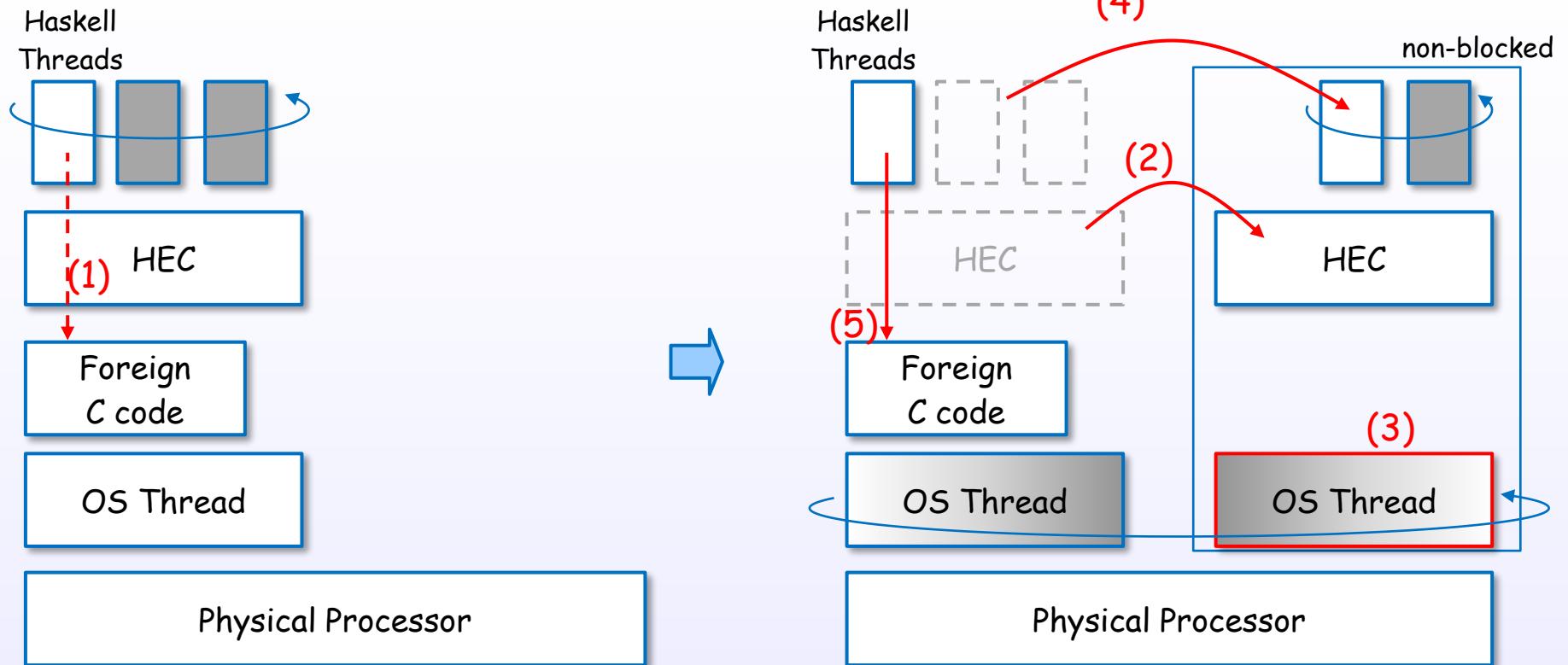
no guarantee of fairness,  
because the RTS has to run all the blocked transaction.

FFI

# FFI (Foreign Function Interface)



# FFI and OS Threads



(1) a safe foreign call (FFI)

(2) move the HEC to other OS thread

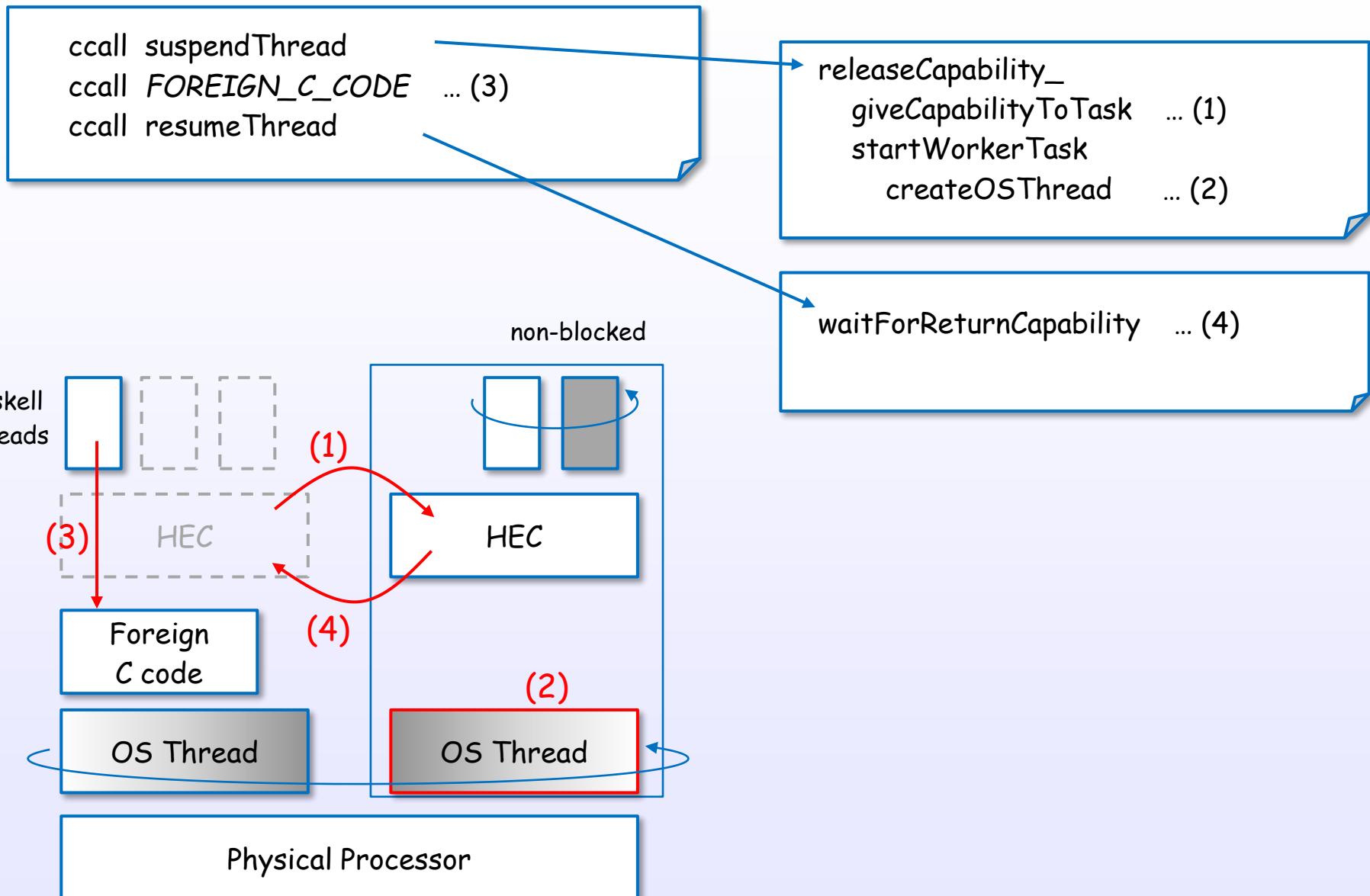
(3) spawn or draw an OS thread

(4) move Haskell threads

(5) call the foreign C code

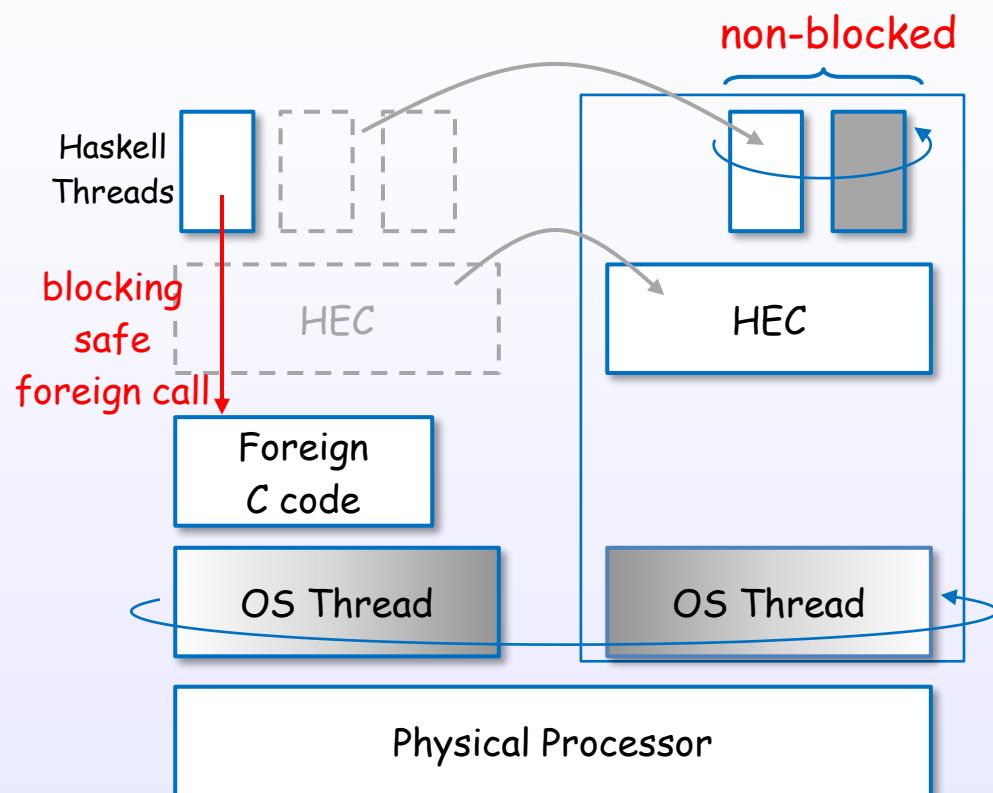
# A safe foreign call (code)

Haskell Threads

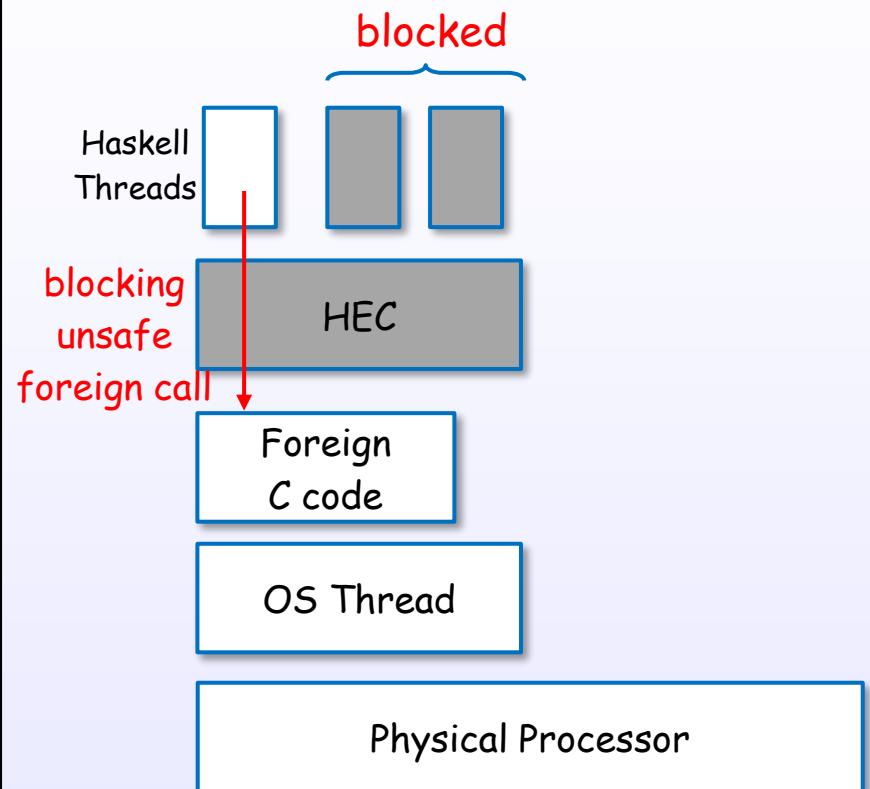


# a safe and an unsafe foreign call

a **safe** foreign call



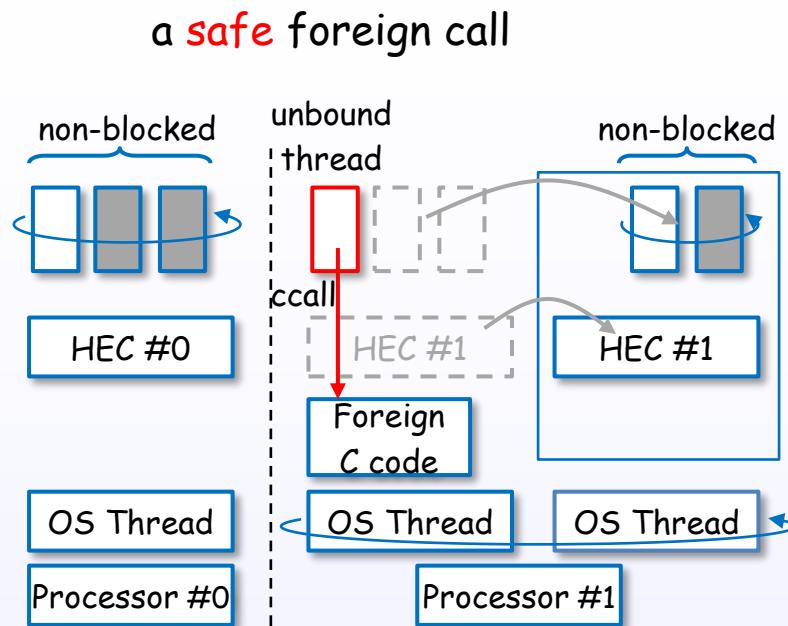
an **unsafe** foreign call



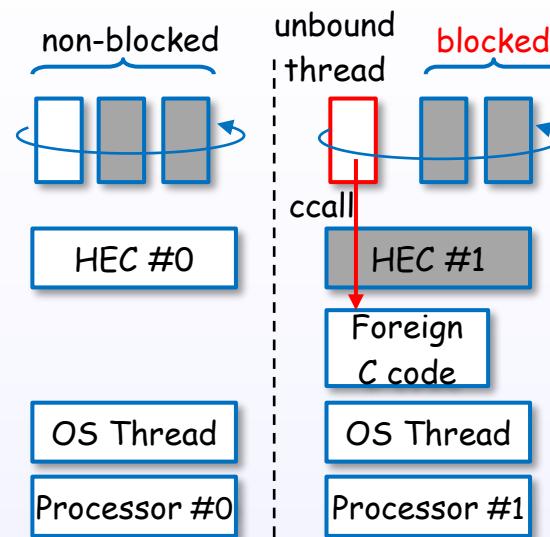
faster,  
but blocking to the other Haskell threads

# Safe/unsafe foreign call and bound/unbound thread

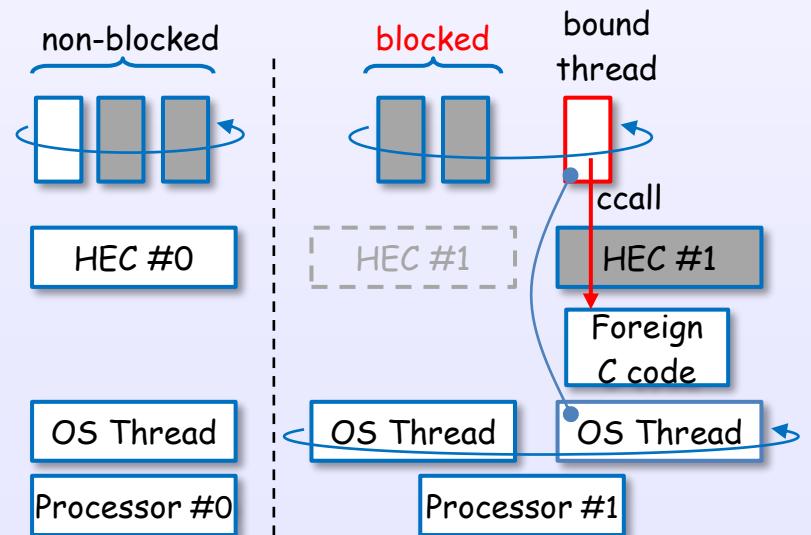
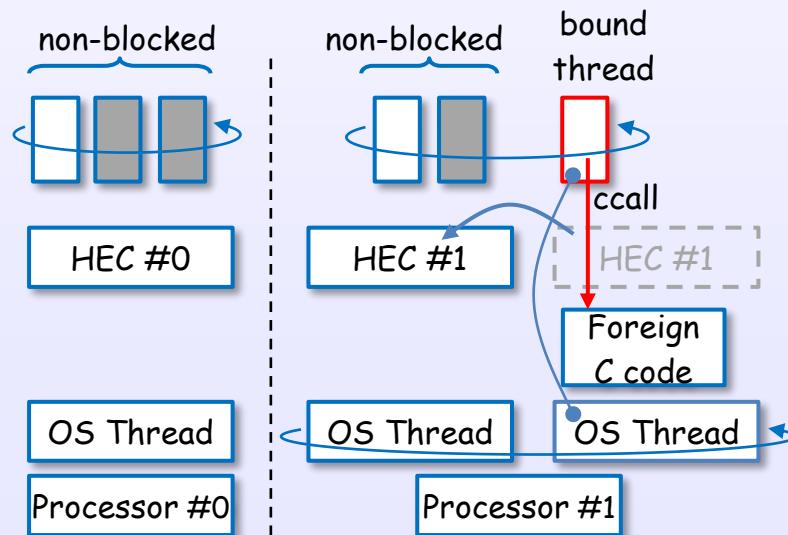
an **unbound**  
thread



an **unsafe** foreign call



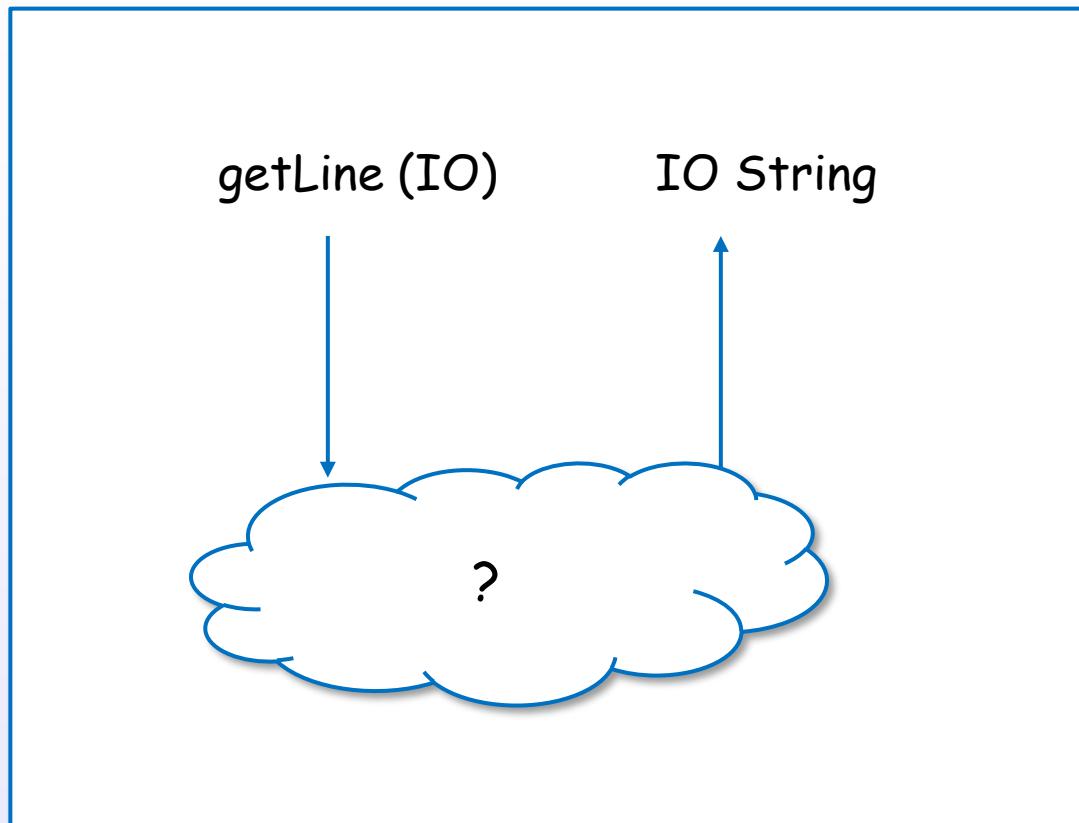
a **bound**  
thread



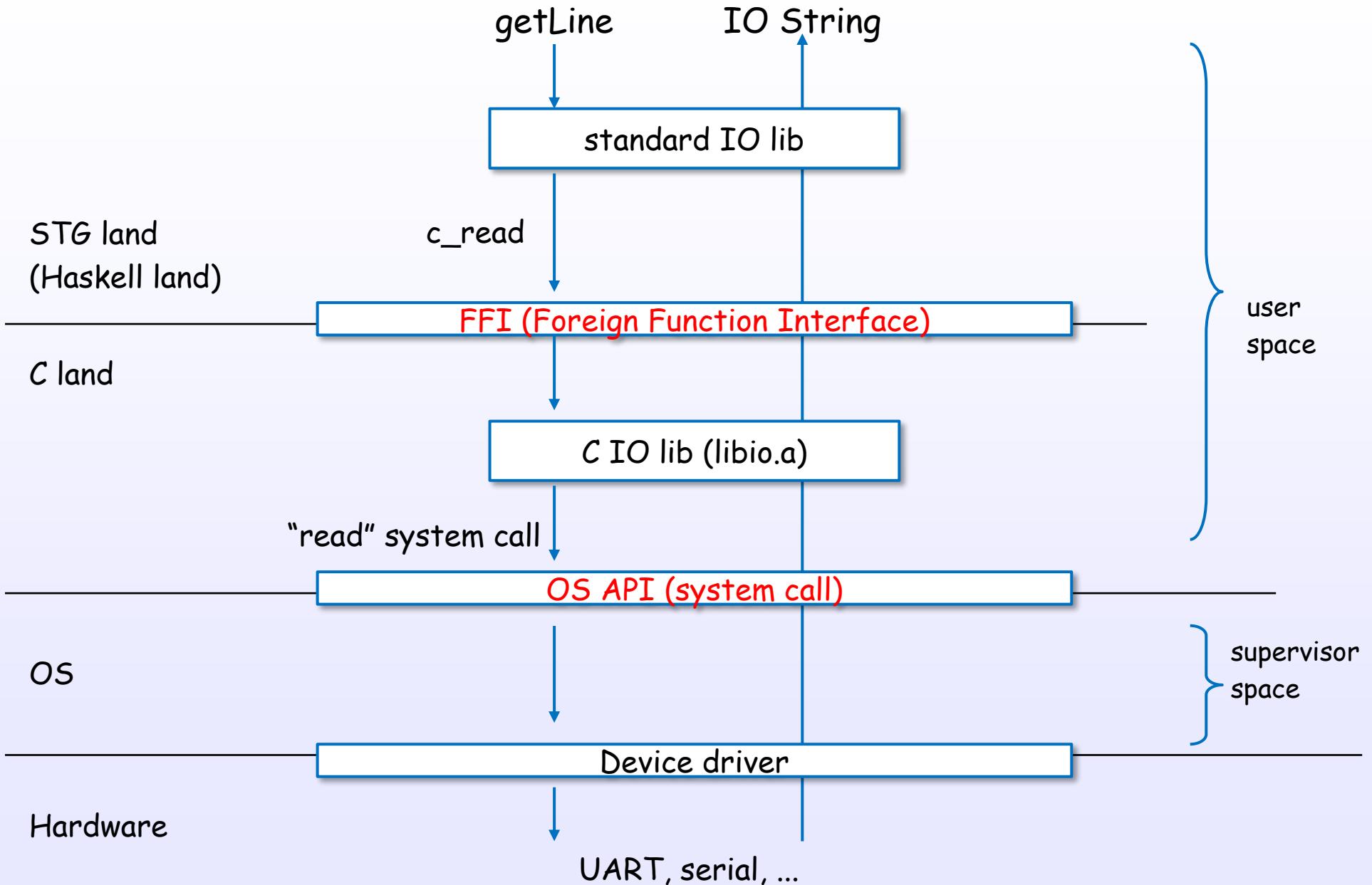
# IO and FFI

# IO

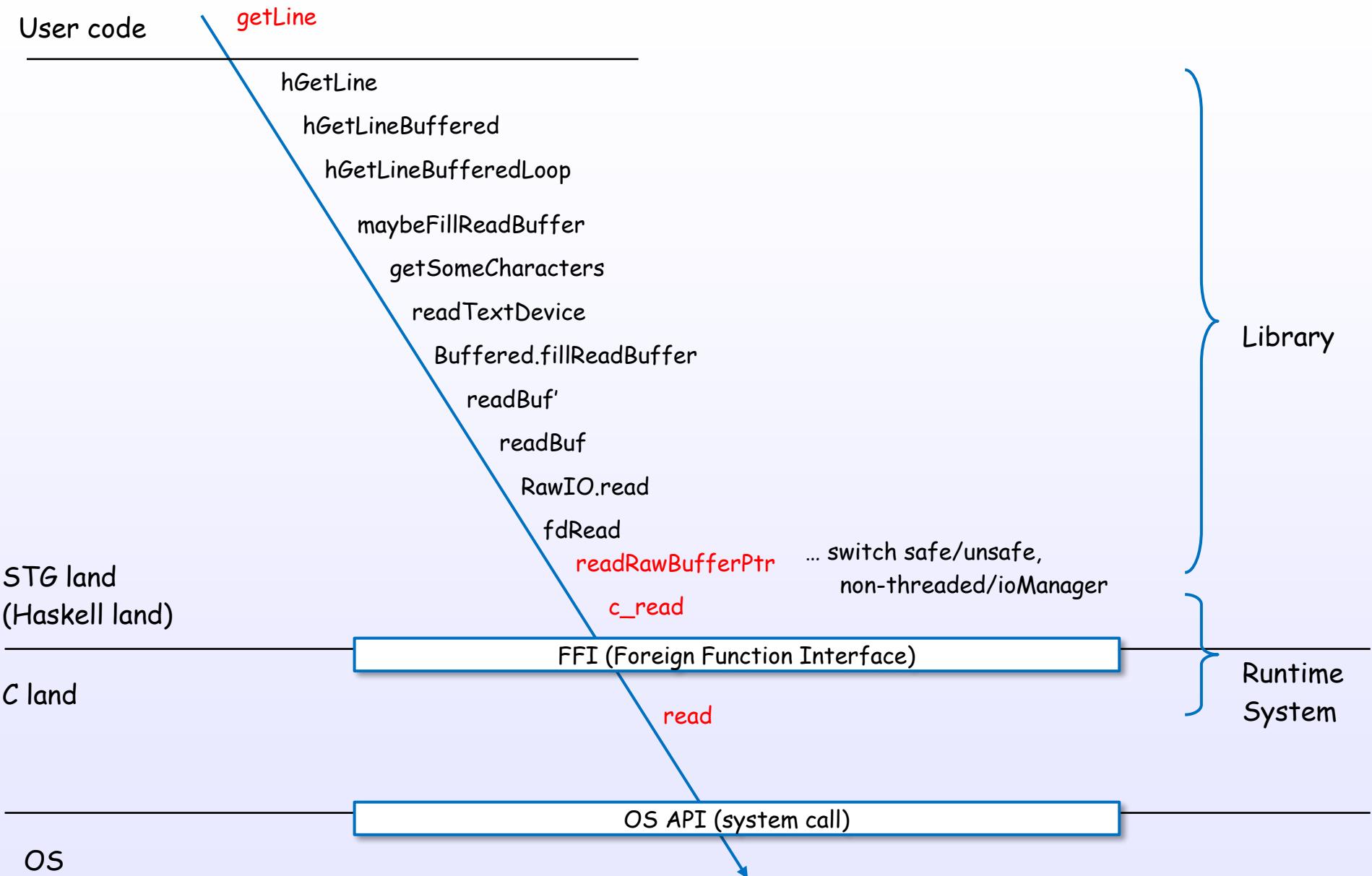
Haskell Thread



# IO example: getLine

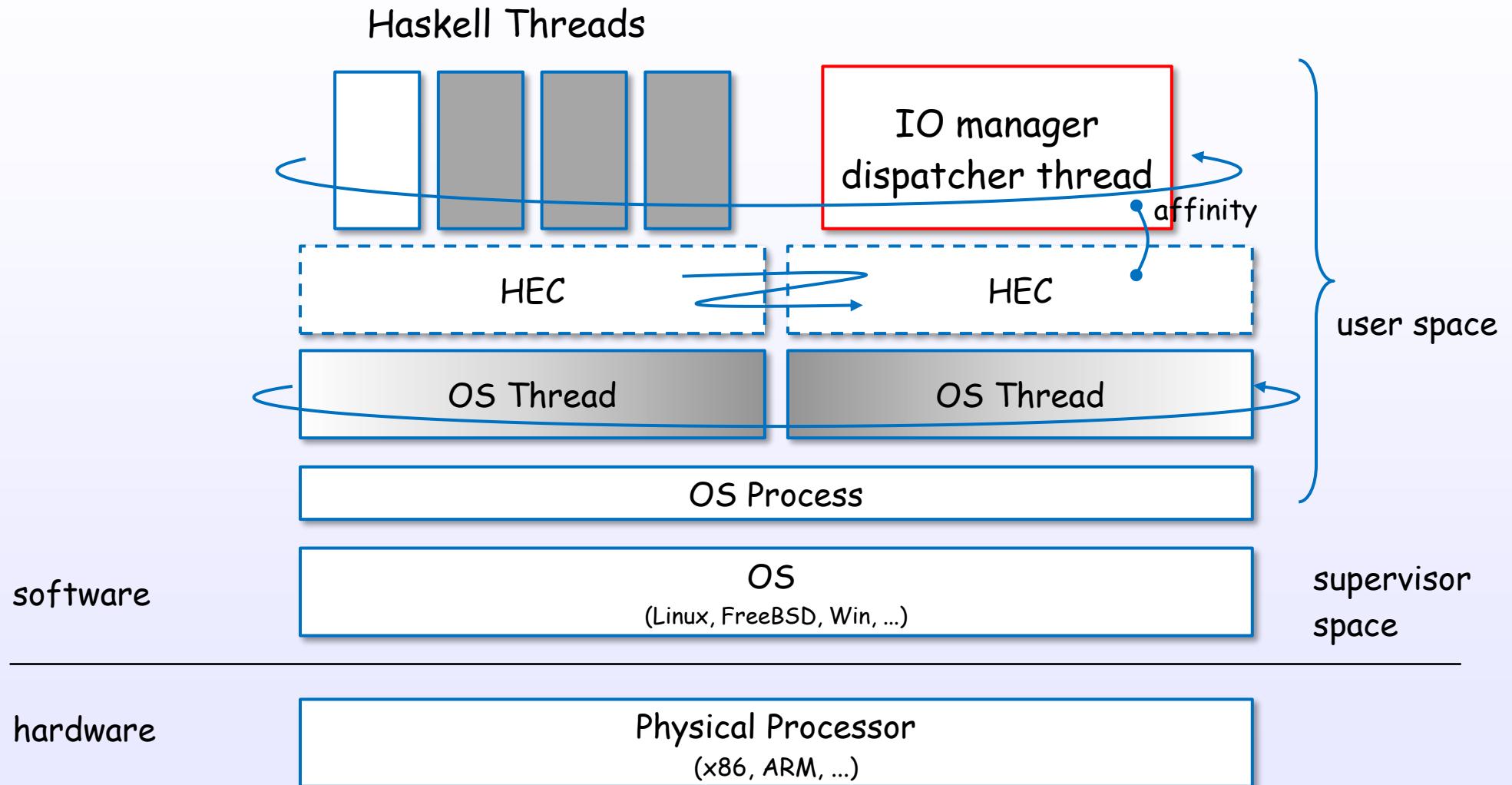


# IO example: getLine (code)



IO manager

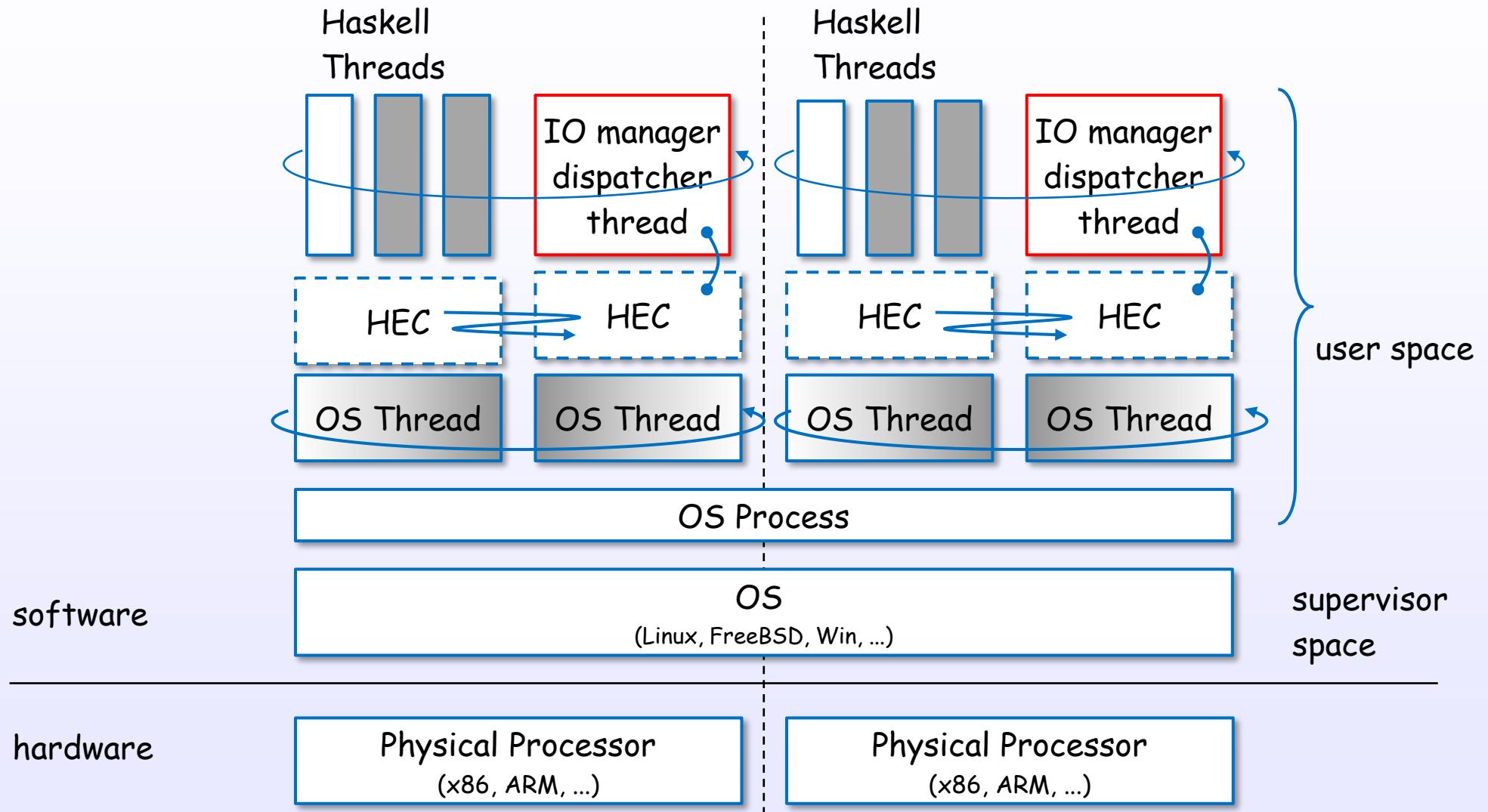
# IO manager (single core)



\*Threaded option case (ghc -threaded)

References : [7], [5], [8]

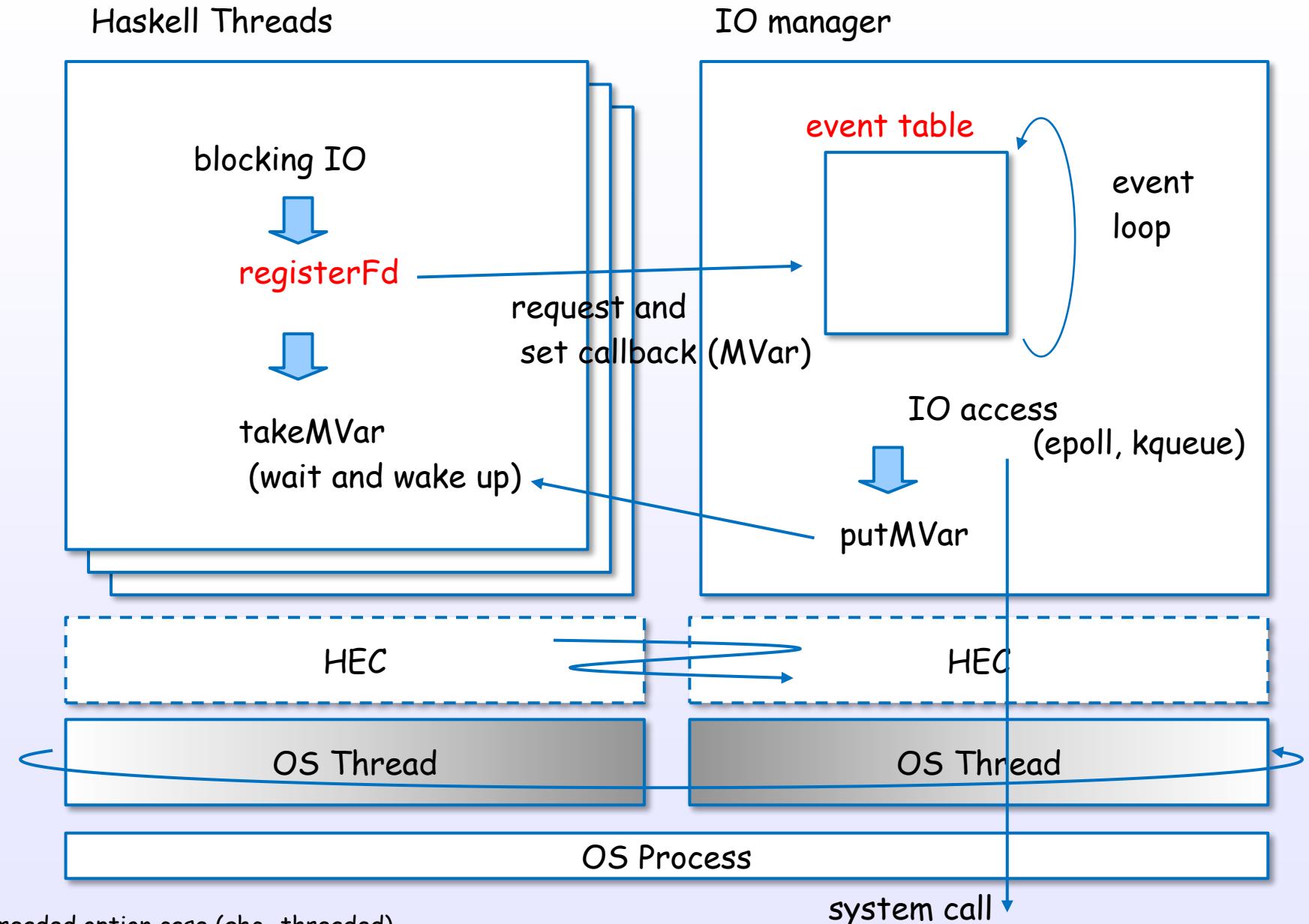
# IO manager (multi core)



\*Threaded option case (ghc -threaded)

References : [7], [5], [8]

# IO manager

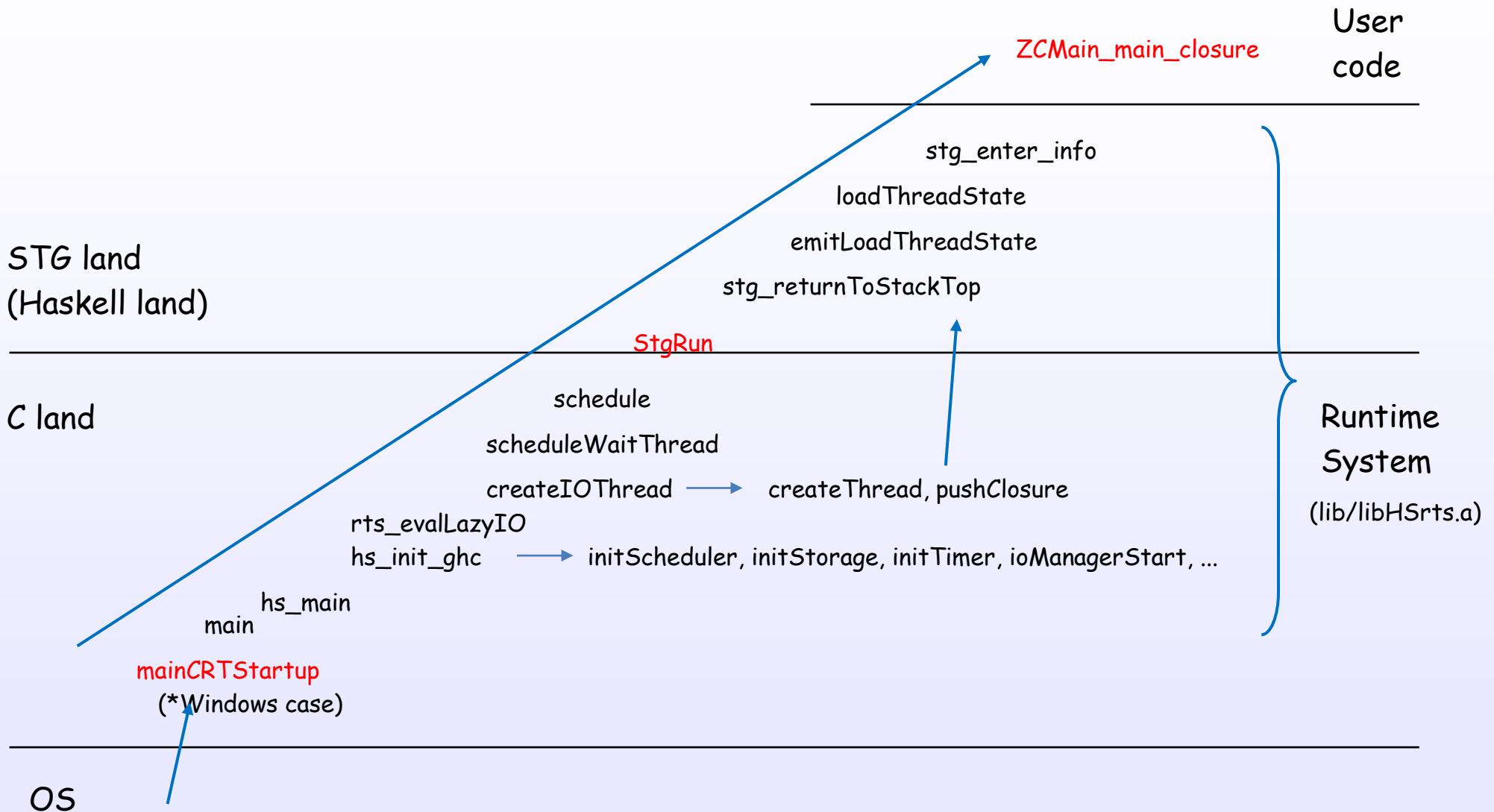


\*Threaded option case (ghc -threaded)

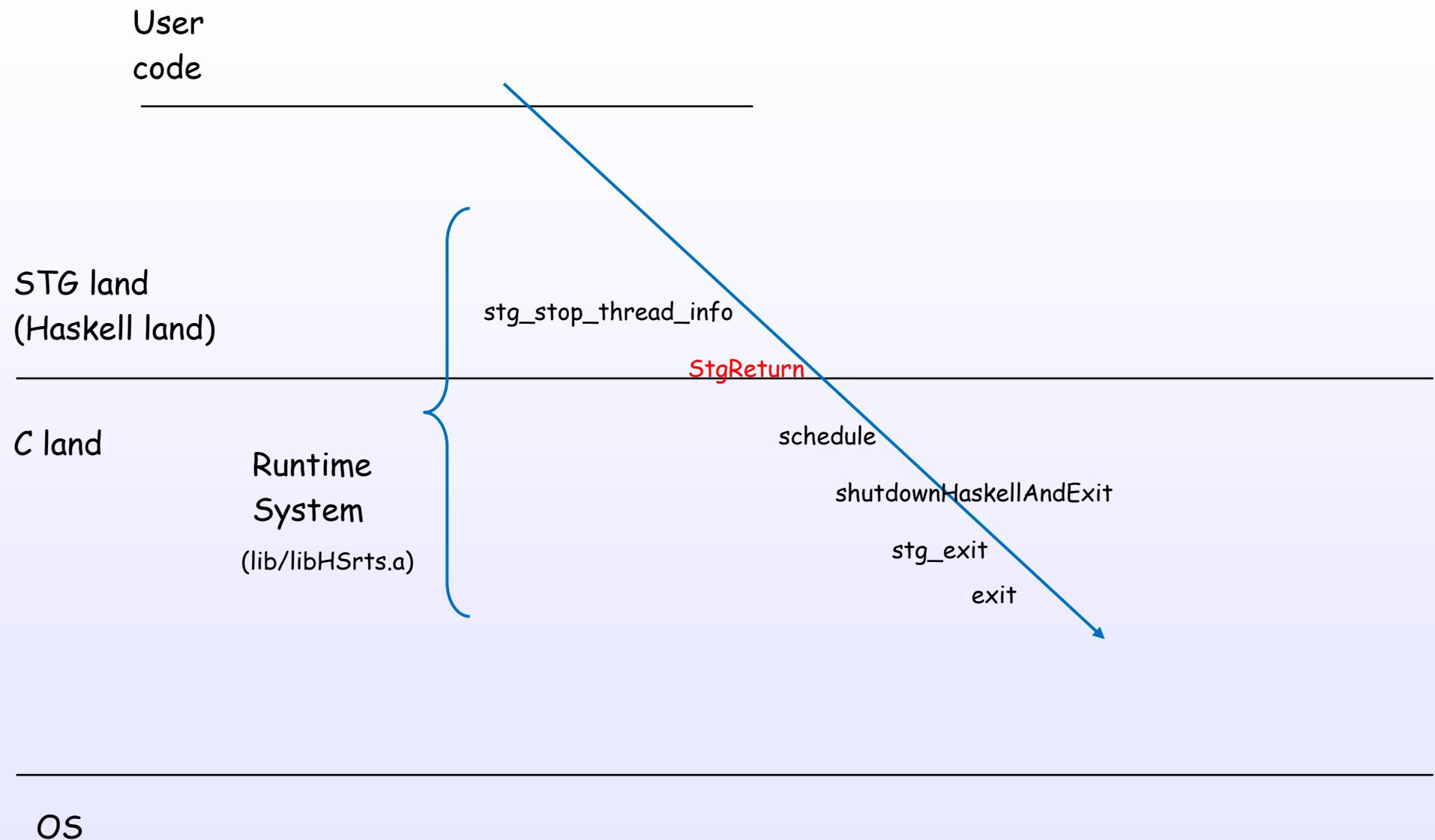
References : [7], [5], [8], [S29], [S30], [S33], [S38], [S36], [S3]

# Bootstrap

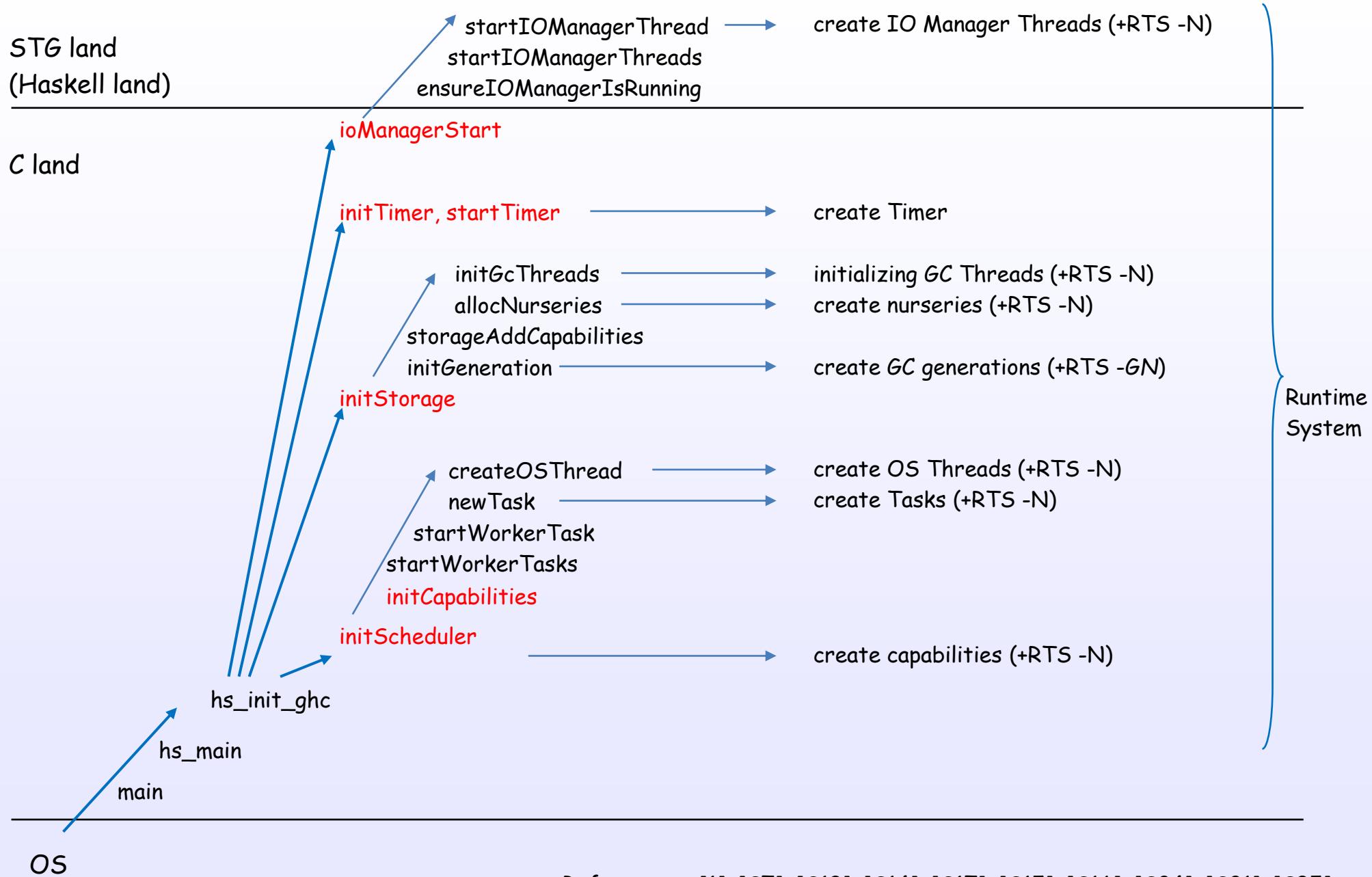
# Bootstrap sequence



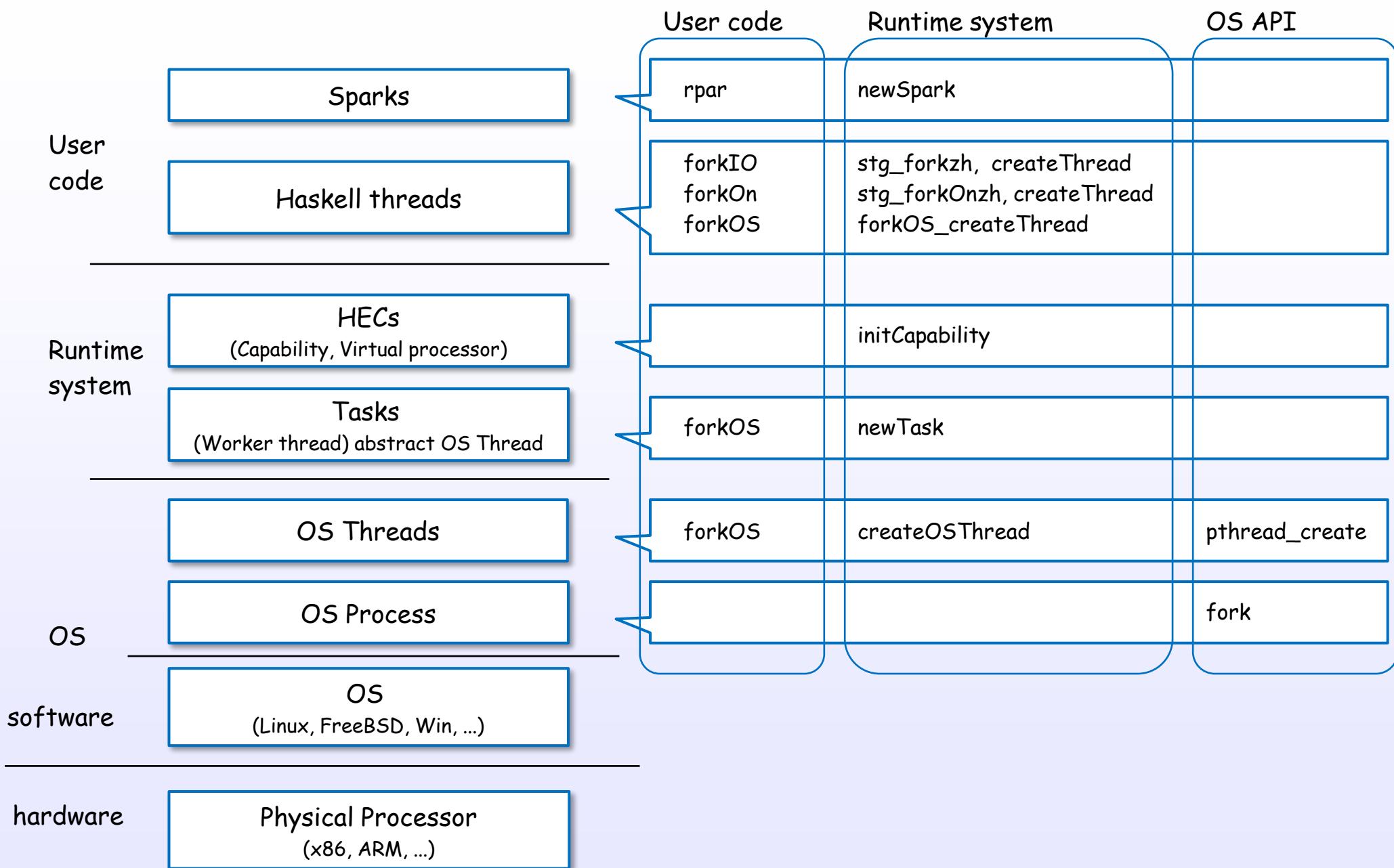
# Exit sequence



# Initializing



# Create each layers



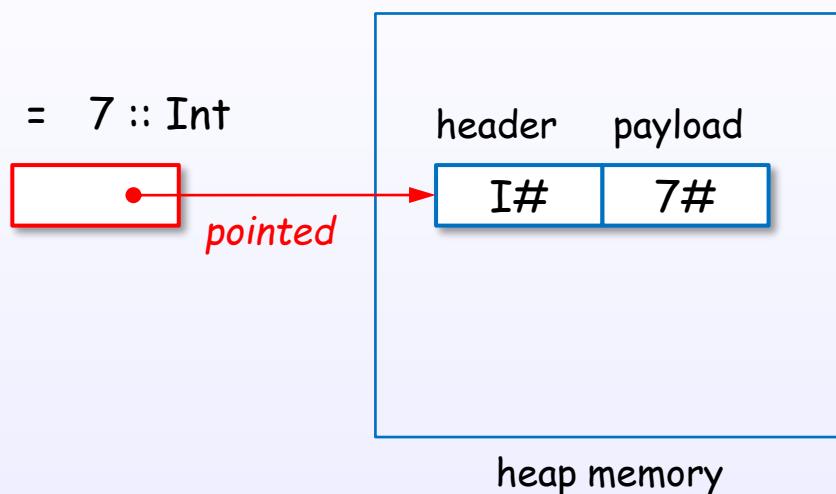
# Appendix

Boxity : boxed and unboxed

# Boxed and unboxed types

A **boxed** type

`x = 7 :: Int`



A boxed type is represented by a pointer to a heap-allocated object.

An **unboxed** type

`x = 7# :: Int#`



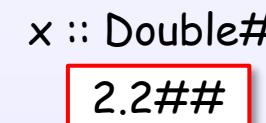
An unboxed type is represented by the value itself.

# Boxity examples

## Boxed types



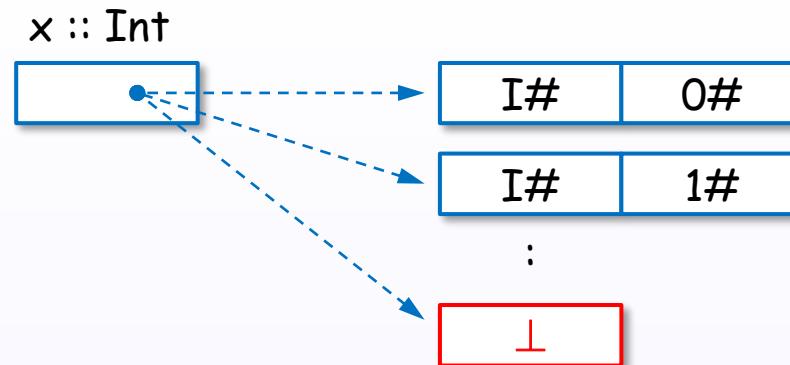
## Unboxed types



Levity : lifted and unlifted

# Lifted and unlifted types

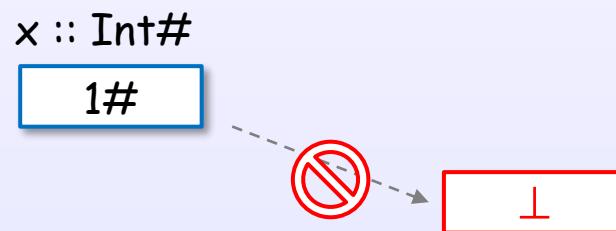
A **lifted** type



A lifted type has one extra element representing a non-terminating computation (bottom,  $\perp$ ).

---

An **unlifted** type



An unlifted type has no bottom.

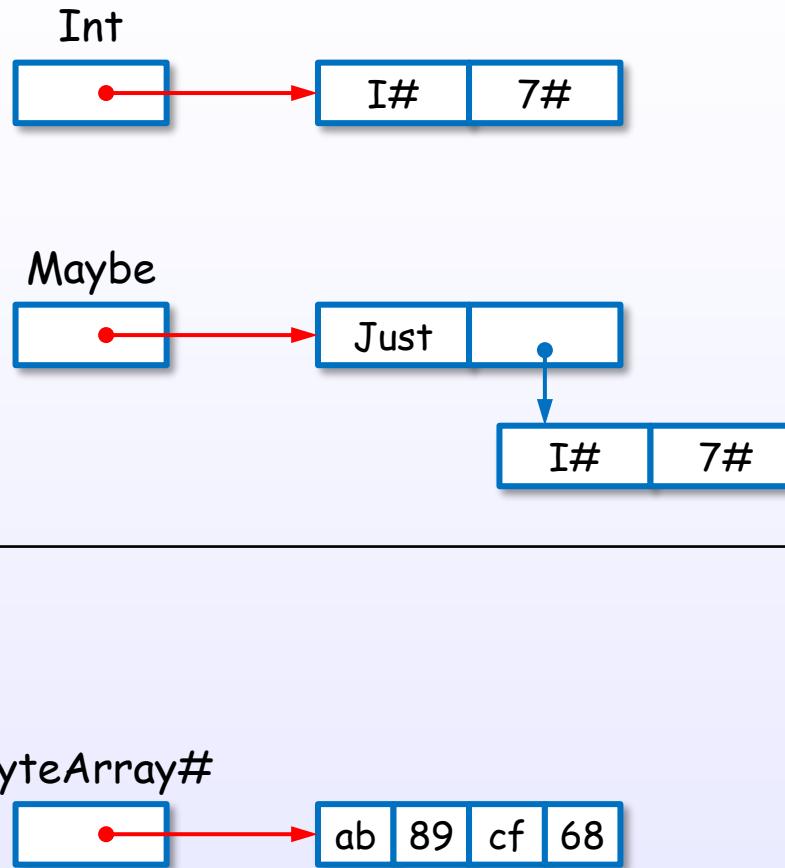
Boxity and levity

# Boxity and levity examples

Lifted types  
include bottom

Unlifted types

Boxed types represented by a pointer

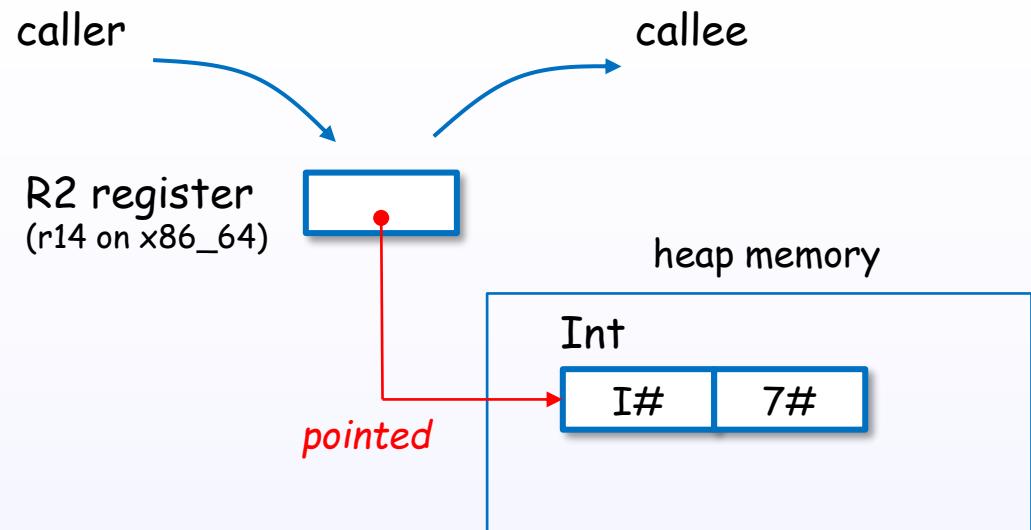


# Calling convention examples

## Lifted types

(non-strict,  
call-by-need)

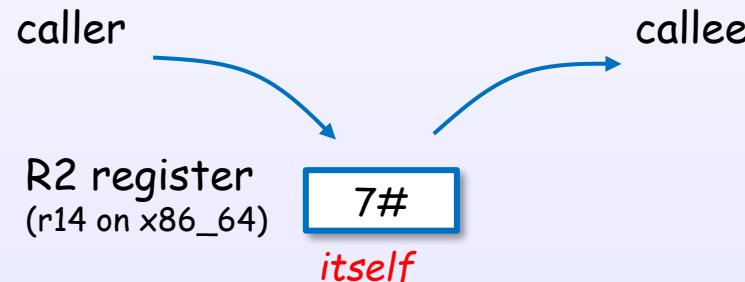
Int



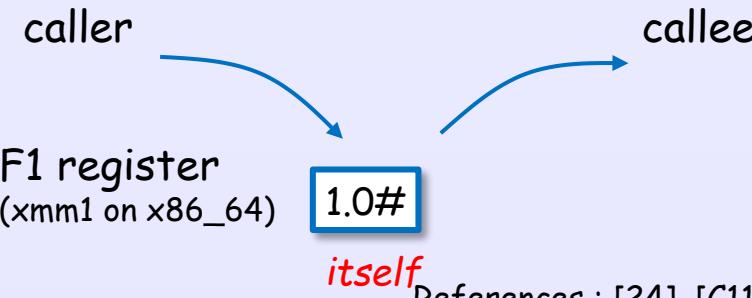
## Unlifted types

(strict,  
call-by-value)

Int#



Float#



References : [24], [C11], [C10], [C13], [S31], [23]

# References

# References

- [1] The Glorious Glasgow Haskell Compilation System User's Guide  
[https://downloads.haskell.org/~ghc/latest/docs/html/users\\_guide/index.html](https://downloads.haskell.org/~ghc/latest/docs/html/users_guide/index.html)
- [2] Implementing lazy functional languages on stock hardware: the Spineless Tagless G-machine Version 2.5  
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- [3] Making a Fast Curry Push/Enter vs Eval/Apply for Higher-order Languages  
<https://www.microsoft.com/en-us/research/wp-content/uploads/2016/07/eval-apply.pdf>
- [4] Faster Laziness Using Dynamic Pointer Tagging  
<https://research.microsoft.com/en-us/um/people/simonpj/papers/ptr-tag/ptr-tagging.pdf>
- [5] Runtime Support for Multicore Haskell  
<https://research.microsoft.com/en-us/um/people/simonpj/papers/parallel/multicore-ghc.pdf>
- [6] Extending the Haskell Foreign Function Interface with Concurrency  
<https://simonmar.github.io/bib/papers/conc-ffi.pdf>
- [7] Mio: A High-Performance Multicore IO Manager for GHC  
<https://dl.acm.org/doi/pdf/10.1145/2503778.2503790>
- [8] The GHC Runtime System  
<https://web.mit.edu/~ezyang/Public/jfp-ghc-rts.pdf>
- [9] The GHC Runtime System  
<https://www.scs.stanford.edu/14sp-cs240h/slides/ghc-rts.pdf>
- [10] Evaluation on the Haskell Heap  
<http://blog.ezyang.com/2011/04/evaluation-on-the-haskell-heap/>

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- [11] IO evaluates the Haskell Heap  
<http://blog.ezyang.com/2011/04/io-evaluates-the-haskell-heap/>
- [12] Understanding the Stack  
<https://www.well-typed.com/blog/94/>
- [13] Understanding the RealWorld  
<https://www.well-typed.com/blog/95/>
- [14] The GHC scheduler  
<http://blog.ezyang.com/2013/01/the-ghc-scheduler/>
- [15] GHC's Garbage Collector  
(Lost link: Garbage Collection & Memory Management Summer School, 2004)
- [16] Concurrent Haskell  
<https://www.microsoft.com/en-us/research/wp-content/uploads/1996/01/concurrent-haskell.pdf>
- [17] Beautiful Concurrency  
<https://www.schoolofhaskell.com/school/advanced-haskell/beautiful-concurrency>
- [18] Anatomy of an MVar operation  
<http://blog.ezyang.com/2013/05/anatomy-of-an-mvar-operation/>
- [19] Parallel and Concurrent Programming in Haskell  
<https://simonmar.github.io/pages/pcph.html>
- [20] Real World Haskell  
<http://book.realworldhaskell.org/>

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<https://www.scs.stanford.edu/16wi-cs240h/slides/ghc-compiler-slides.html>
- [22] Dive into GHC  
[https://www.stephendiehl.com/posts/ghc\\_01.html](https://www.stephendiehl.com/posts/ghc_01.html)
- [23] Unboxed Values as First-Class Citizens  
<https://www.microsoft.com/en-us/research/wp-content/uploads/1991/01/unboxed-values.pdf>
- [24] Levity Polymorphism (extended version)  
<https://cs.brynmawr.edu/~rae/papers/2017/levity/levity.pdf>
- [25] GHC Reading Guide  
[https://takenobu-hs.github.io/downloads/haskell\\_ghc\\_reading\\_guide.pdf](https://takenobu-hs.github.io/downloads/haskell_ghc_reading_guide.pdf)

# References

## The GHC Commentary

- [C1] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary>
- [C2] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/source-tree>
- [C3] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/compiler>
- [C4] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/compiler/hsc-main>
- [C5] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/compiler/core-syn-type>
- [C6] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/compiler/stg-syn-type>
- [C7] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/compiler/cmm-type>
- [C8] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/compiler/generated-code>
- [C9] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/compiler/symbol-names>
- [C10] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/compiler/type-type>
- [C11] <https://gitlab.haskell.org/ghc/ghc/-/wikis/levity-polymorphism>
- [C12] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/rts>
- [C13] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/rts/storage/heap-objects>
- [C14] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/rts/storage/stack>
- [C15] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/rts/storage/gc>
- [C16] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/rts/haskell-execution>
- [C17] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/rts/haskell-execution/registers>
- [C18] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/rts/haskell-execution/pointer-tagging>
- [C19] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/rts/scheduler>
- [C20] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/rts/stm>
- [C21] <https://gitlab.haskell.org/ghc/ghc/-/wikis/commentary/libraries>

# References

## Source code

- [S1] [includes/stg/Regs.h](#)
- [S2] [includes/stg/MachRegs.h](#)
- [S3] [includes/rts/storage/ClosureTypes.h](#)
- [S4] [includes/rts/storage/Closures.h](#)
- [S5] [includes/rts/storage/TSO.h](#)
- [S6] [includes/rts/storage/InfoTables.h](#)
- [S7] [compiler/GHC/Driver/Pipeline.hs](#)
- [S8] [compiler/GHC/Driver/Main.hs](#)
- [S9] [compiler/GHC/Parser.y](#)
- [S10] [compiler/GHC/StgToCmm/Foreign.hs](#)
- [S11] [compiler/GHC/StgToCmm/\\*.hs](#)
- [S12] [rts/PrimOps.cmm](#)
- [S13] [rts/RtsMain.c](#)
- [S14] [rts/RtsAPI.c](#)
- [S15] [rts/Capability.h](#)
- [S16] [rts/Capability.c](#)
- [S17] [rts/Schedule.c](#)
- [S18] [rts/StgCRun.c](#)
- [S19] [rts/StgStartup.cmm](#)
- [S20] [rts/StgMiscClosures.cmm](#)
- [S21] [rts/HeapStackCheck.cmm](#)
- [S22] [rts/Threads.c](#)
- [S23] [rts/Task.c](#)
- [S24] [rts/Timer.c](#)
- [S25] [rts/sm/GC.c](#)
- [S26] [rts/Sparks.c](#)
- [S27] [rts/WSDeque.c](#)
- [S28] [rts/STM.h](#)
- [S29] [rts posix/Signals.c](#)
- [S30] [rts/win32/ThrIOManager.c](#)
- [S31] [libraries/ghc-prim/GHC/Types.hs](#)
- [S32] [libraries/base/GHC/MVar.hs](#)
- [S33] [libraries/base/GHC/Conc/IO.hs](#)
- [S34] [libraries/base/GHC/Conc/Sync.hs](#)
- [S35] [libraries/base/GHC/Event/Manager.hs](#)
- [S36] [libraries/base/GHC/Event/Thread.hs](#)
- [S37] [libraries/base/GHC/IO/BufferedIO.hs](#)
- [S38] [libraries/base/GHC/IO/FD.hs](#)
- [S39] [libraries/base/GHC/IO/Handle/Text.hs](#)
- [S40] [libraries/base/System/IO.hs](#)
- [S41] [libraries/base/System/Posix/Internals.hs](#)
- [S42] [AutoApply.o \(utils/genapply/Main.hs\)](#)

*Connect the algorithm and transistor*